



SPEC® CINT2006 Result

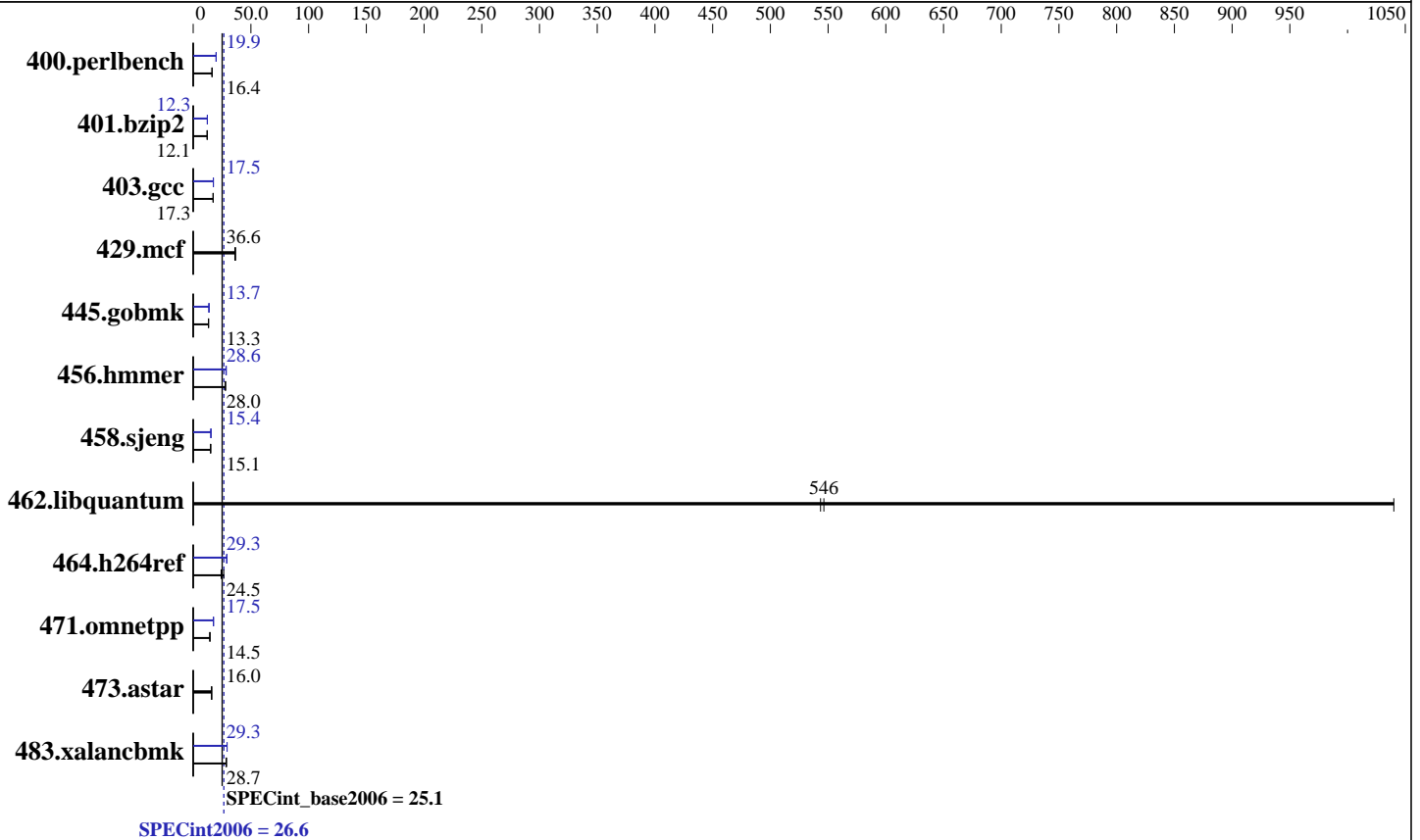
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Huawei Huawei CH221

SPECint®2006 = **26.6**
SPECint_base2006 = **25.1**

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2014
Hardware Availability: Jan-2012
Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2603
 CPU Characteristics:
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-10600R-09, ECC)
 Disk Subsystem: 1 X 300 GB SAS 7200RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>597</u>	<u>16.4</u>	598	16.3	597	16.4	491	19.9	<u>492</u>	<u>19.9</u>	492	19.9
401.bzip2	<u>795</u>	<u>12.1</u>	794	12.1	795	12.1	787	12.3	787	12.3	<u>787</u>	<u>12.3</u>
403.gcc	466	17.3	<u>466</u>	<u>17.3</u>	465	17.3	<u>461</u>	<u>17.5</u>	461	17.5	461	17.5
429.mcf	249	36.6	253	36.1	<u>249</u>	<u>36.6</u>	249	36.6	253	36.1	<u>249</u>	<u>36.6</u>
445.gobmk	<u>790</u>	<u>13.3</u>	796	13.2	784	13.4	<u>767</u>	<u>13.7</u>	767	13.7	767	13.7
456.hammer	335	27.8	<u>334</u>	<u>28.0</u>	334	28.0	329	28.4	326	28.6	<u>326</u>	<u>28.6</u>
458.sjeng	801	15.1	<u>801</u>	<u>15.1</u>	801	15.1	<u>788</u>	<u>15.4</u>	787	15.4	788	15.4
462.libquantum	38.1	543	19.9	1040	<u>37.9</u>	<u>546</u>	38.1	543	19.9	1040	<u>37.9</u>	<u>546</u>
464.h264ref	915	24.2	835	26.5	<u>904</u>	<u>24.5</u>	755	29.3	767	28.9	<u>756</u>	<u>29.3</u>
471.omnetpp	432	14.5	432	14.5	<u>432</u>	<u>14.5</u>	<u>356</u>	<u>17.5</u>	356	17.5	356	17.6
473.astar	<u>439</u>	<u>16.0</u>	438	16.0	443	15.9	<u>439</u>	<u>16.0</u>	438	16.0	443	15.9
483.xalancbmk	240	28.8	241	28.6	<u>241</u>	<u>28.7</u>	235	29.4	235	29.3	<u>235</u>	<u>29.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Sysinfo program /spec/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on huawei Wed Apr 9 19:13:07 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 0 @ 1.80GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
cache size : 10240 KB
```

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Platform Notes (Continued)

```

From /proc/meminfo
  MemTotal:      132103760 kB
  HugePages_Total:    0
  Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux huawei 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
  x86_64 x86_64 GNU/Linux

run-level 3 Apr 9 18:50

SPEC is set to: /spec
  Filesystem      Type  Size  Used Avail Use% Mounted on
  /dev/sda1       ext4  193G   56G  127G  31% /

Additional information from dmidecode:
  BIOS Insyde Corp. RMIBV372 12/21/2013
  Memory:
    8x NO DIMM NO DIMM
    11x Samsung M393B1K70CH0-CH9 8 GB 1066 MHz 2 rank
    5x Samsung M393B1K70DH0-CH9 8 GB 1066 MHz 2 rank

(End of data from sysinfo program)

```

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH221 and
the Huawei CH222 models are electronically equivalent.
The results have been measured on a Huawei CH221 model



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-ra-region-strategy=block -ansi-alias
 -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
 -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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 For other inquiries, please contact webmaster@spec.org.

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