SPEC® CINT2006 Result

Dell Inc.

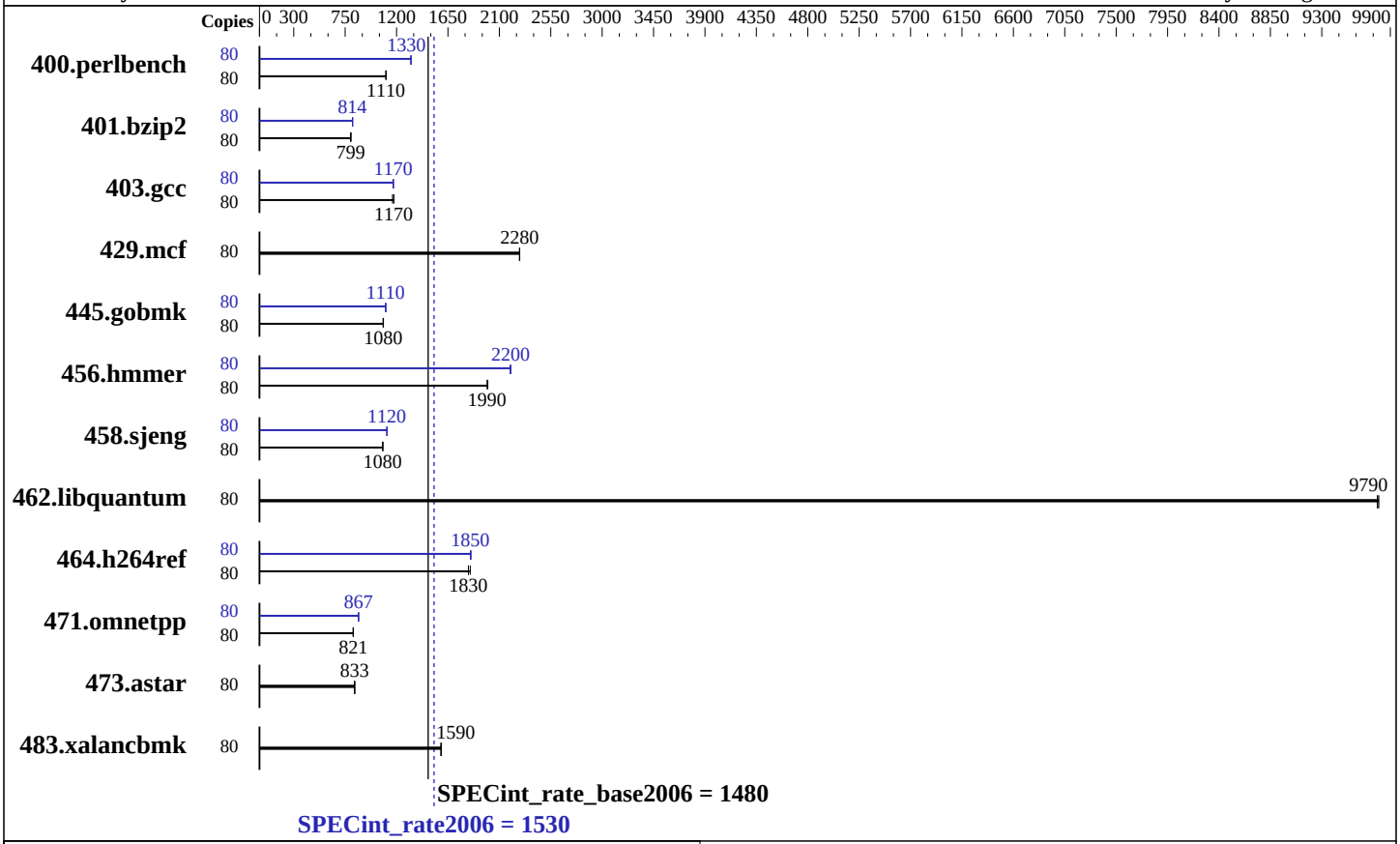
PowerEdge R820 (Intel Xeon E5-4650 v2, 2.40 GHz)

SPECint®_rate2006 = 1530

SPECint_rate_base2006 = 1480

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2013
Hardware Availability: Mar-2014
Software Availability: Aug-2013



Hardware

CPU Name:	Intel Xeon E5-4650 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 x 200 GB SAS SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	706	1110	**706**	**1110**	705	1110	80	589	1330	590	1330	**589**	**1330**
401.bzip2	80	**966**	**799**	964	801	967	798	80	948	814	946	816	**948**	**814**
403.gcc	80	553	1160	547	1180	**548**	**1170**	80	**549**	**1170**	549	1170	551	1170
429.mcf	80	**320**	**2280**	321	2280	320	2280	80	**320**	**2280**	321	2280	320	2280
445.gobmk	80	776	1080	773	1090	**774**	**1080**	80	760	1100	759	1110	**759**	**1110**
456.hmmer	80	373	2000	374	1990	**374**	**1990**	80	340	2200	339	2200	**340**	**2200**
458.sjeng	80	897	1080	896	1080	**897**	**1080**	80	867	1120	868	1110	**867**	**1120**
462.libquantum	80	169	9800	169	9790	**169**	**9790**	80	169	9800	169	9790	**169**	**9790**
464.h264ref	80	**968**	**1830**	959	1850	968	1830	80	958	1850	956	1850	**958**	**1850**
471.omnetpp	80	609	821	**609**	**821**	610	820	80	576	868	**577**	**867**	577	867
473.astar	80	675	832	673	834	**674**	**833**	80	675	832	673	834	**674**	**833**
483.xalancbmk	80	348	1590	**347**	**1590**	347	1590	80	348	1590	**347**	**1590**	347	1590

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Performance
Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux Mon Oct 21 15:13:15 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4650 v2 @ 2.40GHz
      4 "physical id"s (chips)
      80 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
```

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Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 10
         siblings  : 20
         physical 0: cores 0 1 2 3 4 8 9 10 11 12
         physical 1: cores 0 1 2 3 4 8 9 10 11 12
         physical 2: cores 0 1 2 3 4 8 9 10 11 12
         physical 3: cores 0 1 2 3 4 8 9 10 11 12
      cache size : 25600 KB

   From /proc/meminfo
      MemTotal:       529392212 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      SUSE Linux Enterprise Server 11 (x86_64)

   From /etc/*release* /etc/*version*
      SuSE-release:
         SUSE Linux Enterprise Server 11 (x86_64)
         VERSION = 11
         PATCHLEVEL = 3

   uname -a:
      Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Oct 21 15:12 last=S

   SPEC is set to: /root/cpu2006.1.2.ic13
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda2      ext2  175G   12G  163G   7% /

   Additional information from dmidecode:
     BIOS Dell Inc. 2.0.17 10/01/2013
     Memory:
      32x 00AD00B300AD HMT42GR7AFR4C-RD 16 GB 1866 MHz

   (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
  Filesystem page cache cleared with:
```

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General Notes (Continued)

```
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
`icc -m32`

C++ benchmarks:
`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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