



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

**SPECint<sup>®</sup>2006 = 56.6**

ProLiant DL380p Gen8  
(2.60 GHz, Intel Xeon E5-2650 v2)

**SPECint\_base2006 = 52.2**

CPU2006 license: 3

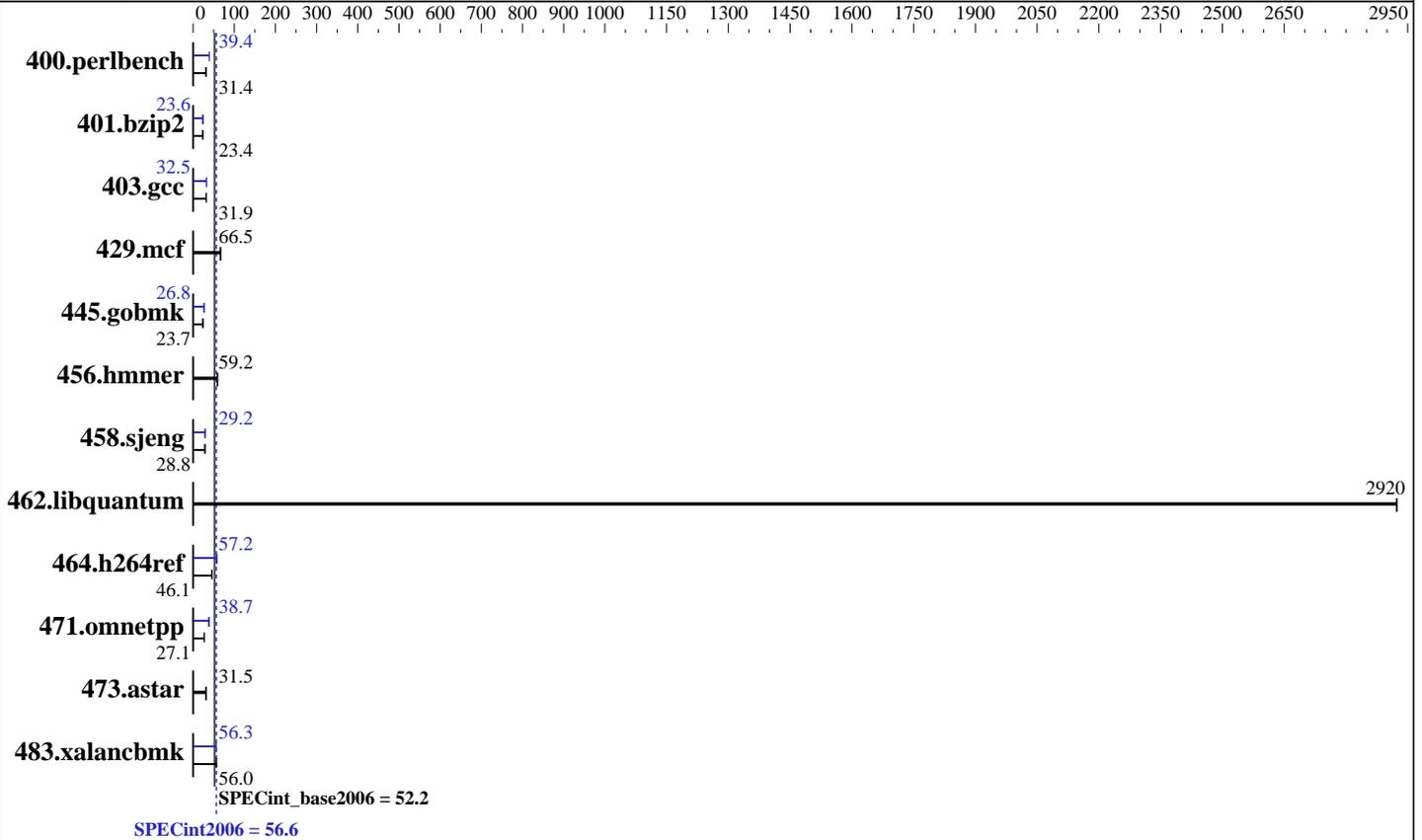
Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2650 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB 10 K SAS, RAID 0  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86\_64)  
 Kernel 3.0.76-0.11-default  
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	311	31.5	311	31.4	<b>311</b>	<b>31.4</b>	248	39.4	<b>248</b>	<b>39.4</b>	248	39.4
401.bzip2	413	23.4	413	23.4	<b>413</b>	<b>23.4</b>	408	23.6	<b>408</b>	<b>23.6</b>	408	23.6
403.gcc	252	32.0	<b>252</b>	<b>31.9</b>	253	31.8	247	32.5	<b>248</b>	<b>32.5</b>	248	32.4
429.mcf	137	66.4	<b>137</b>	<b>66.5</b>	137	66.5	137	66.4	<b>137</b>	<b>66.5</b>	137	66.5
445.gobmk	<b>443</b>	<b>23.7</b>	443	23.7	443	23.7	392	26.8	<b>392</b>	<b>26.8</b>	392	26.8
456.hammer	160	58.4	157	59.3	<b>158</b>	<b>59.2</b>	160	58.4	157	59.3	<b>158</b>	<b>59.2</b>
458.sjeng	421	28.8	421	28.8	<b>421</b>	<b>28.8</b>	414	29.3	414	29.2	<b>414</b>	<b>29.2</b>
462.libquantum	7.09	2920	7.09	2920	<b>7.09</b>	<b>2920</b>	7.09	2920	7.09	2920	<b>7.09</b>	<b>2920</b>
464.h264ref	<b>480</b>	<b>46.1</b>	481	46.0	480	46.1	387	57.2	387	57.2	<b>387</b>	<b>57.2</b>
471.omnetpp	230	27.2	231	27.0	<b>230</b>	<b>27.1</b>	<b>161</b>	<b>38.7</b>	161	38.7	163	38.4
473.astar	223	31.5	<b>223</b>	<b>31.5</b>	223	31.5	223	31.5	<b>223</b>	<b>31.5</b>	223	31.5
483.xalancbmk	124	55.8	<b>123</b>	<b>56.0</b>	121	57.1	123	56.3	123	56.2	<b>123</b>	<b>56.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Reclaim mode enabled with:
  echo 1 > /proc/sys/vm/zone_reclaim_mode
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
  echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
  numactl --localalloc runspec <etc>
```

### Platform Notes

```
BIOS Configuration:
Intel Hyperthreading Options set to Disabled
HP Power Profile set to Maximum Performance
Minimum Processor Idle Power Core State set to C1E
Minimum Processor Idle Power Package State set to C6 (retention)
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on dl380p-gen8-0sb Fri Oct 25 03:47:41 2013
```

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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 8
  siblings       : 8
  physical 0:    cores 0 1 2 3 4 5 6 7
  physical 1:    cores 0 1 2 3 4 5 6 7
  cache size     : 20480 KB
```

```
From /proc/meminfo
MemTotal:      132130192 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux dl380p-gen8-0sb 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 24 22:24 last=S
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext3  274G   12G  261G   5% /
```

```
Additional information from dmidecode:
BIOS HP P70 09/08/2013
Memory:
16x HP 712382-071 8 GB 1866 MHz
8x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)  
Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:  
16x HP 712382-071 8 GB 1866 MHz



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## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

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## Base Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc  
-opt-malloc-options=3 -auto-ilp32

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmcr: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml>



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