



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant DL560 Gen8
(2.20 GHz, Intel Xeon E5-4640 v2)

SPECint®2006 = 46.8

SPECint_base2006 = 43.7

CPU2006 license: 3

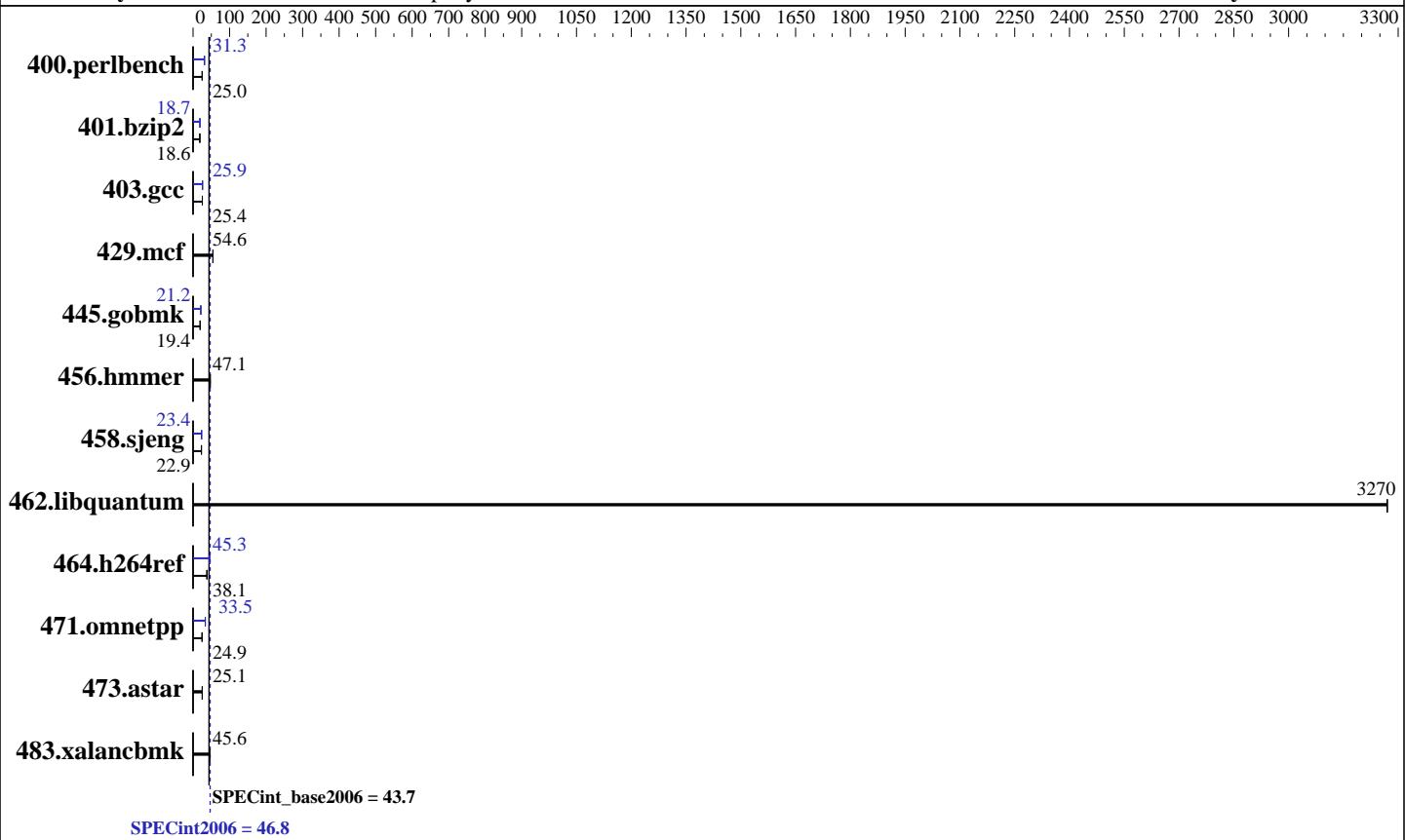
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Mar-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-4640 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: Kernel 2.6.32-431.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	391	25.0	391	25.0	392	24.9	312	31.3	313	31.3	312	31.3
401.bzip2	519	18.6	519	18.6	520	18.6	515	18.7	515	18.7	514	18.8
403.gcc	316	25.5	317	25.4	317	25.4	310	25.9	310	25.9	310	25.9
429.mcf	168	54.3	167	54.6	167	54.6	168	54.3	167	54.6	167	54.6
445.gobmk	540	19.4	540	19.4	540	19.4	495	21.2	495	21.2	495	21.2
456.hmmer	199	46.9	198	47.1	198	47.1	199	46.9	198	47.1	198	47.1
458sjeng	528	22.9	528	22.9	528	22.9	518	23.4	518	23.4	519	23.3
462.libquantum	6.34	3270	6.34	3270	6.34	3270	6.34	3270	6.34	3270	6.34	3270
464.h264ref	583	37.9	582	38.1	581	38.1	488	45.4	489	45.3	488	45.3
471.omnetpp	251	24.9	255	24.5	251	24.9	185	33.8	187	33.4	187	33.5
473.astar	280	25.1	280	25.1	278	25.3	280	25.1	280	25.1	278	25.3
483.xalancbmk	151	45.7	152	45.5	151	45.6	151	45.7	152	45.5	151	45.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

Reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

runspec command invoked through numactl i.e.:

```
numactl --localalloc runspec <etc>
```

Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled

HP Power Profile set to Maximum Performance

Minimum Processor Idle Power Core State set to C1E state

Minimum Processor Idle Power Package State set to Package C6 (retention) State

Memory Power Savings Mode set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Dynamic Power Capping Functionality set to Disabled

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on DL560p-Gen8-1K4 Tue Mar 4 05:49:50 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4640 v2 @ 2.20GHz
        4 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    physical 2: cores 0 1 2 3 4 8 9 10 11 12
    physical 3: cores 0 1 2 3 4 8 9 10 11 12
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      529267592 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DL560p-Gen8-1K4 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 3 15:59
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext4  365G   12G  334G   4%  /
```

Additional information from dmidecode:

```
BIOS HP P77 11/14/2013
Memory:
 32x HP 712383-081 16 GB 1866 MHz 2 rank
 16x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of
memory is 512 GB and the dmidecode description should have one line reading as:

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Platform Notes (Continued)

32x HP 712383-081 16 GB 1866 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/lib64:/cpu2006/sh"

OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
 -Wl,-z,muldefs -L/sh -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

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Peak Optimization Flags (Continued)

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: basepeak = yes

458sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml>



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