SPEC® CINT2006 Result

Dell Inc.

PowerEdge R920 (Intel Xeon E7-2870 v2, 2.30 GHz)

$SPECint^®\_rate2006 =$ 1010

$SPECint\_rate\_base2006 =$ 985

CPU2006 license: 55		**Test date:**	Jan-2014
Test sponsor:	Dell Inc.	**Hardware Availability:**	Mar-2014
Tested by:	Dell Inc.	**Software Availability:**	Nov-2013
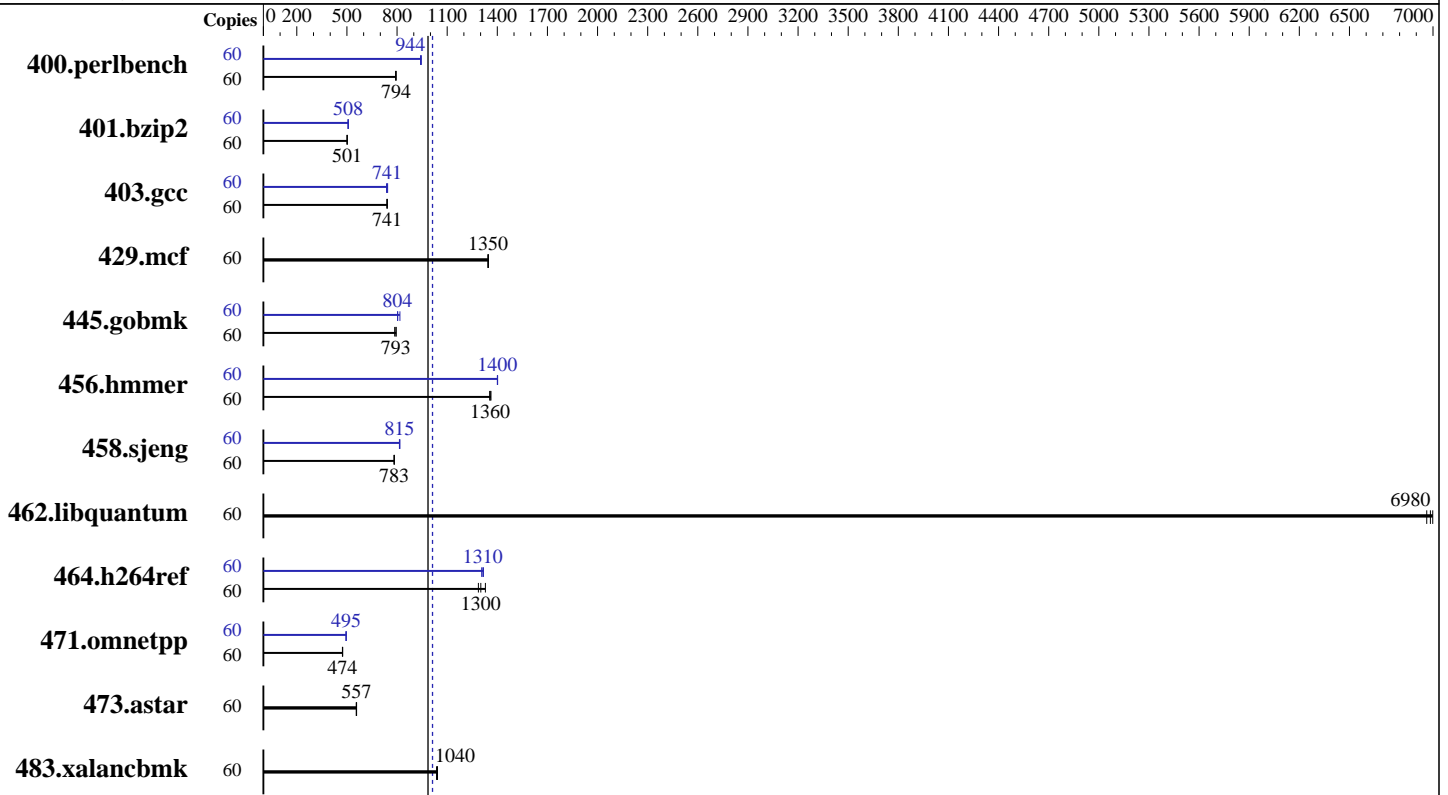
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Hardware

CPU Name:	Intel Xeon E7-2870 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	30 cores, 2 chips, 15 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem:	1 x 300 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	60	741	791	**738**	**794**	738	794	60	623	940	621	945	**621**	**944**
401.bzip2	60	1156	501	1153	502	**1155**	**501**	60	**1140**	**508**	1140	508	1143	507
403.gcc	60	653	739	**652**	**741**	651	741	60	655	737	650	744	**652**	**741**
429.mcf	60	**407**	**1350**	406	1350	407	1340	60	**407**	**1350**	406	1350	407	1340
445.gobmk	60	792	795	**793**	**793**	801	786	60	770	817	**783**	**804**	784	803
456.hmmer	60	**412**	**1360**	411	1360	413	1350	60	400	1400	399	1400	**399**	**1400**
458.sjeng	60	**927**	**783**	927	783	928	783	60	**890**	**815**	892	814	889	817
462.libquantum	60	179	6960	**178**	**6980**	178	7000	60	179	6960	**178**	**6980**	178	7000
464.h264ref	60	1032	1290	999	1330	**1020**	**1300**	60	1008	1320	1016	1310	**1011**	**1310**
471.omnetpp	60	791	474	793	473	**791**	**474**	60	757	495	**757**	**495**	757	495
473.astar	60	**756**	**557**	755	558	758	556	60	**756**	**557**	755	558	758	556
483.xalancbmk	60	**398**	**1040**	399	1040	397	1040	60	**398**	**1040**	399	1040	397	1040

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    BIOS settings:
    Virtualization Technology disabled
    Execute Disable disabled
    System Profile set to Performance
     Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
     $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
     running on localhost.localdomain Fri Jan 24 13:45:28 2014

     This section contains SUT (System Under Test) info as seen by
     some common utilities. To remove or add to this section, see:
       http://www.spec.org/cpu2006/Docs/config.html#sysinfo

     From /proc/cpuinfo
        model name : Intel(R) Xeon(R) CPU E7-2870 v2 @ 2.30GHz
           2 "physical id"s (chips)
           60 "processors"
        cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

```
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 15
       siblings  : 30
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
    cache size : 30720 KB

  From /proc/meminfo
    MemTotal:       529354152 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

  /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)

  From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
    Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
    EST 2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jan 24 13:44

  SPEC is set to: /root/cpu2006-1.2
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2       ext4  271G  8.6G  249G   4% /

  Additional information from dmidecode:
    BIOS Dell Inc. 1.0.1 01/13/2014
    Memory:
     32x 00AD04B300AD HMT42GR7AFR4A-PB 16 GB 1333 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/sh -lsmartheap

     473.astar: basepeak = yes
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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