



# SPEC<sup>®</sup> CINT2006 Result

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## NEC Corporation

SPECint<sup>®</sup>\_rate2006 = 509

Express5800/R120e-2E (Intel Xeon E5-2440 v2)

SPECint\_rate\_base2006 = 490

CPU2006 license: 9006

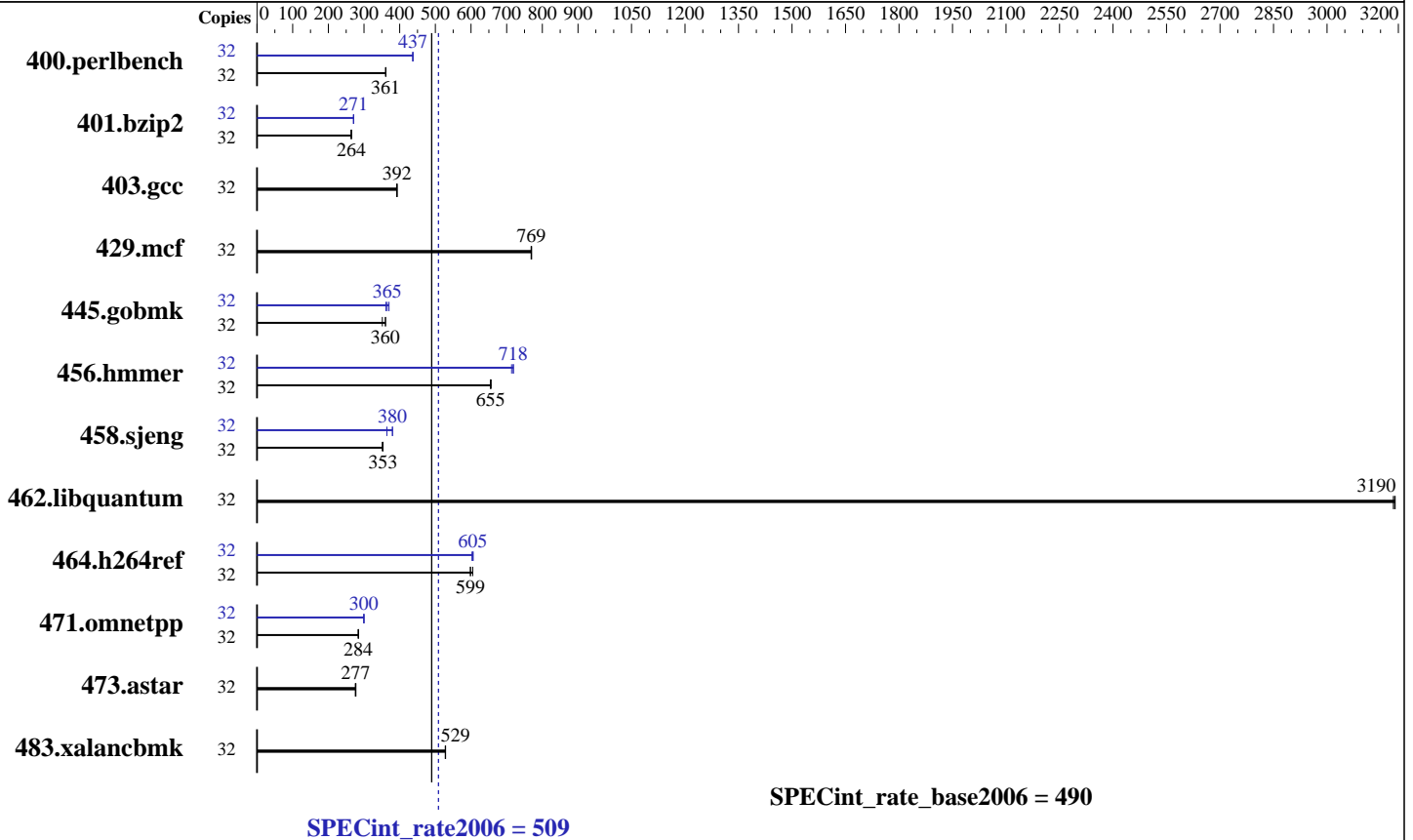
Test date: Feb-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2440 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 1900  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Kernel 2.6.32-358.18.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	865	362	867	361	<b>866</b>	<b>361</b>	32	715	437	<b>715</b>	<b>437</b>	716	437
401.bzip2	32	1171	264	1165	265	<b>1168</b>	<b>264</b>	32	1139	271	<b>1141</b>	<b>271</b>	1142	270
403.gcc	32	655	393	658	391	<b>656</b>	<b>392</b>	32	655	393	658	391	<b>656</b>	<b>392</b>
429.mcf	32	379	770	<b>380</b>	<b>769</b>	380	769	32	379	770	<b>380</b>	<b>769</b>	380	769
445.gobmk	32	932	360	955	351	<b>932</b>	<b>360</b>	32	907	370	929	361	<b>920</b>	<b>365</b>
456.hammer	32	<b>456</b>	<b>655</b>	455	656	456	654	32	<b>416</b>	<b>718</b>	418	714	415	719
458.sjeng	32	<b>1096</b>	<b>353</b>	1103	351	1096	353	32	1063	364	1019	380	<b>1020</b>	<b>380</b>
462.libquantum	32	208	3190	208	3190	<b>208</b>	<b>3190</b>	32	208	3190	208	3190	<b>208</b>	<b>3190</b>
464.h264ref	32	<b>1183</b>	<b>599</b>	1171	605	1184	598	32	<b>1170</b>	<b>605</b>	1175	603	1169	606
471.omnetpp	32	703	284	705	284	<b>704</b>	<b>284</b>	32	<b>668</b>	<b>300</b>	668	299	667	300
473.astar	32	815	276	811	277	<b>811</b>	<b>277</b>	32	815	276	811	277	<b>811</b>	<b>277</b>
483.xalancbmk	32	418	528	418	529	<b>418</b>	<b>529</b>	32	418	528	418	529	<b>418</b>	<b>529</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance  
Memory Voltage: 1.5 V

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

The Express5800/R120e-1E and the Express5800/R120e-2E models are electronically equivalent. The results have been measured on the Express5800/R120e-2E model.

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:

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## General Notes (Continued)

```
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m32  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64

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## Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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