SPEC® CINT2006 Result
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Huawei

Huawei RH5885H v3 (Intel Xeon E7-4890 v2)

SPECint®_rate2006 =   Not Run

SPECint_rate_base2006 = 2320

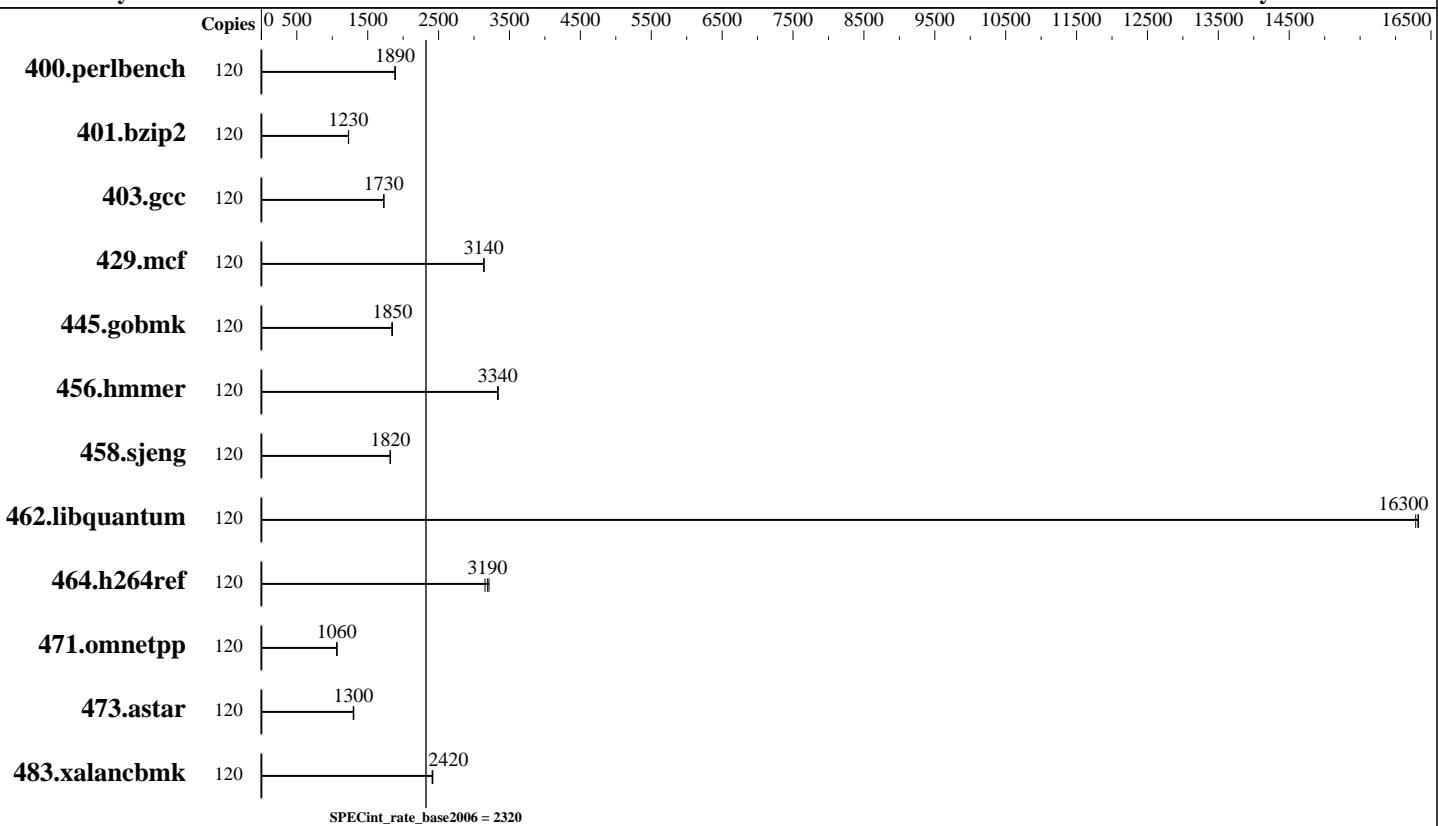|		
CPU2006 license: 13		**Test date:** Feb-2014
Test sponsor: Huawei		**Hardware Availability:** Feb-2014
Tested by: Huawei		**Software Availability:** Nov-2013

Copies 0 500 1500 2500 3500 4500 5500 6500 7500 8500 9500 10500 11500 12500 13500 14500 16500

400.perlbench 120 — 1890

401.bzip2 120 — 1230

403.gcc 120 — 1730

429.mcf 120 — 3140

445.gobmk 120 — 1850

456.hmmer 120 — 3340

458.sjeng 120 — 1820

462.libquantum 120 — 16300

464.h264ref 120 — 3190

471.omnetpp 120 — 1060

473.astar 120 — 1300

483.xalancbmk 120 — 2420

SPECint_rate_base2006 = 2320

Hardware

CPU Name:	Intel Xeon E7-4890 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	60 cores, 4 chips, 15 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	37.5 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (64 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 300 GB SAS, 10 K RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago)
	2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	120	620	1890	**621**	**1890**	622	1880							
401.bzip2	120	941	1230	943	1230	**942**	**1230**							
403.gcc	120	**560**	**1730**	561	1720	558	1730							
429.mcf	120	**349**	**3140**	349	3140	349	3140							
445.gobmk	120	684	1840	681	1850	**681**	**1850**							
456.hmmer	120	335	3340	336	3330	**336**	**3340**							
458.sjeng	120	799	1820	798	1820	**798**	**1820**							
462.libquantum	120	**152**	**16300**	153	16300	152	16300							
464.h264ref	120	827	3210	**832**	**3190**	842	3160							
471.omnetpp	120	704	1060	**704**	**1060**	704	1060							
473.astar	120	647	1300	650	1300	**647**	**1300**							
483.xalancbmk	120	343	2420	**343**	**2420**	344	2410							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

   Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    BIOS configuration:
    Set Power Efficiency Mode to Performance
    Set Lock_step to disabled
    Baseboard Management Controller used to adjust the fan speed to 100%
    Sysinfo program /spec/config/sysinfo.rev6818
    $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
    running on speccpu Sun Feb 9 20:10:41 2014

    This section contains SUT (System Under Test) info as seen by
    some common utilities. To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E7-4890 v2 @ 2.80GHz
          4 "physical id"s (chips)
          120 "processors"
       cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 15
         siblings  : 30
         physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
         physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
         physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
         physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
      cache size : 38400 KB

   From /proc/meminfo
      MemTotal:       529098336 kB
      HugePages_Total:        0
      Hugepagesize:        2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.5 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Feb 9 19:50

   SPEC is set to: /spec
      Filesystem      Type  Size  Used Avail Use% Mounted on
      /dev/sda1       ext4  241G  8.6G  220G   4% /

   Additional information from dmidecode:
     BIOS American Megatrends Inc. BLISV099 02/09/2014
     Memory:
      64x   8 GB
      12x Hynix HMT31GR7AFR4C-H9 8 GB 1333 MHz 2 rank
      52x Hynix HMT31GR7BFR4C-H9 8 GB 1333 MHz 2 rank
      32x NO DIMM NO DIMM

   (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
```

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General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.xml

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For other inquiries, please contact webmaster@spec.org.

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