



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 2390

PowerEdge R920 (Intel Xeon E7-4890 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 2320

CPU2006 license: 55

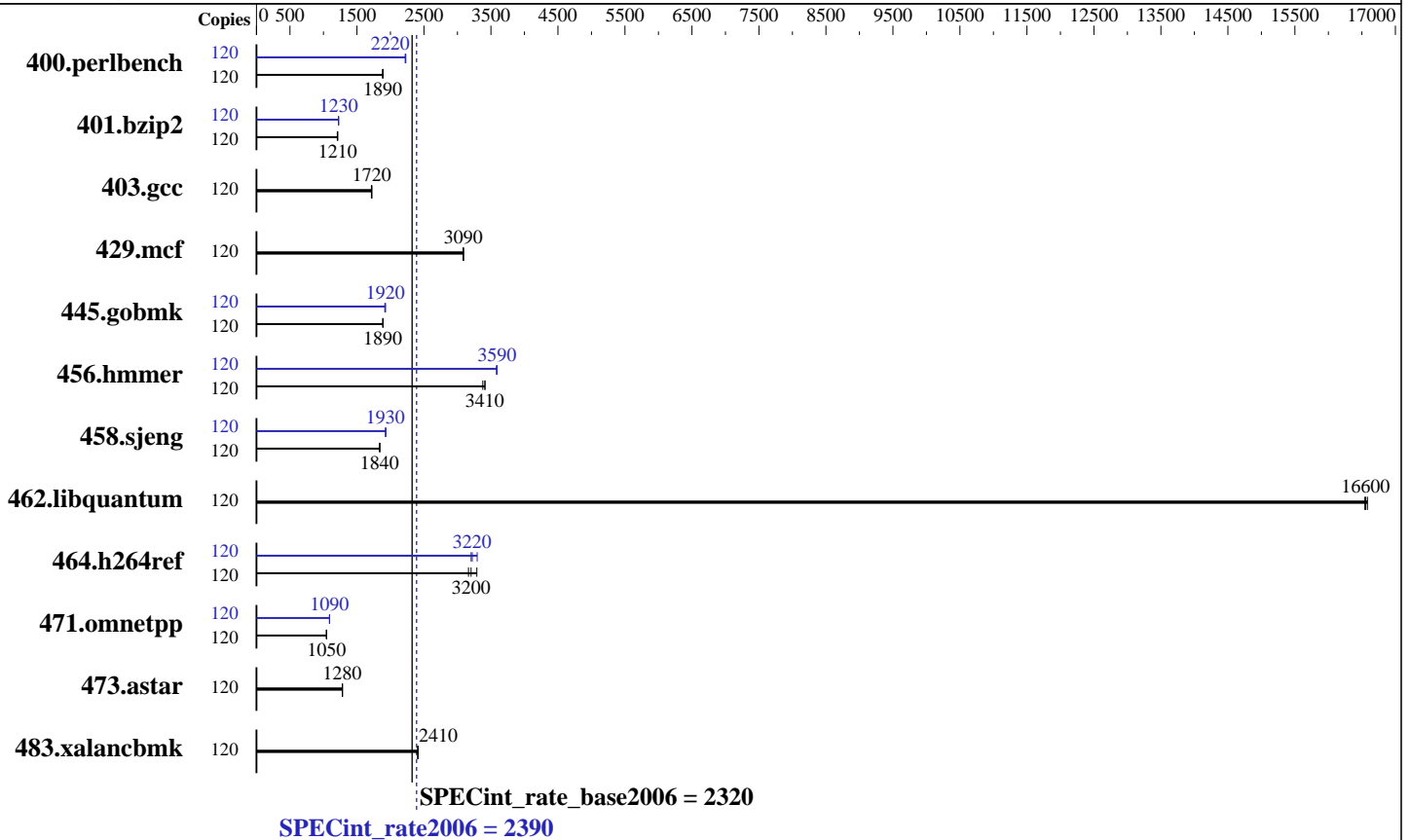
Test date: Feb-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Mar-2014



## Hardware

CPU Name: Intel Xeon E7-4890 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 37.5 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 400 GB SAS6 SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	120	621	1890	622	1880	<u>622</u>	<u>1890</u>	120	526	2230	<u>527</u>	<u>2220</u>	528	2220
401.bzip2	120	<u>956</u>	<u>1210</u>	956	1210	960	1210	120	946	1220	<u>945</u>	<u>1230</u>	944	1230
403.gcc	120	561	1720	<u>561</u>	<u>1720</u>	563	1720	120	561	1720	<u>561</u>	<u>1720</u>	563	1720
429.mcf	120	354	3090	<u>354</u>	<u>3090</u>	355	3090	120	354	3090	<u>354</u>	<u>3090</u>	355	3090
445.gobmk	120	668	1890	<u>667</u>	<u>1890</u>	666	1890	120	657	1920	654	1920	<u>654</u>	<u>1920</u>
456.hammer	120	331	3380	<u>328</u>	<u>3410</u>	328	3410	120	312	3590	<u>312</u>	<u>3590</u>	313	3580
458.sjeng	120	789	1840	789	1840	<u>789</u>	<u>1840</u>	120	<u>753</u>	<u>1930</u>	754	1930	752	1930
462.libquantum	120	150	16500	<u>150</u>	<u>16600</u>	150	16600	120	150	16500	<u>150</u>	<u>16600</u>	150	16600
464.h264ref	120	<u>829</u>	<u>3200</u>	807	3290	839	3160	120	829	3200	806	3290	<u>825</u>	<u>3220</u>
471.omnetpp	120	<u>716</u>	<u>1050</u>	720	1040	716	1050	120	<u>687</u>	<u>1090</u>	688	1090	687	1090
473.astar	120	657	1280	<u>657</u>	<u>1280</u>	656	1280	120	657	1280	<u>657</u>	<u>1280</u>	656	1280
483.xalancbmk	120	<u>344</u>	<u>2410</u>	343	2410	344	2410	120	<u>344</u>	<u>2410</u>	343	2410	344	2410

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Custom
Memory Patrol Scrub set to disabled
Sysinfo program
/root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on slesperf1 Sat Feb 1 17:25:54 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4890 v2 @ 2.80GHz
4 "physical id"s (chips)

```

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## Platform Notes (Continued)

120 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 15
siblings  : 30
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size : 38400 KB
```

From /proc/meminfo

```
MemTotal:      1058789108 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 11 (x86_64)
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

uname -a:

```
Linux slesperf1 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 31 17:26 last=S

SPEC is set to: /root/Desktop/Performance/ic14.0\_Oct17\_2013

```
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        ext2      365G      30G   334G   9% /
```

Additional information from dmidecode:

BIOS Dell Inc. 1.0.4 01/27/2014

Memory:

```
32x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
10x 00CE00B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
22x 00CE04B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = */root/Desktop/Performance/ic14.0_Oct17_2013/libs/32:/root/Desktop/Performance/ic14.0_Oct17_2013/libs/64:/root/Desktop/Performance/ic14.0_Oct17_2013/sh*
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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http://www.spec.org/



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## General Notes (Continued)

```
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmarthearp

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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