



# SPEC® CINT2006 Result

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## Sugon

SPECint®\_rate2006 = 634

I620-G15 (Intel Xeon E5-2643 v2, 3.50 GHz)

SPECint\_rate\_base2006 = 610

CPU2006 license: 9046

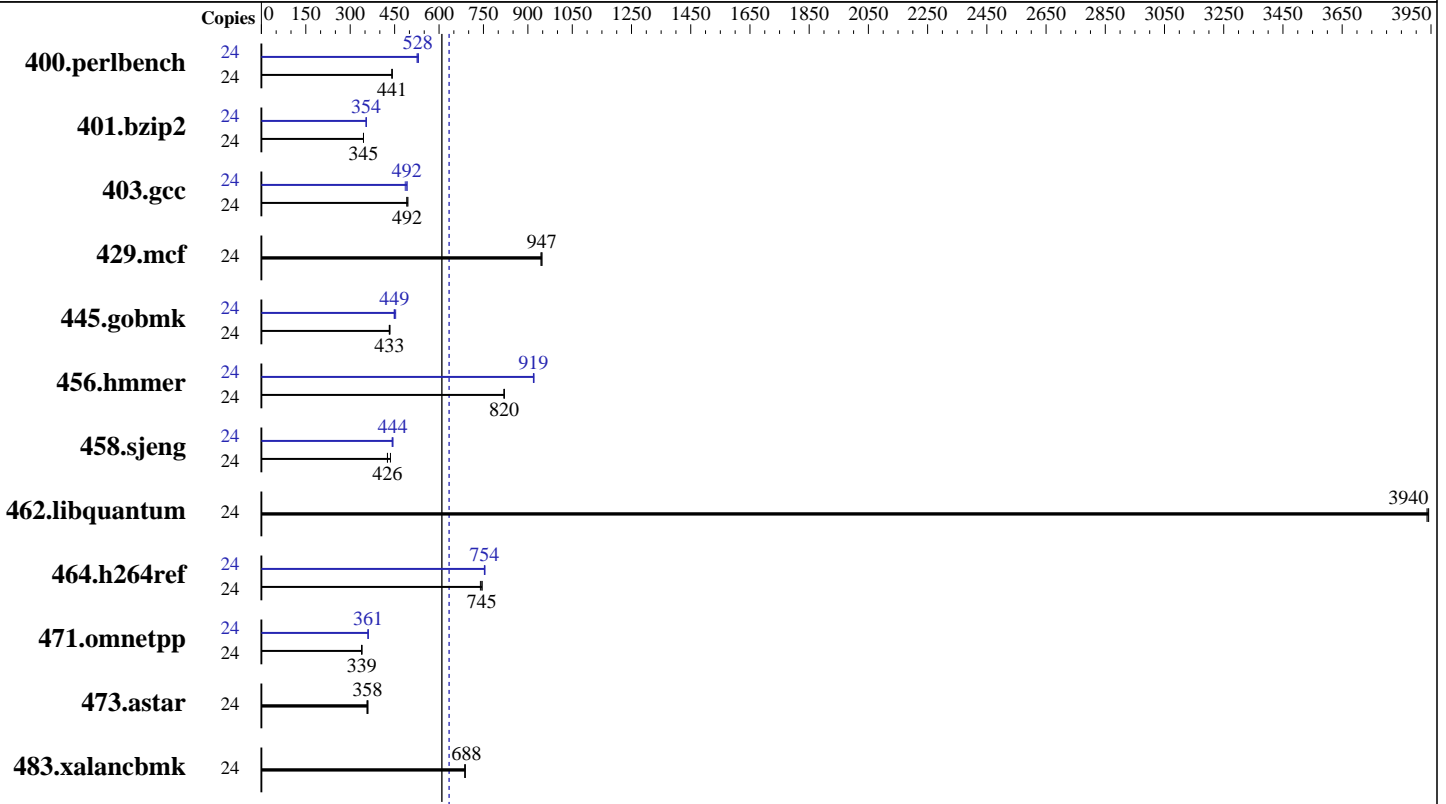
Test date: Jan-2014

Test sponsor: Sugon

Hardware Availability: Jan-2014

Tested by: Sugon

Software Availability: Jan-2014



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### Hardware

CPU Name: Intel Xeon E5-2643 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 X 2 TB SATA 7200 RPM, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>531</b>	<b>441</b>	532	441	531	442	24	<b>444</b>	<b>528</b>	442	530	446	526
401.bzip2	24	672	345	672	345	<b>672</b>	<b>345</b>	24	<b>654</b>	<b>354</b>	654	354	654	354
403.gcc	24	393	491	<b>393</b>	<b>492</b>	390	495	24	393	492	398	486	<b>393</b>	<b>492</b>
429.mcf	24	232	944	<b>231</b>	<b>947</b>	231	947	24	232	944	<b>231</b>	<b>947</b>	231	947
445.gobmk	24	579	434	583	432	<b>582</b>	<b>433</b>	24	<b>560</b>	<b>449</b>	561	449	555	454
456.hammer	24	273	820	273	820	<b>273</b>	<b>820</b>	24	<b>244</b>	<b>919</b>	244	919	243	921
458.sjeng	24	<b>682</b>	<b>426</b>	666	436	682	426	24	654	444	<b>655</b>	<b>444</b>	658	442
462.libquantum	24	126	3940	<b>126</b>	<b>3940</b>	126	3940	24	126	3940	<b>126</b>	<b>3940</b>	126	3940
464.h264ref	24	713	745	718	740	<b>713</b>	<b>745</b>	24	<b>705</b>	<b>754</b>	705	753	703	755
471.omnetpp	24	444	338	<b>442</b>	<b>339</b>	442	340	24	417	360	<b>416</b>	<b>361</b>	416	361
473.astar	24	472	357	<b>470</b>	<b>358</b>	468	360	24	472	357	<b>470</b>	<b>358</b>	468	360
483.xalancbmk	24	<b>241</b>	<b>688</b>	241	688	241	687	24	<b>241</b>	<b>688</b>	241	688	241	687

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Intel Virtualization technology set to disabled

Power Technology set to performance

Turbo boost set to enabled

DDR Speed set to force 1866

Sysinfo program /home/cpu2006/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date:: 2013-11-20 # \$ 654bd3fcf53b06faef0efe54ed011998

running on cpu2006 Mon Jan 13 18:06:37 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2643 v2 @ 3.50GHz

2 "physical id"s (chips)

24 "processors"

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings  : 12
physical 0: cores 2 3 4 8 9 10
physical 1: cores 2 3 4 8 9 10
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      264503572 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux cpu2006 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 13 18:05

SPEC is set to: /home/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_cpu2006-lv_home
                ext4      1.8T  107G  1.6T   7% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. V8.100A 10/31/2013

Memory:

16x Hynix Semiconductor HMT42GR7AFR4C-RD 16 GB 1 rank 1866 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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## General Notes (Continued)

memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmarthheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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