SPEC® CINT2006 Result

Sugon

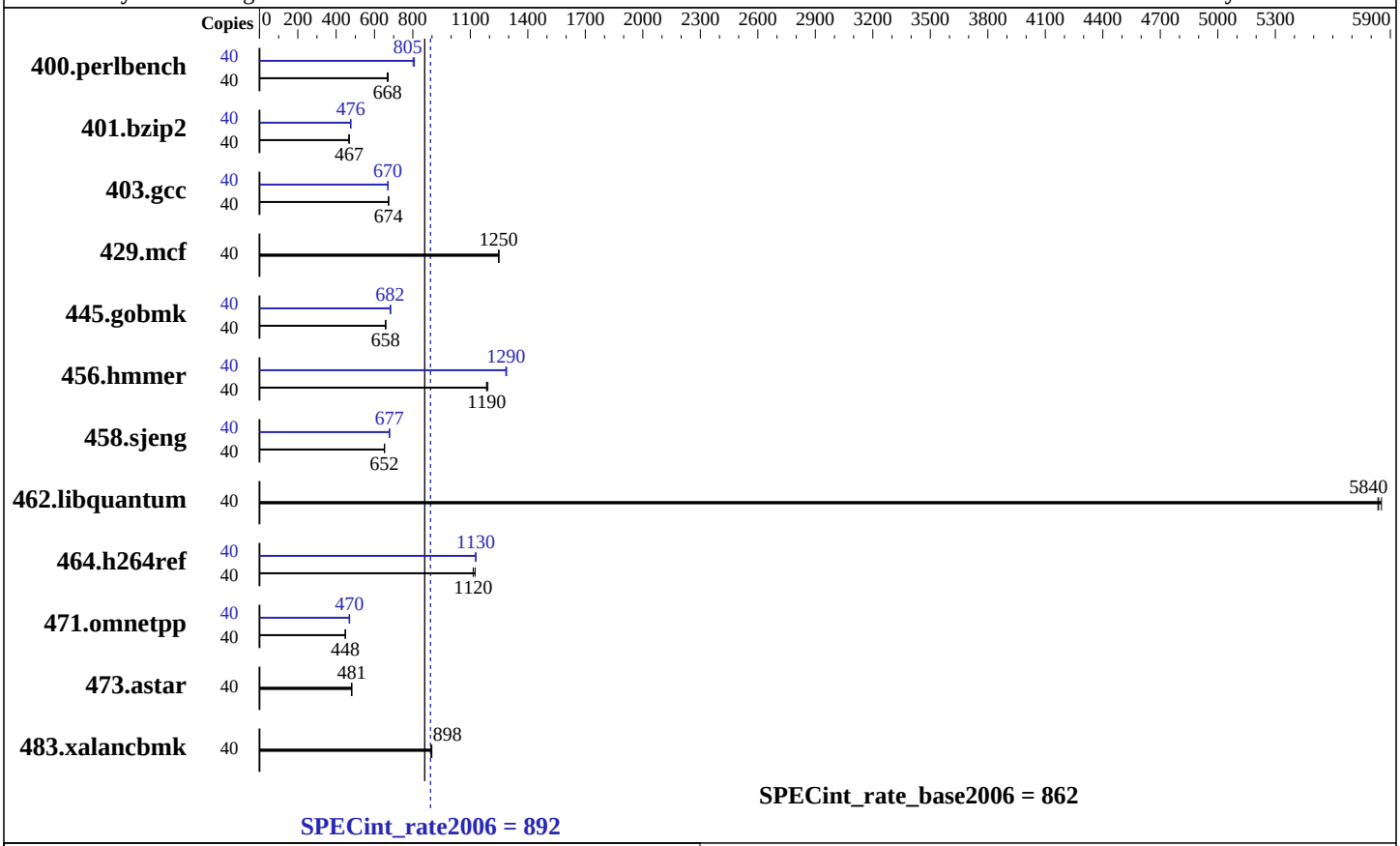
I620-G15 (Intel Xeon E5-2690 v2, 3.00 GHz)

SPECint®_rate2006 = 892

SPECint_rate_base2006 = 862

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Jan-2014
Hardware Availability: Jan-2014
Software Availability: Jan-2014



Hardware

CPU Name:	Intel Xeon E5-2690 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 X 2 TB SATA 7200 RPM, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	586	667	582	672	**585**	**668**	40	**485**	**805**	483	809	488	801
401.bzip2	40	825	468	826	467	**826**	**467**	40	810	476	812	476	**811**	**476**
403.gcc	40	**478**	**674**	479	673	478	674	40	**480**	**670**	480	671	481	669
429.mcf	40	**292**	**1250**	292	1250	292	1250	40	**292**	**1250**	292	1250	292	1250
445.gobmk	40	**638**	**658**	638	658	637	659	40	615	682	**615**	**682**	613	685
456.hmmer	40	315	1190	313	1190	**315**	**1190**	40	289	1290	**290**	**1290**	290	1290
458.sjeng	40	743	652	**742**	**652**	742	653	40	715	677	713	679	**714**	**677**
462.libquantum	40	**142**	**5840**	142	5840	142	5850	40	**142**	**5840**	142	5840	142	5850
464.h264ref	40	**793**	**1120**	786	1130	794	1110	40	**784**	**1130**	785	1130	784	1130
471.omnetpp	40	559	447	557	449	**558**	**448**	40	535	467	532	470	**532**	**470**
473.astar	40	585	480	**584**	**481**	582	482	40	585	480	**584**	**481**	582	482
483.xalancbmk	40	307	899	**307**	**898**	309	894	40	307	899	**307**	**898**	309	894

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Configuration:
Intel Virtualization technology set to disabled
Power Technology set to performance
Turbo boost set to enabled
DDR Speed set to force 1866
Sysinfo program /home/cpu2006/config/sysinfo.rev6874
$Rev: 6874 $ $Date:: 2013-11-20 #$ 654bd3fcf53b06faef0efe54ed011998
running on cpu2006 Fri Jan  3 20:11:55 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
      2 "physical id"s (chips)
      40 "processors"
```

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Platform Notes (Continued)

```
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 10
         siblings  : 20
         physical 0: cores 0 1 2 3 4 8 9 10 11 12
         physical 1: cores 0 1 2 3 4 8 9 10 11 12
      cache size : 25600 KB

   From /proc/meminfo
      MemTotal:       264501652 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.4 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux cpu2006 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Jan 3 20:09

   SPEC is set to: /home/cpu2006
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/mapper/vg_cpu2006-lv_home
                     ext4    1.8T   70G  1.6T   5% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS American Megatrends Inc. V8.100A 10/31/2013
     Memory:
      16x Hynix Semiconductor HMT42GR7AFR4C-RD 16 GB 1 rank 1866 MHz

   (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
```

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General Notes (Continued)

```
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

Continued on next page

Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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