



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 215

Altos R320 F3 (Intel Xeon E3-1280 v3)

SPECint_rate_base2006 = 208

CPU2006 license: 97

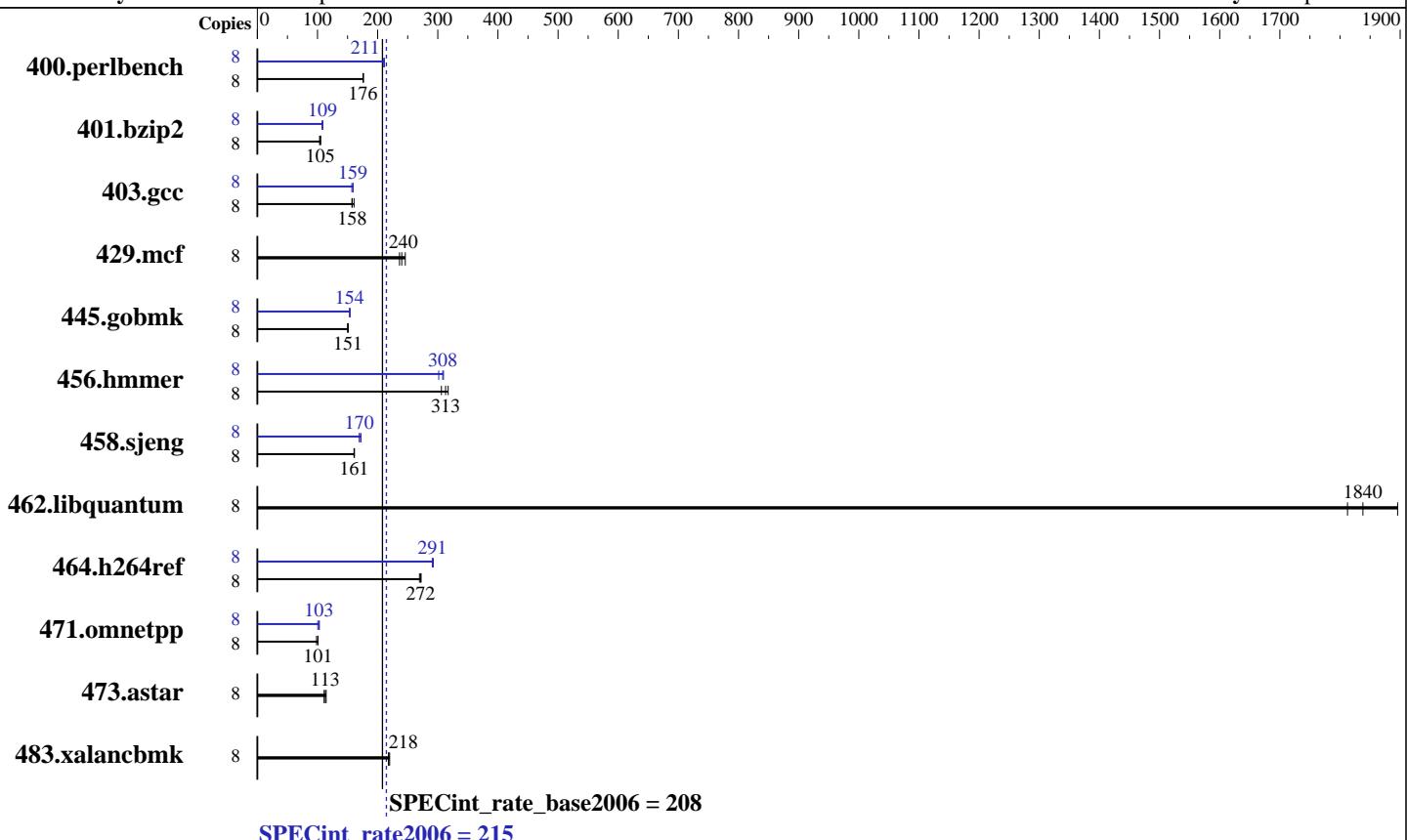
Test date: Nov-2013

Test sponsor: Acer Incorporated

Hardware Availability: Dec-2013

Tested by: Acer Incorporated

Software Availability: Sep-2013



Hardware	
CPU Name:	Intel Xeon E3-1280 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem:	1 x 1000 GB SATA, 7200 RPM
Other Hardware:	None

Software	
Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	443	176	445	176	444	176	8	372	210	371	211	370	211
401.bzip2	8	739	105	746	103	733	105	8	717	108	711	109	709	109
403.gcc	8	409	157	401	161	408	158	8	405	159	410	157	404	159
429.mcf	8	297	246	304	240	309	236	8	297	246	304	240	309	236
445.gobmk	8	559	150	557	151	556	151	8	546	154	544	154	547	153
456.hammer	8	235	317	238	313	244	306	8	241	309	247	302	242	308
458.sjeng	8	601	161	602	161	600	161	8	571	170	570	170	561	172
462.libquantum	8	91.5	1810	87.4	1900	90.2	1840	8	91.5	1810	87.4	1900	90.2	1840
464.h264ref	8	657	270	651	272	652	272	8	605	293	608	291	609	291
471.omnetpp	8	497	101	508	98.4	496	101	8	486	103	486	103	498	101
473.astar	8	497	113	506	111	491	114	8	497	113	506	111	491	114
483.xalancbmk	8	251	220	253	218	253	218	8	251	220	253	218	253	218

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date::: 2012-07-17 #$
running on localhost.localdomain Fri Nov 22 16:48:17 2013
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1280 v3 @ 3.60GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
```

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Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       16107800 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 22 16:43

SPEC is set to: /usr/cpu2006
Filesystem      Type   Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                  ext4   902G   13G  844G    2%  /


Additional information from dmidecode:
BIOS Intel Corp. S1200RP.86B.01.03.0004.082120131450 08/21/2013
Memory:
2x 8 GB
2x Kingston ACR16D3LE1EFG/8G 8 GB 1600 MHz 2 rank
2x [Empty] [Empty]

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/sh -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.html>
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 16:53:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 31 December 2013.