SPEC® CINT2006 Result

IBM Corporation

IBM System x3650 M4
(Intel Xeon E5-2660 v2, 2.20 GHz)

$SPECint®\_rate2006 = 743$

$SPECint\_rate\_base2006 = 717$

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2013
Hardware Availability: Dec-2013
Software Availability: Sep-2013

Benchmark	Copies	Base	Peak
400.perlbench	40	533	639
401.bzip2	40	387	395
403.gcc	40	571	567
429.mcf	40	1110	
445.gobmk	40	523	542
456.hmmer	40	971	1070
458.sjeng	40	520	540
462.libquantum	40	4720	
464.h264ref	40	885	898
471.omnetpp	40	402	424
473.astar	40	409	
483.xalancbmk	40	773	

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Hardware

CPU Name: Intel Xeon E5-2660 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
2.6.32-358.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

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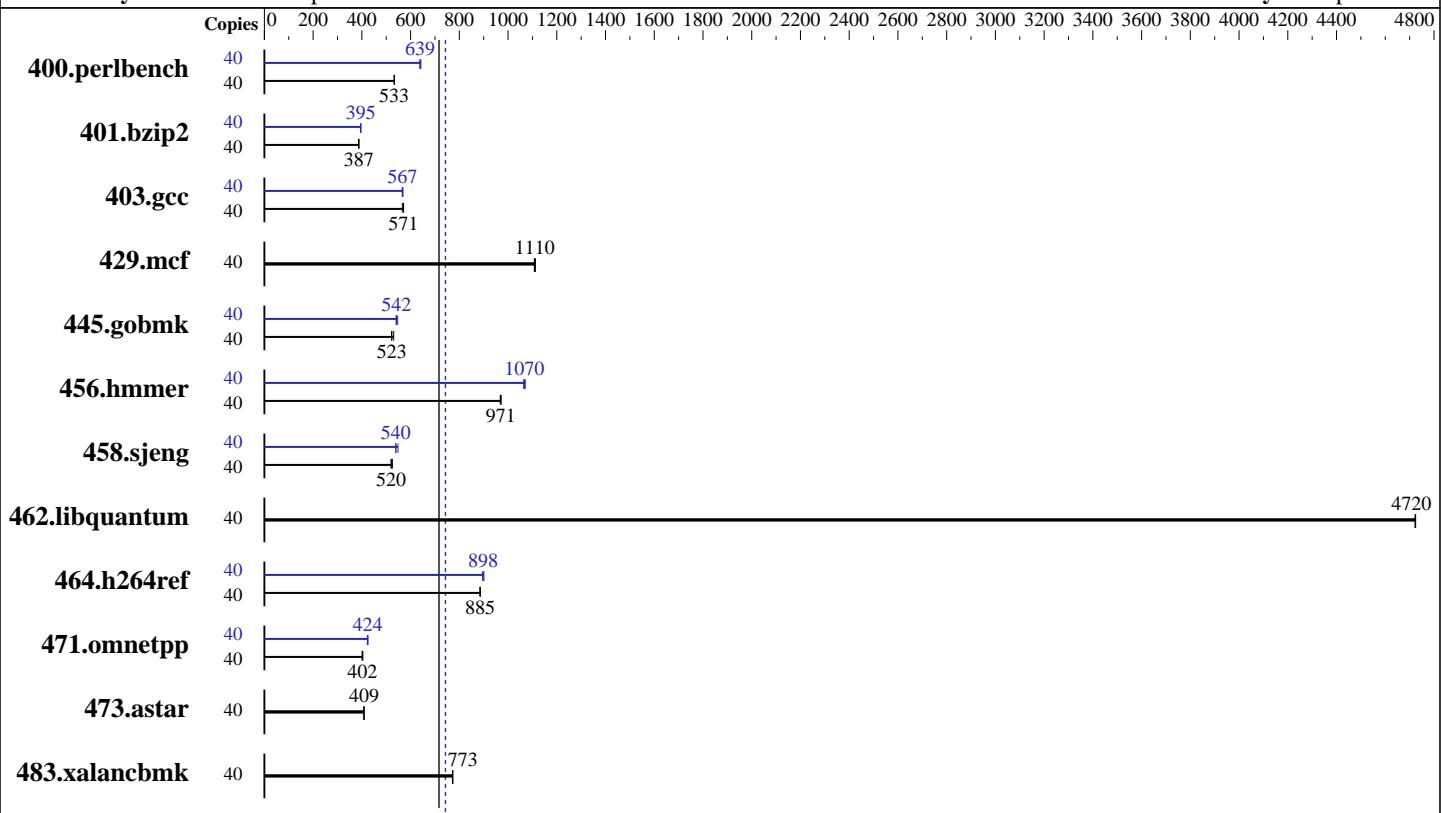SPECint_rate_base2006 = 717

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	**733**	**533**	733	533	733	533	40	**611**	**639**	612	638	608	642
401.bzip2	40	995	388	997	387	**997**	**387**	40	**976**	**395**	975	396	978	395
403.gcc	40	**564**	**571**	564	571	569	566	40	566	569	569	566	**568**	**567**
429.mcf	40	**329**	**1110**	329	1110	328	1110	40	**329**	**1110**	329	1110	328	1110
445.gobmk	40	804	522	**802**	**523**	791	531	40	767	547	776	541	**775**	**542**
456.hmmer	40	386	968	384	971	**384**	**971**	40	349	1070	351	1060	**349**	**1070**
458.sjeng	40	930	520	**930**	**520**	921	525	40	**897**	**540**	898	539	883	548
462.libquantum	40	**175**	**4720**	176	4720	175	4720	40	**175**	**4720**	176	4720	175	4720
464.h264ref	40	1000	885	**1000**	**885**	999	886	40	**986**	**898**	982	901	988	896
471.omnetpp	40	619	404	**622**	**402**	623	401	40	591	423	588	425	**589**	**424**
473.astar	40	688	408	**686**	**409**	686	409	40	688	408	**686**	**409**	686	409
483.xalancbmk	40	358	772	357	774	**357**	**773**	40	358	772	357	774	**357**	**773**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3650M4 Tue Nov 19 12:06:02 2013

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2660 v2 @ 2.20GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.   The

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Platform Notes (Continued)

```
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 10
       siblings  : 20
       physical 0: cores 0 1 2 3 4 8 9 10 11 12
       physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 25600 KB

  From /proc/meminfo
    MemTotal:       264339664 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

  /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
    Linux x3650M4 2.6.32-358.18.1.el6.x86_64 #1 SMP Fri Aug 2 17:04:38 EDT 2013
    x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Nov 19 12:04

  SPEC is set to: /home/SPECcpu-new
    Filesystem     Type    Size  Used Avail Use% Mounted on
    /dev/mapper/vg_x3650m4-lv_home
                   ext4    313G  200G   97G  68% /home

  Additional information from dmidecode:
    BIOS IBM -[TESTBUILD-1.50]- 08/09/2013
    Memory:
     8x Not Specified Not Specified
    16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

  (End of data from sysinfo program)
 "Not Specified" memory information from dmidecode indicates unused DIMM slots.
 The BIOS IBM -[TESTBUILD-1.50] is equivalent to production version [VVE134TUS-1.51]
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

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General Notes (Continued)

```
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
    icc   -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc   -m32

    400.perlbench: icc -m64

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Peak Compiler Invocation (Continued)

            401.bzip2: `icc -m64`

         456.hmmer: `icc -m64`

           458.sjeng: `icc -m64`

    C++ benchmarks:
        `icpc -m32`

Peak Portability Flags

        400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
            401.bzip2: `-DSPEC_CPU_LP64`
            456.hmmer: `-DSPEC_CPU_LP64`
            458.sjeng: `-DSPEC_CPU_LP64`
        462.libquantum: `-DSPEC_CPU_LINUX`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-auto-ilp32`

            401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-opt-prefetch -auto-ilp32 -ansi-alias`

            403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

            429.mcf: basepeak = yes

        445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                `-ansi-alias -opt-mem-layout-trans=3`

        456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

            458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-unroll4 -auto-ilp32`

        462.libquantum: basepeak = yes

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Peak Optimization Flags (Continued)

464.h264ref: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs`
`-L/sh` `-lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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