SPEC® CINT2006 Result
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Cisco Systems

Cisco UCS B200 M3 (Intel Xeon E5-2697 v2, 2.70 GHz)

$SPECint^®\_rate2006 =$ Not Run

SPECint_rate_base2006 = 940

CPU2006 license: 9019	**Test date:** Aug-2013
Test sponsor: Cisco Systems	**Hardware Availability:** Sep-2013
Tested by: Cisco Systems	**Software Availability:** Aug-2013

Copies 0 200 400 600 800 1100 1400 1700 2000 2300 2600 2900 3200 3500 3800 4100 4400 4700 5000 5300 5600 5900 6400

```
400.perlbench    48    729
401.bzip2        48    511
403.gcc          48    729
429.mcf          48    1370
445.gobmk        48    717
456.hmmer        48    1270
458.sjeng        48    712
462.libquantum   48    6330
464.h264ref      48    1250
471.omnetpp      48    498
473.astar        48    516
483.xalancbmk    48    972
```

SPECint_rate_base2006 = 940

Hardware

CPU Name:	Intel Xeon E5-2697 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-14900R-11, ECC)
Disk Subsystem:	1 X 300 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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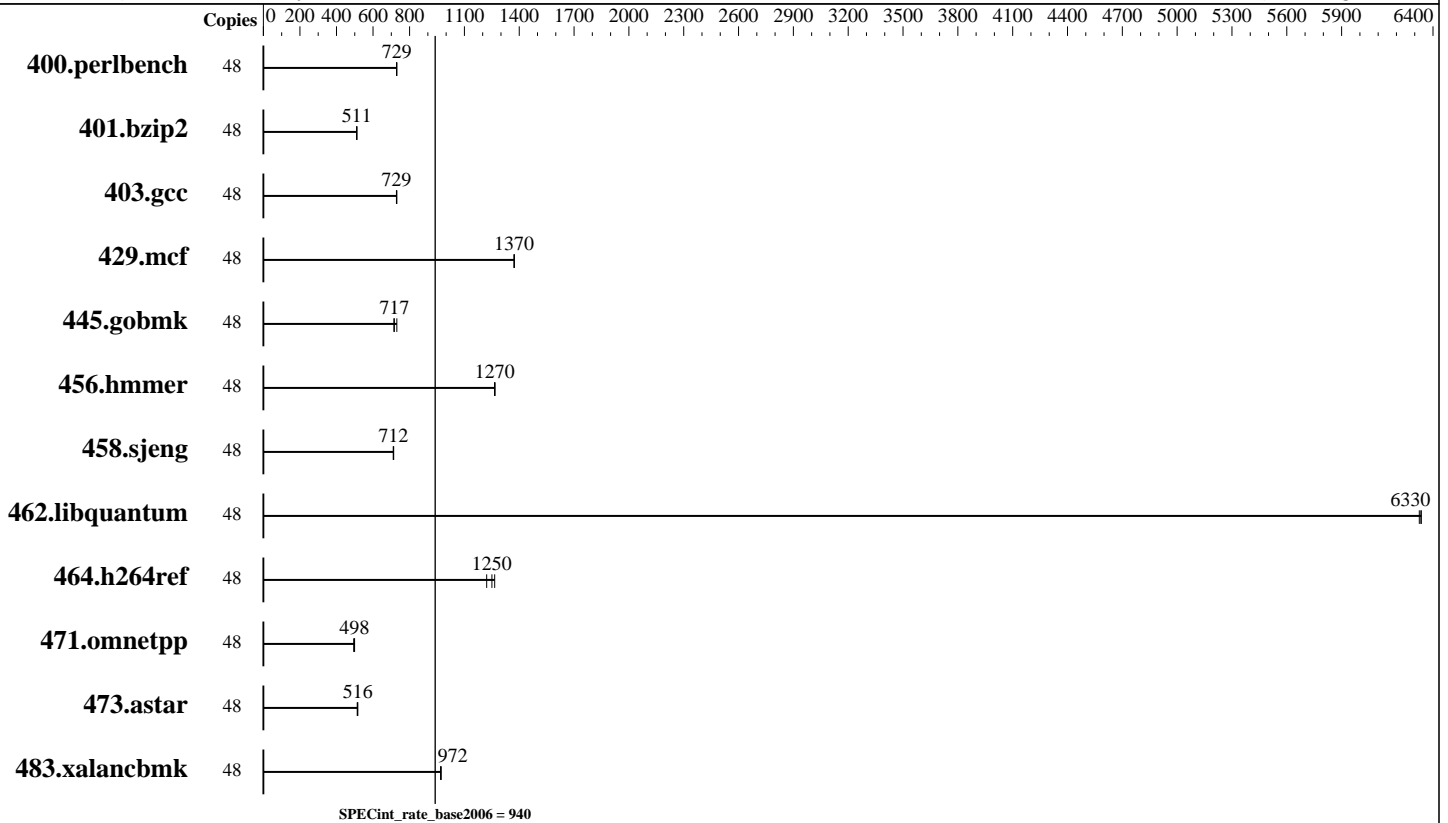
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	**643**	**729**	644	729	642	730							
401.bzip2	48	907	511	905	512	**907**	**511**							
403.gcc	48	529	730	531	728	**530**	**729**							
429.mcf	48	319	1370	**319**	**1370**	319	1370							
445.gobmk	48	**703**	**717**	705	714	690	730							
456.hmmer	48	353	1270	**354**	**1270**	354	1260							
458.sjeng	48	814	713	**816**	**712**	817	711							
462.libquantum	48	157	6340	157	6330	**157**	**6330**							
464.h264ref	48	839	1270	869	1220	**849**	**1250**							
471.omnetpp	48	601	499	**602**	**498**	607	494							
473.astar	48	653	516	656	513	**653**	**516**							
483.xalancbmk	48	341	972	342	970	**341**	**972**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    BIOS Settings:
    CPU performance set to HPC
    Processor C State set to Disabled
    Processor C1E set to Disabled
    Processor C6 report set to Disabled
    Energy Performance Policy set to Performance
    Memory RAS configuration Set to Max-Performance
    LV DDR Mode set to Performance-mode
    DRAM Refresh Rate Set to 1x
    Intel HT Technology = Enable
     Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6818
     $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
     running on SPEC-RHEL Wed Aug 28 13:40:26 2013

     This section contains SUT (System Under Test) info as seen by
     some common utilities. To remove or add to this section, see:
       http://www.spec.org/cpu2006/Docs/config.html#sysinfo

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:       132085336 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux SPEC-RHEL 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 28 13:18

SPEC is set to: /opt/cpu2006-1.2
   Filesystem      Type     Size  Used Avail Use% Mounted on
   /dev/sda2       ext4     274G   48G  212G  19% /

Additional information from dmidecode:
  BIOS Cisco Systems, Inc. B200M3.2.1.2.12.080620131158 08/06/2013
  Memory:
   16x 0xAD00 HMT31GR7EFR4C-RD 8 GB 1866 MHz 2 rank
   8x NO DIMM NO DIMM

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
```

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General Notes (Continued)

```
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_IA32
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

```
   403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.20130717.html

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You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.20130717.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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