



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp®_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

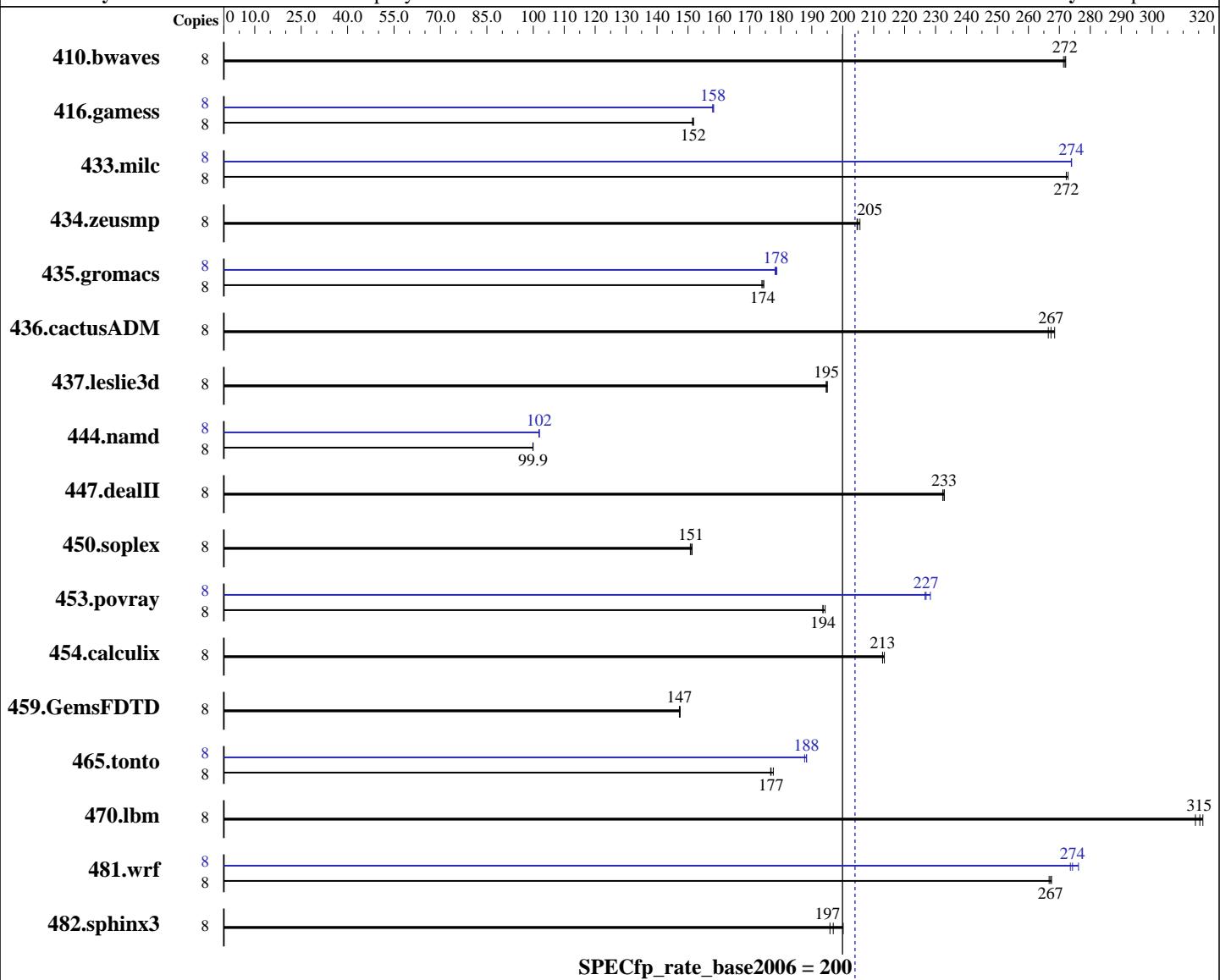
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013



SPECfp_rate_base2006 = 200

SPECfp_rate2006 = 204

Hardware

CPU Name: Intel Xeon E5-2603 v2
CPU Characteristics:
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: Kernel 2.6.32-358.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	401	271	400	272	400	272	8	401	271	400	272	400	272
416.gamess	8	1032	152	1034	151	1033	152	8	991	158	992	158	990	158
433.milc	8	269	273	270	272	270	272	8	268	274	268	274	268	274
434.zeusmp	8	356	205	355	205	354	206	8	356	205	355	205	354	206
435.gromacs	8	329	174	328	174	327	175	8	320	179	321	178	320	178
436.cactusADM	8	358	267	356	268	359	266	8	358	267	356	268	359	266
437.leslie3d	8	386	195	386	195	386	195	8	386	195	386	195	386	195
444.namd	8	642	99.9	642	99.9	642	99.9	8	629	102	630	102	630	102
447.dealII	8	393	233	394	232	393	233	8	393	233	394	232	393	233
450.soplex	8	442	151	441	151	443	151	8	442	151	441	151	443	151
453.povray	8	219	194	220	194	220	194	8	188	227	188	227	186	228
454.calculix	8	309	213	310	213	310	213	8	309	213	310	213	310	213
459.GemsFDTD	8	576	147	577	147	576	147	8	576	147	577	147	576	147
465.tonto	8	445	177	443	178	445	177	8	418	188	418	188	419	188
470.lbm	8	350	314	348	315	347	316	8	350	314	348	315	347	316
481.wrf	8	334	267	334	267	335	267	8	324	276	326	274	327	274
482.sphinx3	8	792	197	779	200	796	196	8	792	197	779	200	796	196

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Used "stop-services" script before the run



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
Energy/Performance Bias is set to Maximum Performance
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x

```
Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on DL380p-Gen8-OS9 Sun Oct 27 02:12:23 2013
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v2 @ 1.80GHz
        2 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 4
        siblings : 4
        physical 0: cores 0 1 2 3
        physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      132119284 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DL380p-Gen8-OS9 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 25 21:44
```

```
SPEC is set to: /cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
Continued on next page
```



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Platform Notes (Continued)

/dev/sda3 ext4 365G 27G 320G 8% /

Additional information from dmidecode:

BIOS HP P70 09/08/2013

Memory:

16x HP 689911-071 8 GB 1333 MHz 2 rank
8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:

16x HP 689911-071 8 GB 1333 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:
`LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/lib64:/cpu2006/sh"`

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Base Portability Flags (Continued)

```
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -auto-ilp32
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
            -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECfp_rate2006 = 204

SPECfp_rate_base2006 = 200

CPU2006 license: 3

Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 17:31:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 November 2013.