# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

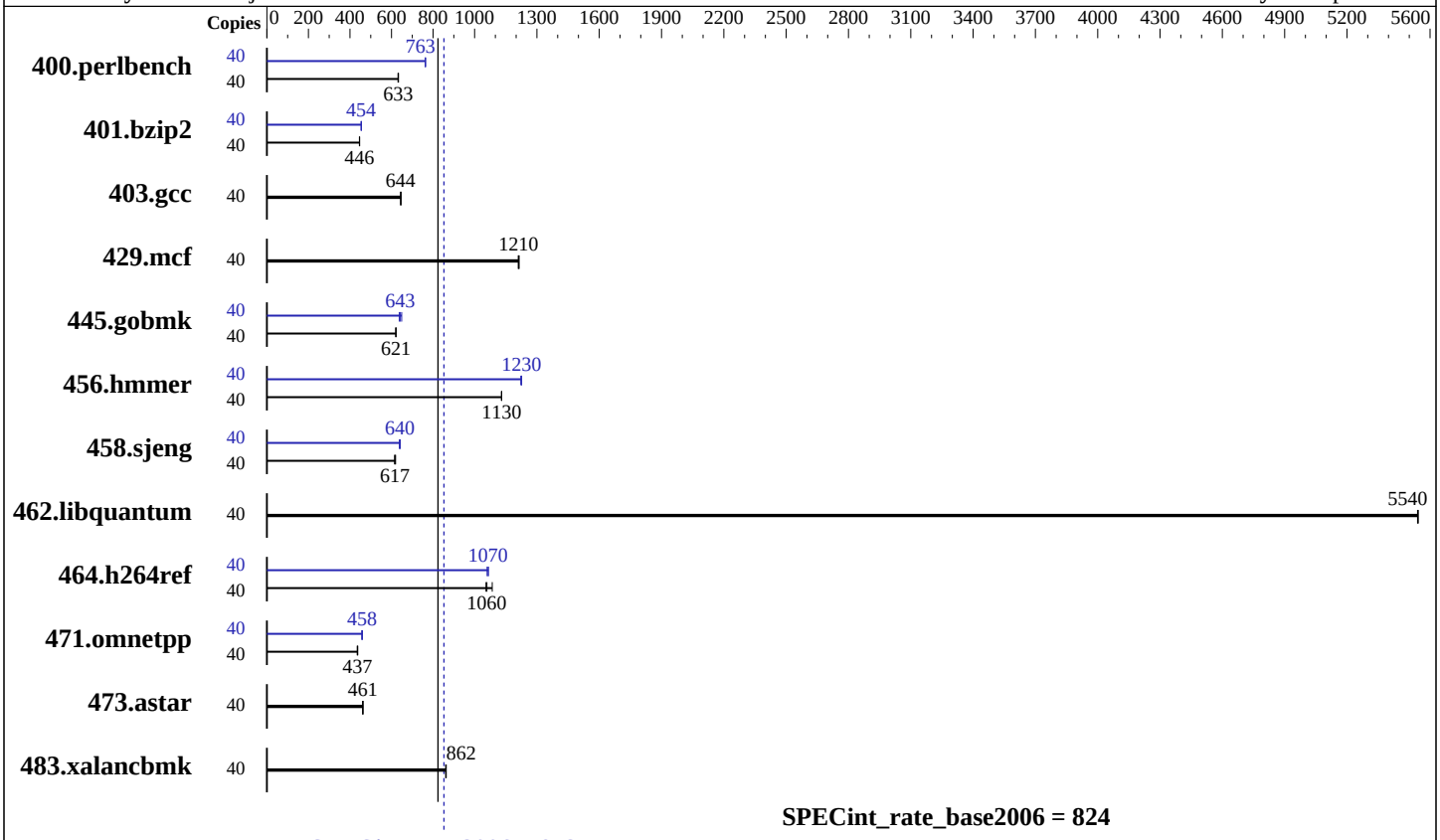
PRIMERGY TX300 S8, Intel Xeon E5-2680 v2, 2.80 GHz

SPECint®_rate2006 = 852

SPECint_rate_base2006 = 824

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Sep-2013
**Hardware Availability:** Oct-2013
**Software Availability:** Sep-2013




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2680 v2 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.60 GHz |
| CPU MHz: | 2800 |
| FPU: | Integrated |
| CPU(s) enabled: | 20 cores, 2 chips, 10 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC) |
| Disk Subsystem: | 1 x SATA, 450 GB, 15000 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>2.6.32-358.11.1.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY TX300 S8, Intel Xeon E5-2680 v2, 2.80 GHz

SPECint_rate2006 = 852

SPECint_rate_base2006 = 824

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Sep-2013
**Hardware Availability:** Oct-2013
**Software Availability:** Sep-2013

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 40 | 619 | 631 | **617** | **633** | 617 | 633 | 40 | **512** | **763** | 512 | 764 | 513 | 762 |
| 401.bzip2 | 40 | 865 | 446 | **865** | **446** | 867 | 445 | 40 | 849 | 455 | **850** | **454** | 850 | 454 |
| 403.gcc | 40 | **500** | **644** | 499 | 646 | 500 | 644 | 40 | **500** | **644** | 499 | 646 | 500 | 644 |
| 429.mcf | 40 | 300 | 1210 | **301** | **1210** | 301 | 1210 | 40 | 300 | 1210 | **301** | **1210** | 301 | 1210 |
| 445.gobmk | 40 | 676 | 621 | 675 | 622 | **676** | **621** | 40 | 647 | 649 | 659 | 637 | **653** | **643** |
| 456.hmmer | 40 | 330 | 1130 | **330** | **1130** | 330 | 1130 | 40 | **304** | **1230** | 305 | 1220 | 304 | 1230 |
| 458.sjeng | 40 | 781 | 620 | 787 | 615 | **784** | **617** | 40 | 759 | 638 | **756** | **640** | 754 | 642 |
| 462.libquantum | 40 | 150 | 5540 | 150 | 5540 | **150** | **5540** | 40 | 150 | 5540 | 150 | 5540 | **150** | **5540** |
| 464.h264ref | 40 | **836** | **1060** | 839 | 1050 | 816 | 1080 | 40 | 830 | 1070 | **831** | **1070** | 835 | 1060 |
| 471.omnetpp | 40 | **572** | **437** | 572 | 437 | 575 | 435 | 40 | 544 | 460 | **545** | **458** | 547 | 457 |
| 473.astar | 40 | **609** | **461** | 606 | 463 | 609 | 461 | 40 | **609** | **461** | 606 | 463 | 609 | 461 |
| 483.xalancbmk | 40 | 320 | 863 | **320** | **862** | 321 | 861 | 40 | 320 | 863 | **320** | **862** | 321 | 861 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Energy Performance = Performance
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Continued on next page

## General Notes (Continued)

This result was measured on the PRIMERGY RX350 S8. The PRIMERGY RX350 S8
and the PRIMERGY TX300 S8 are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com

## Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

Continued on next page

## Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

Continued on next page

## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml`


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 17:24:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 November 2013.