



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant BL460c Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECint®2006 = 29.5

SPECint_base2006 = 28.2

CPU2006 license: 3

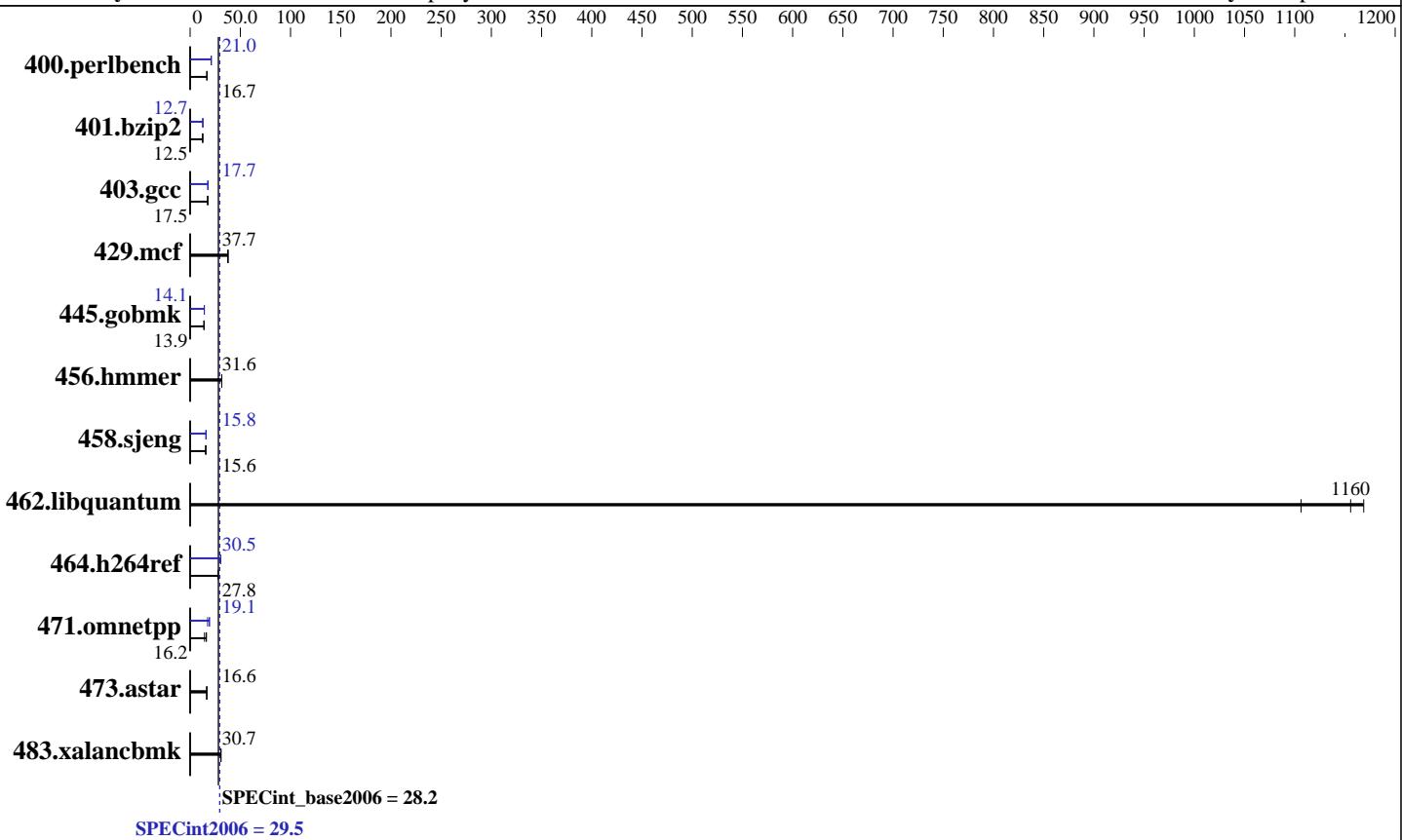
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2603 v2
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem:	1 x 146 GB 15 K SAS, RAID 1
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP3
Compiler:	Kernel 3.0.76-0.11-default
Auto Parallel:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
File System:	Yes
System State:	ext3
Base Pointers:	Run level 3 (multi-user)
Peak Pointers:	32/64-bit
Other Software:	32/64-bit
	Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	584	16.7	583	16.8	584	16.7	465	21.0	465	21.0	464	21.1
401.bzip2	771	12.5	771	12.5	771	12.5	762	12.7	762	12.7	763	12.7
403.gcc	460	17.5	458	17.6	460	17.5	454	17.7	454	17.7	454	17.7
429.mcf	242	37.7	242	37.7	242	37.7	242	37.7	242	37.7	242	37.7
445.gobmk	757	13.9	756	13.9	757	13.9	742	14.1	742	14.1	742	14.1
456.hmmer	296	31.6	296	31.6	296	31.5	296	31.6	296	31.6	296	31.5
458.sjeng	776	15.6	777	15.6	776	15.6	763	15.9	764	15.8	764	15.8
462.libquantum	18.7	1110	17.9	1160	17.7	1170	18.7	1110	17.9	1160	17.7	1170
464.h264ref	796	27.8	798	27.7	794	27.9	728	30.4	726	30.5	726	30.5
471.omnetpp	436	14.3	387	16.2	387	16.2	361	17.3	327	19.1	324	19.3
473.astar	423	16.6	423	16.6	423	16.6	423	16.6	423	16.6	423	16.6
483.xalancbmk	225	30.7	225	30.7	224	30.7	225	30.7	225	30.7	224	30.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

Disabled unused Linux services through "stop_services.sh" before running.

Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance

Minimum Processor Idle Power Core State set to C1E

Minimum Processor Idle Power Package State set to C6 (non-retention)

Energy/Performance Bias is set to Maximum Performance

Memory Power Savings Mode set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Dynamic Power Capping Functionality set to Disabled

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on bl460c-gen8-bay8 Fri Oct 18 17:58:35 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2603 v2 @ 1.80GHz
        2 "physical id"s (chips)
        8 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 4
        siblings : 4
        physical 0: cores 0 1 2 3
        physical 1: cores 0 1 2 3
    cache size : 10240 KB

From /proc/meminfo
    MemTotal:       132130196 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
        VERSION = 11
        PATCHLEVEL = 3

uname -a:
    Linux bl460c-gen8-bay8 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC
    2013 (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 18 17:54 last=S

SPEC is set to: /cpu2006
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2        ext3  133G   12G  121G   9%  /

Additional information from dmidecode:
    BIOS HP I31 09/08/2013
    Memory:
        16x HP 689911-071 8 GB 1333 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "8"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
 -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

 403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
  403.gcc: -DSPEC_CPU_LP64
  429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
  473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -opt-prefetch -ansi-alias
```

```
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
  -opt-prefetch -ansi-alias
```

```
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
  -opt-malloc-options=3 -auto-ilp32
```

429.mcf: basepeak = yes

```
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
  -ansi-alias
```

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

```
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll14
```

462.libquantum: basepeak = yes

```
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll12 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block           -ansi-alias
              -Wl,-z,muldefs -L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html>
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>



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