SPEC® CINT2006 Result

Hewlett-Packard Company

SPECint®2006 = 29.4

ProLiant DL380p Gen8
(1.80 GHz, Intel Xeon E5-2603 v2)

SPECint_base2006 = 27.8

CPU2006 license: 3
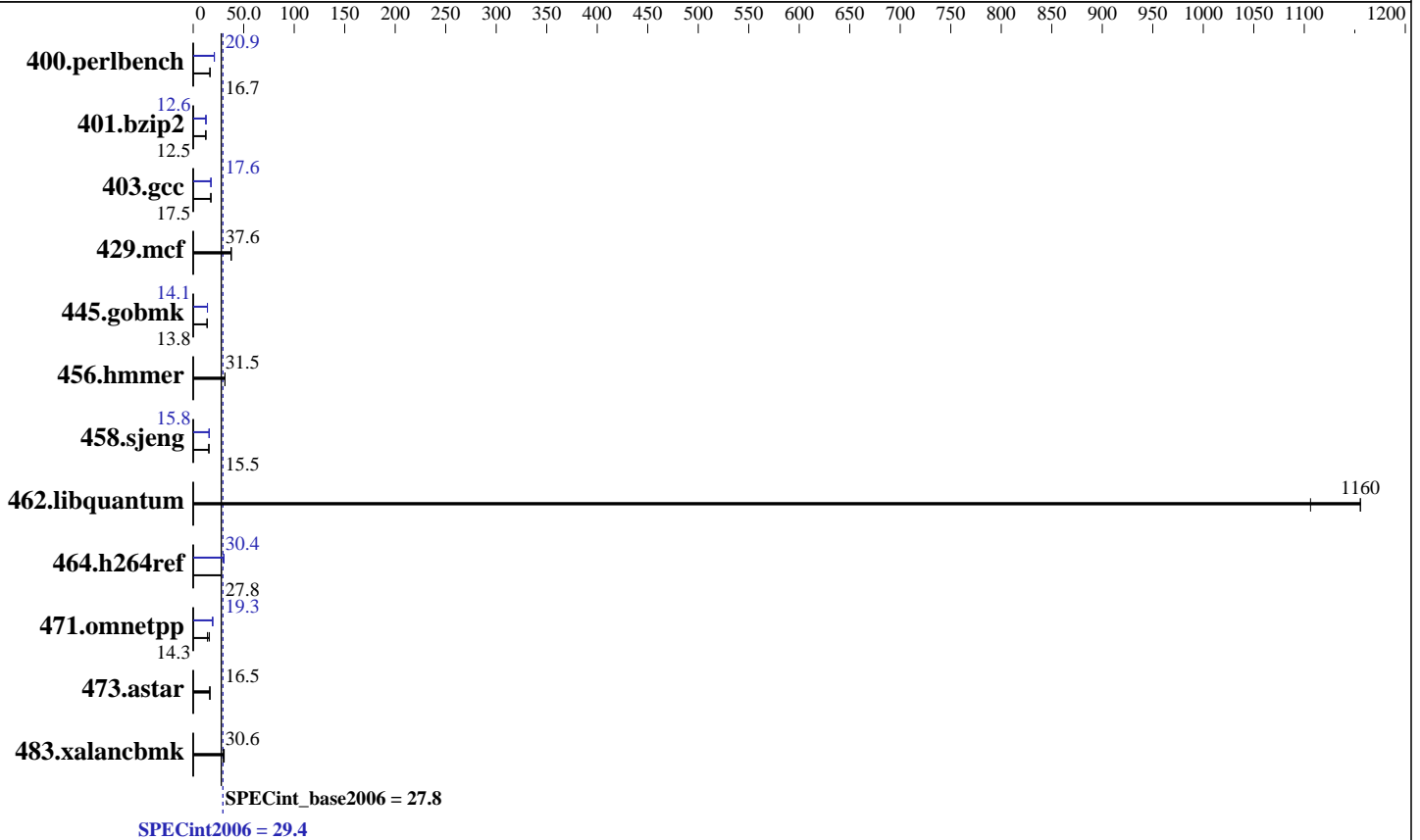
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Benchmark results:

Benchmark	Peak	Base
400.perlbench	20.9	16.7
401.bzip2	12.6	12.5
403.gcc	17.6	17.5
429.mcf		37.6
445.gobmk	14.1	13.8
456.hmmer		31.5
458.sjeng	15.8	15.5
462.libquantum		1160
464.h264ref	30.4	27.8
471.omnetpp	19.3	14.3
473.astar		16.5
483.xalancbmk		30.6

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Hardware

CPU Name: Intel Xeon E5-2603 v2

CPU Characteristics:

CPU MHz: 1800

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip

CPU(s) orderable: 1,2 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 10 MB I+D on chip per chip

Other Cache: None

Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)

Disk Subsystem: 1 x 146 GB 15 K SAS, RAID 0

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3 Kernel 3.0.76-0.11-default

Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux

Auto Parallel: Yes

File System: ext3

System State: Run level 3 (multi-user)

Base Pointers: 32/64-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V10.0

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Results Table

Benchmark	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**586**	**16.7**	585	16.7	588	16.6	467	20.9	466	21.0	**467**	**20.9**
401.bzip2	**773**	**12.5**	773	12.5	773	12.5	**764**	**12.6**	765	12.6	764	12.6
403.gcc	**459**	**17.5**	462	17.4	458	17.6	**456**	**17.6**	456	17.7	460	17.5
429.mcf	242	37.7	**243**	**37.6**	243	37.6	242	37.7	**243**	**37.6**	243	37.6
445.gobmk	758	13.8	760	13.8	**758**	**13.8**	744	14.1	**744**	**14.1**	744	14.1
456.hmmer	296	31.5	**296**	**31.5**	297	31.5	296	31.5	**296**	**31.5**	297	31.5
458.sjeng	778	15.5	778	15.5	**778**	**15.5**	765	15.8	766	15.8	**765**	**15.8**
462.libquantum	**17.9**	**1160**	18.7	1110	17.9	1160	**17.9**	**1160**	18.7	1110	17.9	1160
464.h264ref	796	27.8	**797**	**27.8**	797	27.8	728	30.4	**729**	**30.4**	729	30.3
471.omnetpp	439	14.2	**438**	**14.3**	390	16.0	324	19.3	325	19.2	**325**	**19.3**
473.astar	**425**	**16.5**	426	16.5	423	16.6	**425**	**16.5**	426	16.5	423	16.6
483.xalancbmk	**225**	**30.6**	232	29.8	225	30.7	**225**	**30.6**	232	29.8	225	30.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Reclaim mode enabled with:
 echo 1 > /proc/sys/vm/zone_reclaim_mode
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Maximum Performance
 Minimum Processor Idle Power Core State set to C1E
 Minimum Processor Idle Power Package State set to C6 (retention)
 Energy/Performance Bias is set to Maximum Performance
 Memory Power Savings Mode set to Maximum Performance
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x

 Sysinfo program /cpu2006/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on dl380p-gen8-0s9 Sun Oct 20 04:11:57 2013
```

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2603 v2 @ 1.80GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:        132130192 kB
   HugePages_Total:         0
   Hugepagesize:         2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux dl380p-gen8-0s9 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
   (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 19 18:31 last=S

SPEC is set to: /cpu2006
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda1       ext3  135G   14G  121G  10% /

Additional information from dmidecode:
  BIOS HP P70 09/08/2013
  Memory:
   16x HP 689911-071 8 GB 1333 MHz
   8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 128 GB and the dmidecode description should read as the following:
   16x HP 689911-071 8 GB 1333 MHz
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
```
    icc  -m64
```

C++ benchmarks:
```
    icpc -m64
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
     471.omnetpp: -DSPEC_CPU_LP64
        473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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