



# SPEC® CFP2006 Result

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Acer Incorporated

**SPECfp®\_rate2006 = 150**

Altos T310 F3 (Intel Xeon E3-1280 v3)

**SPECfp\_rate\_base2006 = 145**

CPU2006 license: 97

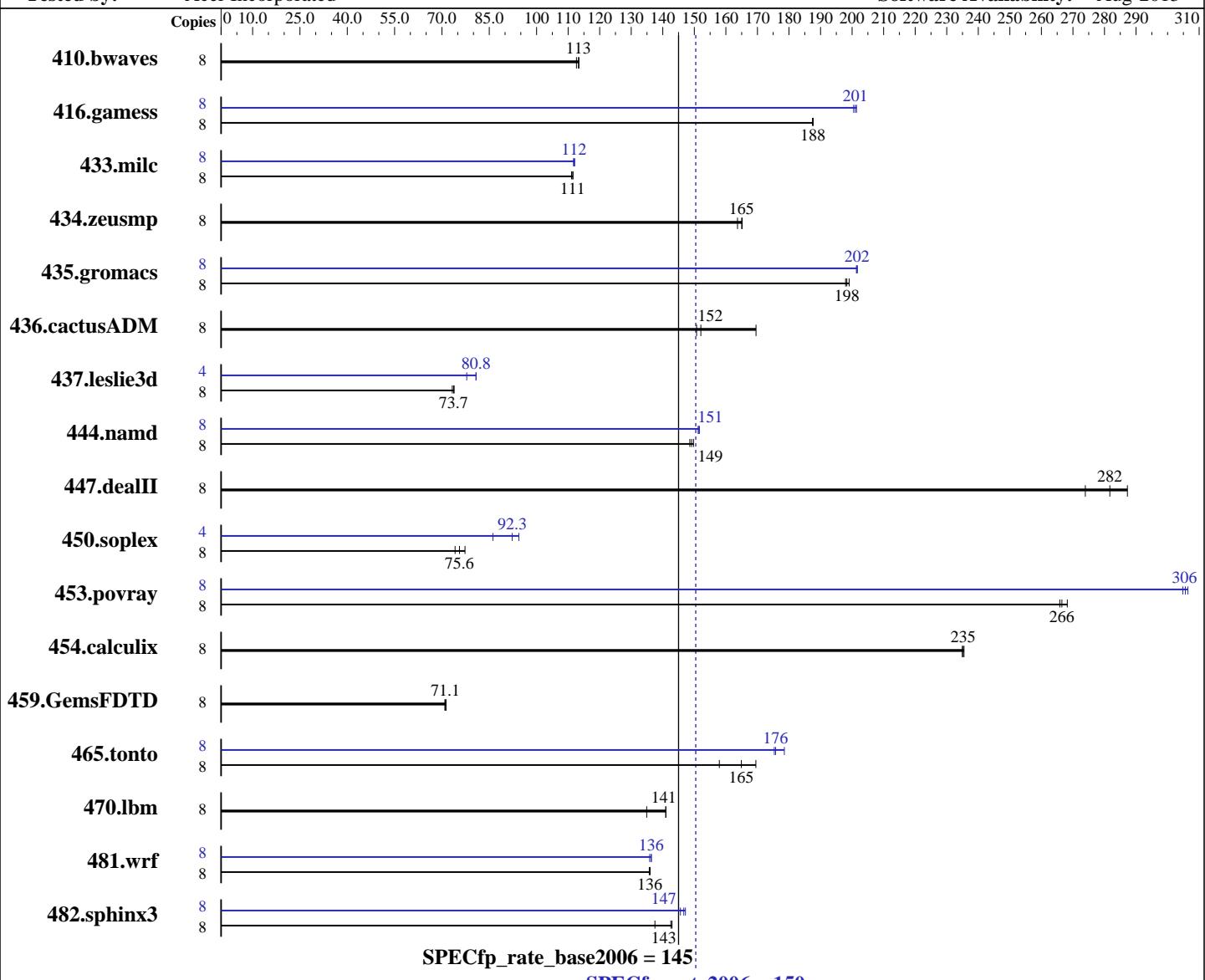
Test date: Sep-2013

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2013

Tested by: Acer Incorporated

Software Availability: Aug-2013



## Hardware

CPU Name: Intel Xeon E3-1280 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
Compiler: 2.6.32-358.el6.x86\_64  
C/C++: Version 13.0.0.133 of Intel C++ Studio XE for Linux  
Auto Parallel: No  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx4 PC3L-12800E-11, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	959	113	965	113	<b>960</b>	<b>113</b>	8	959	113	965	113	<b>960</b>	<b>113</b>
416.gamess	8	836	187	835	188	<b>835</b>	<b>188</b>	8	<b>779</b>	<b>201</b>	778	201	<b>781</b>	200
433.milc	8	<b>659</b>	<b>111</b>	661	111	659	112	8	<b>656</b>	<b>112</b>	657	112	<b>655</b>	112
434.zeusmp	8	441	165	<b>441</b>	<b>165</b>	445	164	8	441	165	<b>441</b>	<b>165</b>	445	164
435.gromacs	8	<b>288</b>	<b>198</b>	289	198	287	199	8	<b>283</b>	<b>202</b>	284	201	283	202
436.cactusADM	8	564	170	634	151	<b>629</b>	<b>152</b>	8	564	170	634	151	<b>629</b>	<b>152</b>
437.leslie3d	8	1018	73.9	1026	73.3	<b>1020</b>	<b>73.7</b>	4	465	80.8	483	77.9	<b>465</b>	<b>80.8</b>
444.namd	8	432	149	<b>430</b>	<b>149</b>	428	150	8	<b>424</b>	<b>151</b>	424	151	423	152
447.dealII	8	334	274	319	287	<b>325</b>	<b>282</b>	8	334	274	319	287	<b>325</b>	<b>282</b>
450.soplex	8	<b>883</b>	<b>75.6</b>	863	77.3	900	74.2	4	387	86.2	353	94.4	<b>362</b>	<b>92.3</b>
453.povray	8	160	266	159	268	<b>160</b>	<b>266</b>	8	<b>139</b>	<b>306</b>	140	305	139	306
454.calculix	8	<b>281</b>	<b>235</b>	280	235	281	235	8	<b>281</b>	<b>235</b>	280	235	281	235
459.GemsFDTD	8	1197	70.9	<b>1193</b>	<b>71.1</b>	1190	71.3	8	1197	70.9	<b>1193</b>	<b>71.1</b>	1190	71.3
465.tonto	8	498	158	464	170	<b>477</b>	<b>165</b>	8	449	175	441	179	<b>448</b>	<b>176</b>
470.lbm	8	815	135	780	141	<b>780</b>	<b>141</b>	8	815	135	780	141	<b>780</b>	<b>141</b>
481.wrf	8	658	136	<b>657</b>	<b>136</b>	657	136	8	658	136	<b>655</b>	<b>136</b>	655	136
482.sphinx3	8	<b>1094</b>	<b>143</b>	1091	143	1134	138	8	<b>1071</b>	<b>146</b>	1059	147	<b>1063</b>	<b>147</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818  
 \$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
 running on localhost.localdomain Mon Sep 9 22:30:42 2013

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1280 v3 @ 3.60GHz
        1 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 8
    physical 0: cores 0 1 2 3
    cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:       16282236 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Sep 9 10:42 last=3
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                  ext3   50G   13G   34G   28%  /
```

Additional information from dmidecode:

```
BIOS Acer P01 08/26/2013
Memory:
2x 8 GB
2x Hynix/Hyundai HMT41GU7AFR8A-PB 8 GB 1600 MHz 2 rank
2x [Empty] [Empty]
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64
```

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -static
           -auto-ilp32
```

470.lbm: basepeak = yes

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
              -unroll12
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2)
             -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
             -opt-malloc-options=3
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2)
             -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
             -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep- -static
```

434.zeusmp: basepeak = yes

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## Peak Optimization Flags (Continued)

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130716.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130716.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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