SPEC® CINT2006 Result
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Supermicro

SuperServer F627R2-F72+
(X9DRFF-7+ , Intel Xeon E5-2697 v2)

$SPECint®\_rate2006 = 961$
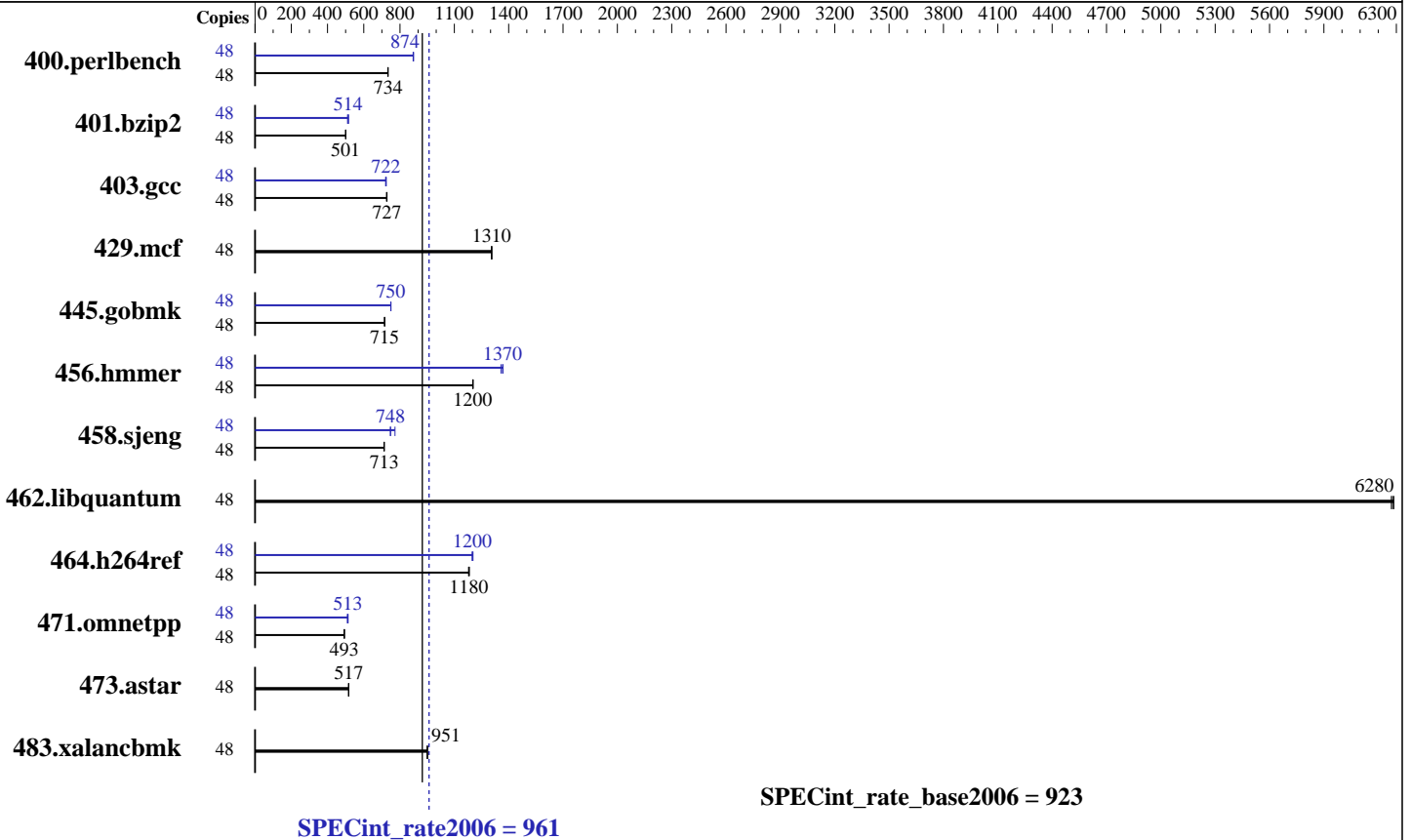
$SPECint\_rate\_base2006 = 923$

Copies
- 400.perlbench: 48 — 874; 48 — 734
- 401.bzip2: 48 — 514; 48 — 501
- 403.gcc: 48 — 722; 48 — 727
- 429.mcf: 48 — 1310
- 445.gobmk: 48 — 750; 48 — 715
- 456.hmmer: 48 — 1370; 48 — 1200
- 458.sjeng: 48 — 748; 48 — 713
- 462.libquantum: 48 — 6280
- 464.h264ref: 48 — 1200; 48 — 1180
- 471.omnetpp: 48 — 513; 48 — 493
- 473.astar: 48 — 517
- 483.xalancbmk: 48 — 951

SPECint_rate_base2006 = 923

SPECint_rate2006 = 961

Hardware

CPU Name:	Intel Xeon E5-2697 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	2 x 400 GB SATA III, RAID0, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.18.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	639	734	**639**	**734**	640	733	48	536	875	**537**	**874**	537	873
401.bzip2	48	927	500	925	501	**925**	**501**	48	907	511	898	516	**901**	**514**
403.gcc	48	530	728	**532**	**727**	532	726	48	**536**	**722**	533	724	536	722
429.mcf	48	**335**	**1310**	335	1310	335	1300	48	**335**	**1310**	335	1310	335	1300
445.gobmk	48	704	716	**704**	**715**	704	715	48	672	750	**672**	**750**	671	750
456.hmmer	48	**372**	**1200**	373	1200	372	1200	48	327	1370	330	1360	**328**	**1370**
458.sjeng	48	**815**	**713**	815	713	816	712	48	778	747	752	772	**776**	**748**
462.libquantum	48	159	6270	**158**	**6280**	158	6290	48	159	6270	**158**	**6280**	158	6290
464.h264ref	48	898	1180	**900**	**1180**	901	1180	48	**885**	**1200**	886	1200	883	1200
471.omnetpp	48	**609**	**493**	610	492	608	493	48	589	509	585	513	**585**	**513**
473.astar	48	**652**	**517**	653	516	651	518	48	**652**	**517**	653	516	651	518
483.xalancbmk	48	**348**	**951**	348	952	348	951	48	**348**	**951**	348	952	348	951

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>   /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

       401.bzip2: icc -m64

     456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
     401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

     401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias

       403.gcc: -xAVX -ipo -O3 -no-prec-div

       429.mcf: basepeak = yes

    445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

     458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32

 462.libquantum: basepeak = yes

    464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/sh -lsmartheap

     473.astar: basepeak = yes

 483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml

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