SPEC® CINT2006 Result
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Hewlett-Packard Company

SPECint®2006 =   35.1

ProLiant BL460c Gen8
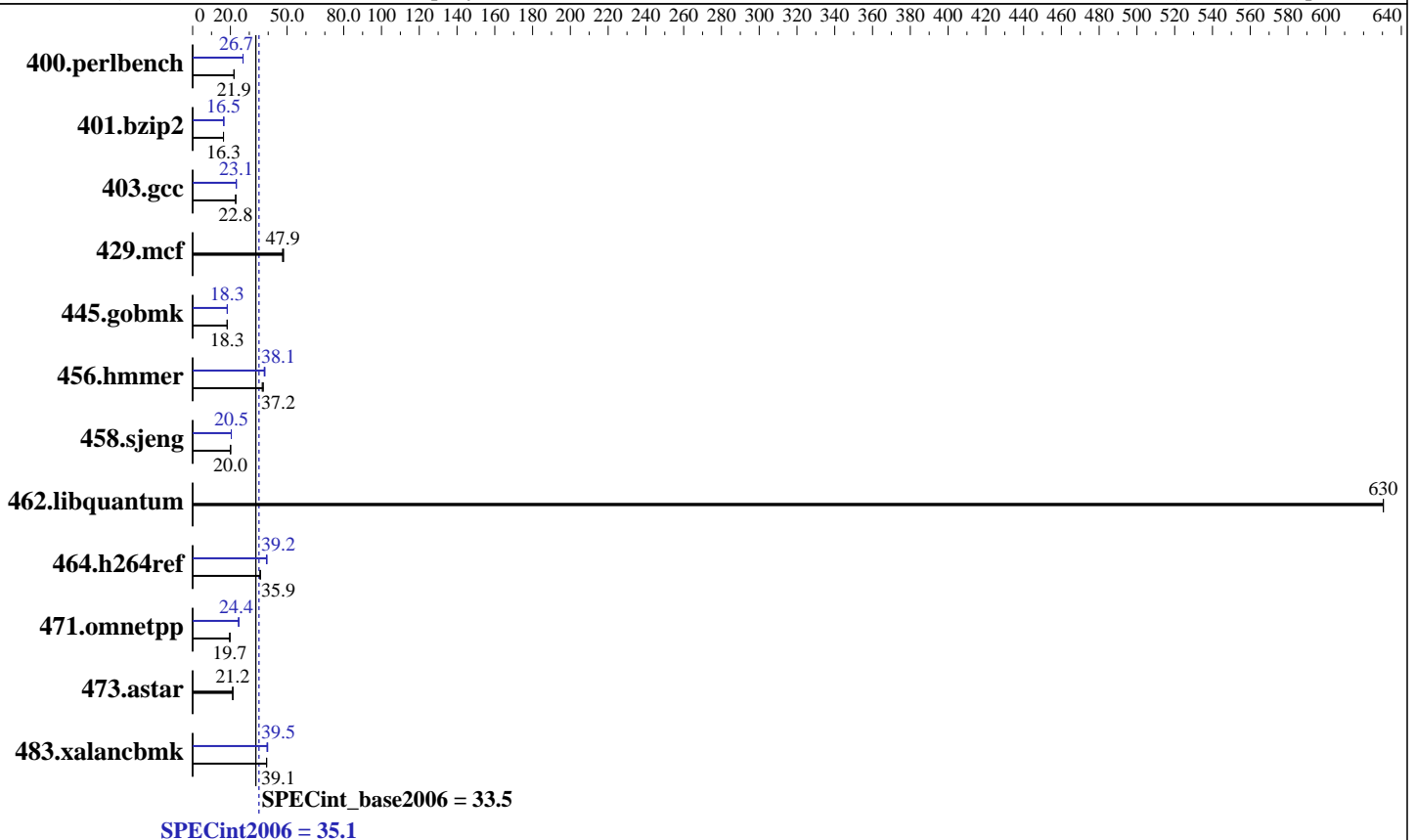(2.40 GHz, Intel Xeon E5-2609)

SPECint_base2006 =   33.5

CPU2006 license: 3	**Test date:** Sep-2013
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Jun-2012
Tested by: Hewlett-Packard Company	**Software Availability:** Sep-2013


```
                0 20.0   50.0   80.0 100 120 140 160 180 200 220 240 260 280 300 320 340 360 380 400 420 440 460 480 500 520 540 560 580 600   640
```

400.perlbench   26.7
                 21.9
401.bzip2   16.5
                 16.3
403.gcc   23.1
                 22.8
429.mcf   47.9
445.gobmk   18.3
                 18.3
456.hmmer   38.1
                 37.2
458.sjeng   20.5
                 20.0
462.libquantum   630
464.h264ref   39.2
                 35.9
471.omnetpp   24.4
                 19.7
473.astar   21.2
483.xalancbmk   39.5
                 39.1

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Hardware

CPU Name:	Intel Xeon E5-2609
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 1Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
Disk Subsystem:	1 x 300 GB SAS SSD, RAID 1
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4, (Santiago) Kernel version 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**446**	**21.9**	446	21.9	446	21.9	367	26.7	366	26.7	**366**	**26.7**
401.bzip2	590	16.4	**591**	**16.3**	591	16.3	586	16.5	**586**	**16.5**	586	16.5
403.gcc	**353**	**22.8**	353	22.8	353	22.8	348	23.1	348	23.1	**348**	**23.1**
429.mcf	189	48.2	192	47.6	**190**	**47.9**	189	48.2	192	47.6	**190**	**47.9**
445.gobmk	573	18.3	573	18.3	**573**	**18.3**	572	18.3	**572**	**18.3**	572	18.3
456.hmmer	249	37.4	253	36.8	**251**	**37.2**	244	38.3	**245**	**38.1**	246	37.9
458.sjeng	601	20.1	604	20.0	**604**	**20.0**	591	20.5	591	20.5	**591**	**20.5**
462.libquantum	**32.9**	**630**	32.9	630	32.9	630	**32.9**	**630**	32.9	630	32.9	630
464.h264ref	**616**	**35.9**	616	35.9	622	35.6	**564**	**39.2**	566	39.1	563	39.3
471.omnetpp	319	19.6	**317**	**19.7**	316	19.8	**256**	**24.4**	256	24.4	256	24.4
473.astar	330	21.3	**331**	**21.2**	331	21.2	330	21.3	**331**	**21.2**	331	21.2
483.xalancbmk	**176**	**39.1**	176	39.2	176	39.1	174	39.8	175	39.4	**175**	**39.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Maximum Performance
 Memory Power Savings Mode set to Maximum Performance
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on BL460cGen8-RFP-BAO Thu Sep  5 11:47:37 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:        8019200 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux BL460cGen8-RFP-BAO 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST
   2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 5 11:46

SPEC is set to: /cpu2006
   Filesystem     Type     Size  Used Avail Use% Mounted on
   /dev/sda3      ext4     360G  8.7G  333G   3% /

Additional information from dmidecode:
  BIOS HP I31 03/01/2013
  Memory:
   1x HP Not Specified 4 GB 1067 MHz 1 rank
   14x UNKNOWN Not Specified
   1x UNKNOWN Not Specified 4 GB 1067 MHz 1 rank

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 8 GB and the dmidecode description should read as the following:
   1x HP Not Specified 4 GB 1067 MHz 1 rank
   1x UNKNOWN Not Specified 4 GB 1067 MHz 1 rank
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.ic14.0/libs/32:/cpu2006.ic14.0/libs/64:/cpu2006.ic14.0/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
     471.omnetpp: -DSPEC_CPU_LP64
       473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

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Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc -m64`

    400.perlbench: `icc -m32`

     445.gobmk: `icc -m32`

    464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
    `icpc -m32`

     473.astar: `icpc -m64`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
       401.bzip2: `-DSPEC_CPU_LP64`
        403.gcc: `-DSPEC_CPU_LP64`
        429.mcf: `-DSPEC_CPU_LP64`
     456.hmmer: `-DSPEC_CPU_LP64`
      458.sjeng: `-DSPEC_CPU_LP64`
  462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
      473.astar: `-DSPEC_CPU_LP64`
  483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-opt-prefetch -ansi-alias`

      401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
                `-opt-prefetch -ansi-alias`

        403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
                `-opt-malloc-options=3 -auto-ilp32`

        429.mcf: `basepeak = yes`

     445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                `-ansi-alias`

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Peak Optimization Flags (Continued)

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4`

462.libquantum: basepeak = yes

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-ra-region-strategy=block          -ansi-alias`
`-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`
`-Wl,-z,muldefs -L/sh -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml

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