SPEC® CINT2006 Result

Supermicro

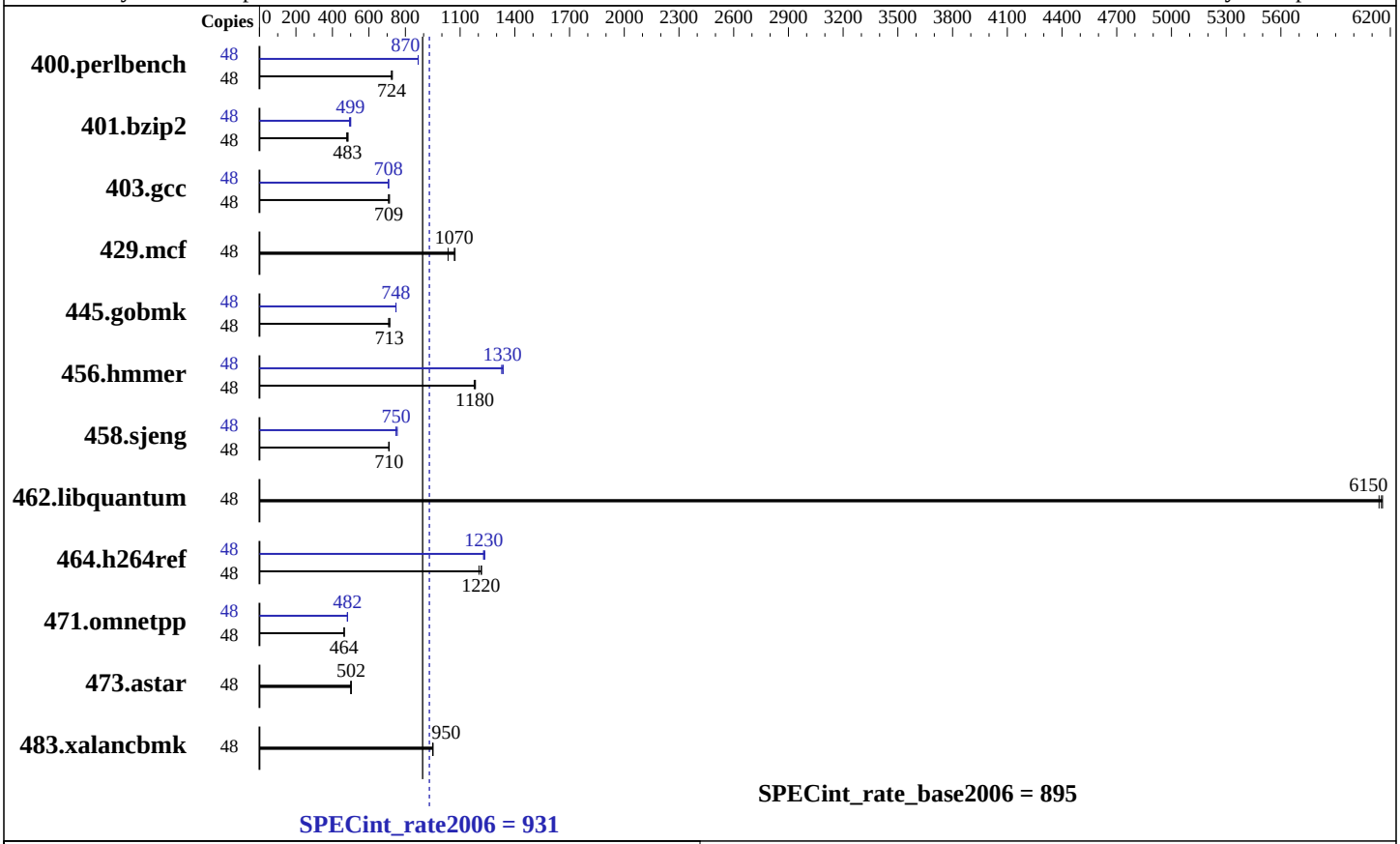
SuperServer 1027TR-TF
(X9DRT-F, Intel Xeon E5-2697 v2)

SPECint®_rate2006 = 931

SPECint_rate_base2006 = 895

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2697 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 x 400 GB SATA III, SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.18.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	644	729	**648**	**724**	648	724	48	**539**	**870**	539	870	539	871
401.bzip2	48	954	486	**958**	**483**	968	478	48	**929**	**499**	939	493	926	500
403.gcc	48	546	708	542	713	**545**	**709**	48	**546**	**708**	547	706	546	708
429.mcf	48	423	1030	**410**	**1070**	408	1070	48	423	1030	**410**	**1070**	408	1070
445.gobmk	48	711	708	**707**	**713**	705	715	48	673	748	**673**	**748**	673	749
456.hmmer	48	**379**	**1180**	380	1180	378	1180	48	337	1330	**336**	**1330**	335	1340
458.sjeng	48	816	711	**818**	**710**	820	709	48	**775**	**750**	769	756	776	749
462.libquantum	48	162	6160	**162**	**6150**	162	6140	48	162	6160	**162**	**6150**	162	6140
464.h264ref	48	**874**	**1220**	872	1220	883	1200	48	**864**	**1230**	860	1240	864	1230
471.omnetpp	48	646	464	646	464	**646**	**464**	48	621	483	**622**	**482**	622	482
473.astar	48	**671**	**502**	674	500	670	503	48	**671**	**502**	674	500	670	503
483.xalancbmk	48	349	949	348	952	**349**	**950**	48	349	949	348	952	**349**	**950**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on X9-CPU2006-2 Thu Sep 12 14:30:53 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
```

Continued on next page

Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
     cache size : 30720 KB

  From /proc/meminfo
     MemTotal:       66000788 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux X9-CPU2006-2 2.6.32-358.18.1.el6.x86_64 #1 SMP Wed Aug 28 09:02:47 CEST
     2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Sep 12 13:58

  SPEC is set to: /home/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_x9cpu20062-lv_home
                    ext4    287G  4.2G  268G   2% /home

  Additional information from dmidecode:
    BIOS American Megatrends Inc. 3.0 09/11/2013
    Memory:
     1x   16 MB
     8x   8 GB
     8x Hynix Semiconductor HMT31GR7CFR4C-RD 8 GB 1866 MHz 1 rank
     1x Micron/Numonyx 25Q Series 16 MB 33 MHz

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 16:48:55 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 November 2013.