SPEC® CINT2006 Result
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Supermicro

SuperServer 6017TR-TQF (X9DRT-IBQF,
Intel Xeon E5-2667, 2.90 GHz)

SPECint®_rate2006 = 520

SPECint_rate_base2006 = 504

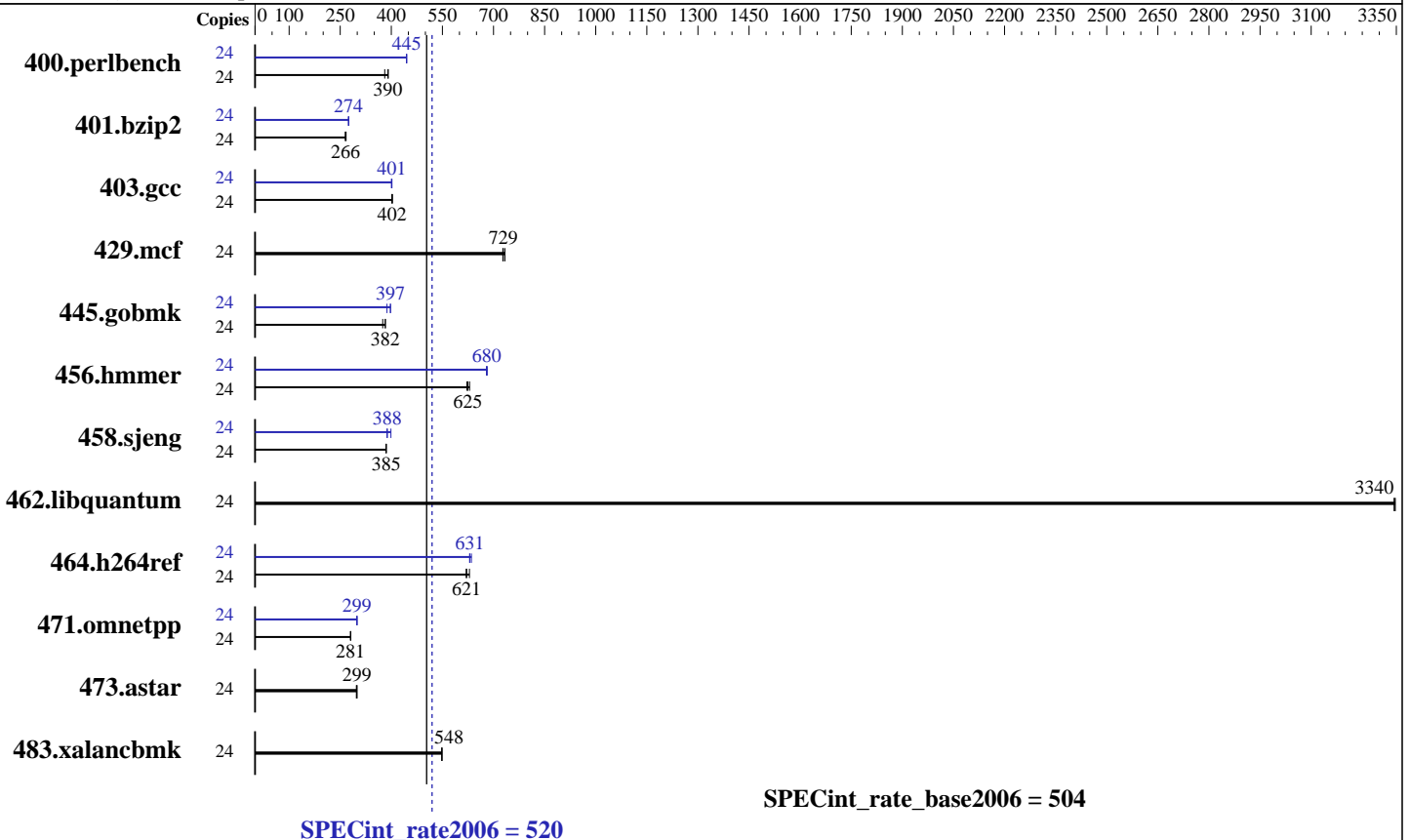|	
CPU2006 license: 001176	**Test date:** Apr-2013
Test sponsor: Supermicro	**Hardware Availability:** Mar-2012
Tested by: Supermicro	**Software Availability:** Oct-2012

Copies

Benchmark	Copies	Peak	Base
400.perlbench	24	445	390
401.bzip2	24	274	266
403.gcc	24	401	402
429.mcf	24		729
445.gobmk	24	397	382
456.hmmer	24	680	625
458.sjeng	24	388	385
462.libquantum	24		3340
464.h264ref	24	631	621
471.omnetpp	24	299	281
473.astar	24		299
483.xalancbmk	24		548

SPECint_rate_base2006 = 504

SPECint_rate2006 = 520

Hardware

CPU Name:	Intel Xeon E5-2667
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 1 TB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 13.0.0.133 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**601**	**390**	600	391	616	381	24	527	445	**527**	**445**	528	444
401.bzip2	24	**872**	**266**	874	265	867	267	24	842	275	**844**	**274**	844	274
403.gcc	24	**480**	**402**	481	402	479	403	24	481	402	**482**	**401**	483	400
429.mcf	24	**300**	**729**	298	733	301	727	24	**300**	**729**	298	733	301	727
445.gobmk	24	**658**	**382**	658	382	672	375	24	650	387	633	398	**634**	**397**
456.hmmer	24	356	630	360	622	**358**	**625**	24	330	679	329	681	**329**	**680**
458.sjeng	24	**755**	**385**	754	385	755	385	24	**748**	**388**	728	399	750	387
462.libquantum	24	149	3340	149	3350	**149**	**3340**	24	149	3340	149	3350	**149**	**3340**
464.h264ref	24	857	620	844	629	**855**	**621**	24	843	630	**842**	**631**	836	635
471.omnetpp	24	**535**	**281**	534	281	536	280	24	502	299	501	299	**501**	**299**
473.astar	24	**564**	**299**	563	299	567	297	24	**564**	**299**	563	299	567	297
483.xalancbmk	24	302	548	**302**	**548**	302	549	24	302	548	**302**	**548**	302	549

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RHEL5.5
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    Filesystem page cache cleared with:
    echo 1>   /proc/sys/vm/drop_caches
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

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CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2013
Hardware Availability: Mar-2012
Software Availability: Oct-2012

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

    401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias

      403.gcc: -xAVX -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

     458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32

462.libquantum: basepeak = yes

    464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/sh -lsmartheap

    473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
```

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Test sponsor: Supermicro
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Test date: Apr-2013
Hardware Availability: Mar-2012
Software Availability: Oct-2012

Peek Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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