



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = 577

Servidor Itaotec MX215 (Intel Xeon E5-2660)

SPECint_rate_base2006 = 561

CPU2006 license: 9001

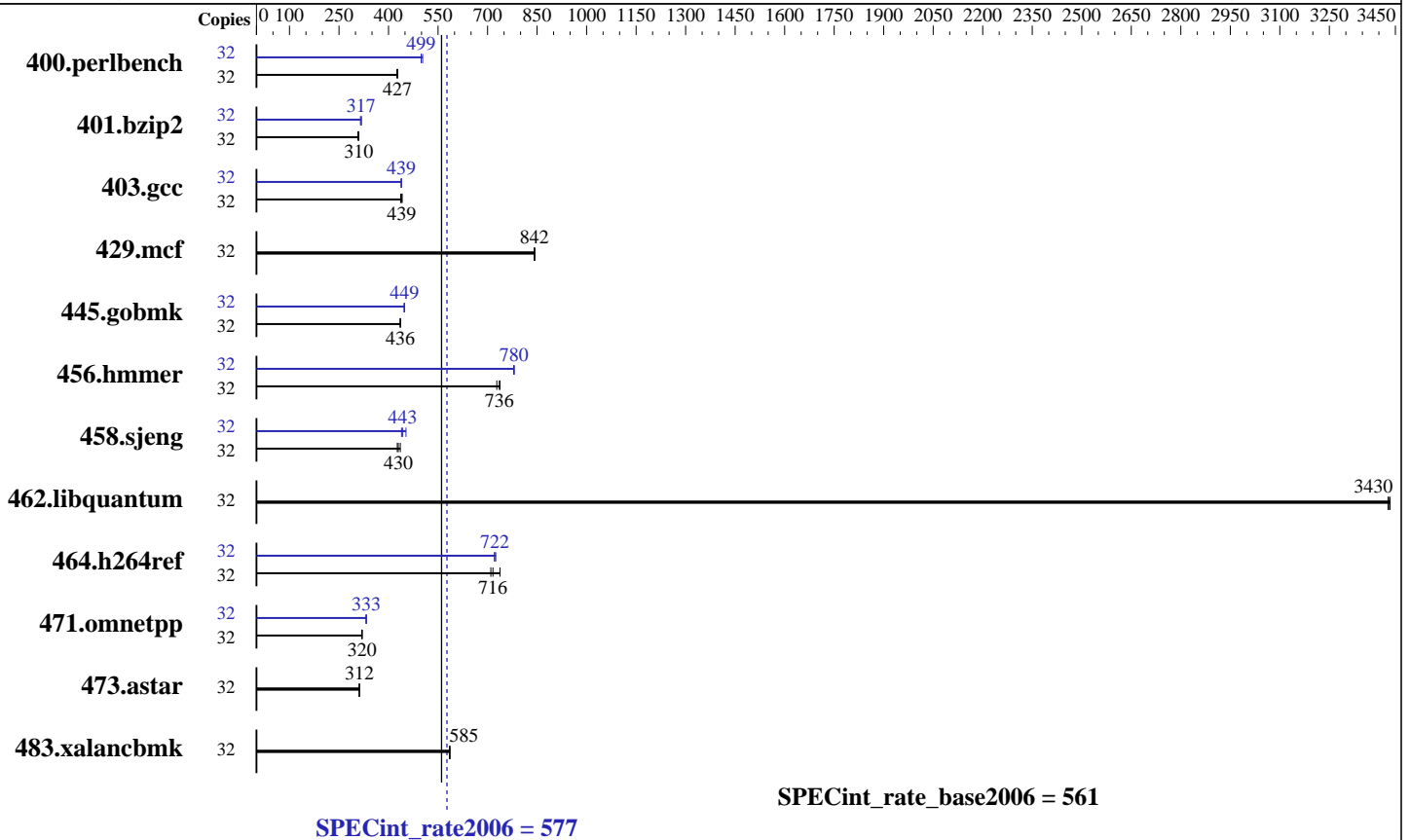
Test date: Mar-2013

Test sponsor: Itaotec

Hardware Availability: Jun-2012

Tested by: Itaotec

Software Availability: Jan-2013



Hardware

CPU Name: Intel Xeon E5-2660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 112 GB (14 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 500 GB, SATA-3, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86_64
 Compiler: C/C++: Version 13.1.0 of Intel Compiler XE Build 20130121
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	<u>732</u>	<u>427</u>	730	428	735	425	32	<u>626</u>	<u>499</u>	621	503	627	499
401.bzip2	32	<u>997</u>	<u>310</u>	996	310	1005	307	32	972	318	<u>974</u>	<u>317</u>	980	315
403.gcc	32	584	441	590	437	<u>586</u>	<u>439</u>	32	587	439	<u>587</u>	<u>439</u>	588	438
429.mcf	32	<u>347</u>	<u>842</u>	347	841	346	843	32	<u>347</u>	<u>842</u>	347	841	346	843
445.gobmk	32	770	436	771	436	<u>770</u>	<u>436</u>	32	748	449	<u>748</u>	<u>449</u>	751	447
456.hammer	32	410	728	405	738	<u>406</u>	<u>736</u>	32	382	781	383	779	<u>383</u>	<u>780</u>
458.sjeng	32	889	436	908	427	<u>901</u>	<u>430</u>	32	<u>874</u>	<u>443</u>	855	453	881	439
462.libquantum	32	<u>193</u>	<u>3430</u>	193	3430	193	3430	32	<u>193</u>	<u>3430</u>	193	3430	193	3430
464.h264ref	32	998	710	960	738	<u>989</u>	<u>716</u>	32	977	725	982	721	<u>981</u>	<u>722</u>
471.omnetpp	32	626	320	<u>625</u>	<u>320</u>	625	320	32	<u>601</u>	<u>333</u>	602	332	601	333
473.astar	32	720	312	<u>721</u>	<u>312</u>	721	311	32	720	312	<u>721</u>	<u>312</u>	721	311
483.xalancbmk	32	377	586	<u>377</u>	<u>585</u>	378	585	32	377	586	<u>377</u>	<u>585</u>	378	585

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on mx225 Sat Mar 23 07:12:45 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Genuine Intel(R) CPU @ 2.20GHz
2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16

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Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      115557980 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.3 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux mx225 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 21 15:14
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_mx225-lv_home
                ext4      404G  4.2G  380G   2% /home
```

(End of data from sysinfo program)

General Notes

This result was measured on the Servidor Itautec MX225.
The Servidor Itautec MX215 and the Servidor Itautec MX225
are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.html>

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.xml>

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml

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For other inquiries, please contact webmaster@spec.org.

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