



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>\_rate2006 = 340

### Huawei RH2288 V2 (Intel Xeon E5-2620)

SPECfp\_rate\_base2006 = 336

CPU2006 license: 3175

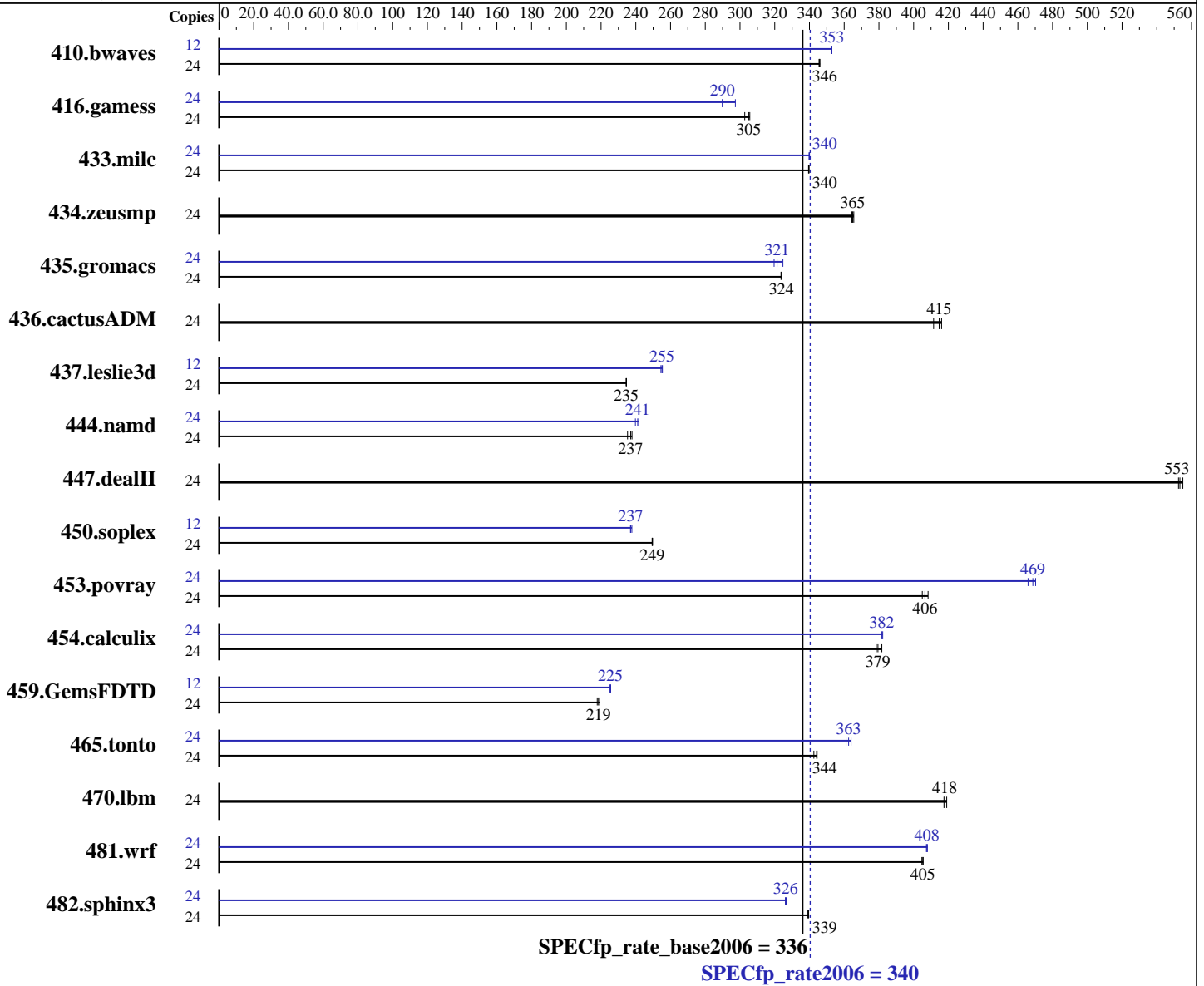
Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2012



#### Hardware

CPU Name: Intel Xeon E5-2620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 13.0.0.079 of Intel C++ Studio XE for Linux;  
 Fortran: Version 13.0.0.079 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<u>943</u>	<u>346</u>	944	346	943	346	12	462	353	<u>462</u>	<u>353</u>	462	353
416.gamess	24	1552	303	<u>1541</u>	<u>305</u>	1537	306	24	1580	297	<u>1621</u>	<u>290</u>	1621	290
433.milc	24	649	339	<u>649</u>	<u>340</u>	648	340	24	648	340	<u>648</u>	<u>340</u>	648	340
434.zeusmp	24	599	364	597	366	<u>599</u>	<u>365</u>	24	599	364	597	366	<u>599</u>	<u>365</u>
435.gromacs	24	<u>529</u>	<u>324</u>	530	324	529	324	24	<u>533</u>	<u>321</u>	528	325	536	320
436.cactusADM	24	697	411	689	416	<u>692</u>	<u>415</u>	24	697	411	689	416	<u>692</u>	<u>415</u>
437.leslie3d	24	<u>962</u>	<u>235</u>	961	235	962	234	12	<u>442</u>	<u>255</u>	442	255	443	254
444.namd	24	818	235	809	238	<u>812</u>	<u>237</u>	24	<u>798</u>	<u>241</u>	796	242	803	240
447.dealII	24	495	555	497	552	<u>496</u>	<u>553</u>	24	495	555	497	552	<u>496</u>	<u>553</u>
450.soplex	24	803	249	<u>802</u>	<u>249</u>	802	250	12	<u>422</u>	<u>237</u>	422	237	421	238
453.povray	24	<u>314</u>	<u>406</u>	315	405	313	408	24	274	466	<u>272</u>	<u>469</u>	272	470
454.calculix	24	519	382	523	378	<u>522</u>	<u>379</u>	24	519	381	518	382	<u>519</u>	<u>382</u>
459.GemsFDTD	24	1162	219	1169	218	<u>1165</u>	<u>219</u>	12	565	225	564	226	<u>565</u>	<u>225</u>
465.tonto	24	686	344	<u>686</u>	<u>344</u>	689	343	24	654	361	<u>651</u>	<u>363</u>	649	364
470.lbm	24	787	419	<u>789</u>	<u>418</u>	790	417	24	787	419	<u>789</u>	<u>418</u>	790	417
481.wrf	24	<u>662</u>	<u>405</u>	661	405	662	405	24	657	408	658	407	<u>658</u>	<u>408</u>
482.sphinx3	24	1378	340	<u>1379</u>	<u>339</u>	1379	339	24	1432	327	1434	326	<u>1434</u>	<u>326</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches  
 runspec command invoked through numactl i.e.:  
 numactl --interleave=all runspec <etc>  
 Select only test related files when installing the operating system



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## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on huawei-rh2288 Sat Mar 16 08:45:40 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 6

siblings : 12

physical 0: cores 0 1 2 3 4 5

physical 1: cores 0 1 2 3 4 5

cache size : 15360 KB

From /proc/meminfo

MemTotal: 132124016 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux huawei-rh2288 2.6.32-220.el6.x86\_64 #1 SMP Wed Nov 9 08:03:13 EST 2011  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Mar 15 18:22

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	99G	45G	49G	48%	/

Additional information from dmidecode:

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory  
using RHEL 6.1

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static  
-unroll2

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-opt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo -O3 -no-prec-div  
-prof-use(pass 2) -xSSE4.2 -opt-prefetch -static  
-auto-ilp32 -opt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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