



# SPEC® CINT2006 Result

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## ACTION S.A.

SPECint®\_rate2006 = 294

ACTINA SOLAR 110 X5+ (Intel Xeon E5-1660)

SPECint\_rate\_base2006 = 282

CPU2006 license: 9008

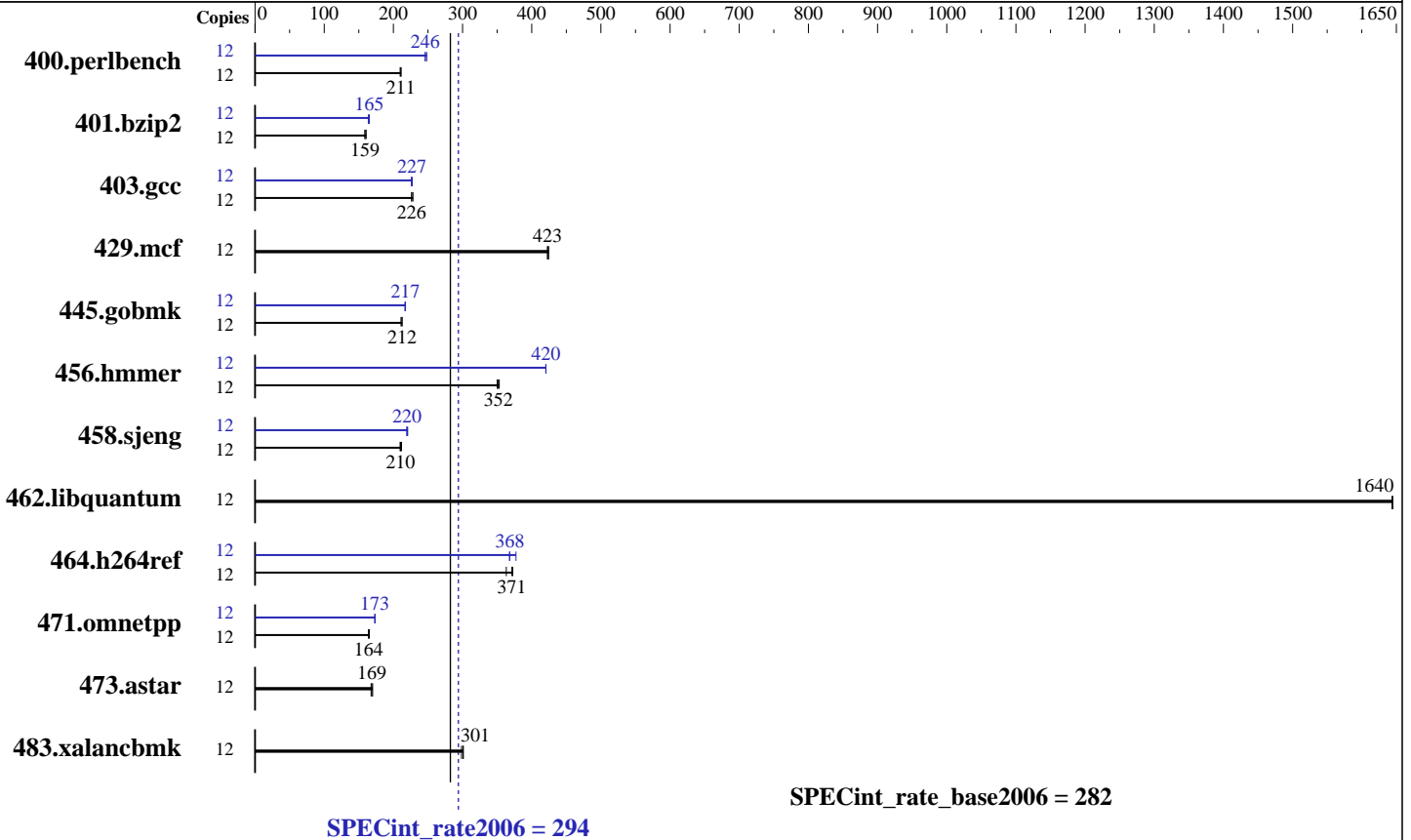
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Nov-2012

Hardware Availability: Mar-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-1660  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 2 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

| Benchmark      | Base   |         |       |            |            |            |             | Peak   |            |            |            |            |            |             |
|----------------|--------|---------|-------|------------|------------|------------|-------------|--------|------------|------------|------------|------------|------------|-------------|
|                | Copies | Seconds | Ratio | Seconds    | Ratio      | Seconds    | Ratio       | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds    | Ratio       |
| 400.perlbench  | 12     | 559     | 210   | 556        | 211        | <b>556</b> | <b>211</b>  | 12     | 472        | 248        | <b>476</b> | <b>246</b> | 477        | 246         |
| 401.bzip2      | 12     | 721     | 161   | 729        | 159        | <b>728</b> | <b>159</b>  | 12     | 701        | 165        | 707        | 164        | <b>702</b> | <b>165</b>  |
| 403.gcc        | 12     | 423     | 228   | 427        | 226        | <b>426</b> | <b>226</b>  | 12     | 427        | 226        | <b>426</b> | <b>227</b> | 425        | 227         |
| 429.mcf        | 12     | 259     | 423   | 258        | 425        | <b>259</b> | <b>423</b>  | 12     | 259        | 423        | 258        | 425        | <b>259</b> | <b>423</b>  |
| 445.gobmk      | 12     | 592     | 213   | 597        | 211        | <b>592</b> | <b>212</b>  | 12     | 581        | 217        | 579        | 217        | <b>579</b> | <b>217</b>  |
| 456.hammer     | 12     | 320     | 350   | <b>318</b> | <b>352</b> | 317        | 353         | 12     | <b>266</b> | <b>420</b> | 266        | 420        | 266        | 421         |
| 458.sjeng      | 12     | 692     | 210   | <b>690</b> | <b>210</b> | 686        | 212         | 12     | 662        | 219        | <b>660</b> | <b>220</b> | 658        | 221         |
| 462.libquantum | 12     | 151     | 1640  | 151        | 1640       | <b>151</b> | <b>1640</b> | 12     | 151        | 1640       | 151        | 1640       | <b>151</b> | <b>1640</b> |
| 464.h264ref    | 12     | 713     | 373   | <b>715</b> | <b>371</b> | 731        | 363         | 12     | <b>721</b> | <b>368</b> | 722        | 368        | 704        | 377         |
| 471.omnetpp    | 12     | 454     | 165   | <b>456</b> | <b>164</b> | 457        | 164         | 12     | 432        | 173        | <b>432</b> | <b>173</b> | 433        | 173         |
| 473.astar      | 12     | 496     | 170   | <b>499</b> | <b>169</b> | 502        | 168         | 12     | 496        | 170        | <b>499</b> | <b>169</b> | 502        | 168         |
| 483.xalancbmk  | 12     | 275     | 301   | 277        | 298        | <b>275</b> | <b>301</b>  | 12     | 275        | 301        | 277        | 298        | <b>275</b> | <b>301</b>  |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on linux-ynxt Mon Nov 12 17:29:54 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-1660 0 @ 3.30GHz  
1 "physical id"s (chips)  
12 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12

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## Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      65889996 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux linux-ynxt 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 12 17:00 last=S
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  1.8T  7.4G  1.8T   1% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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