



# SPEC® CINT2006 Result

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## IBM Corporation

SPECint®\_rate2006 = 418

IBM System x3650 M4 (Intel Xeon E5-2648L)

SPECint\_rate\_base2006 = 400

CPU2006 license: 11

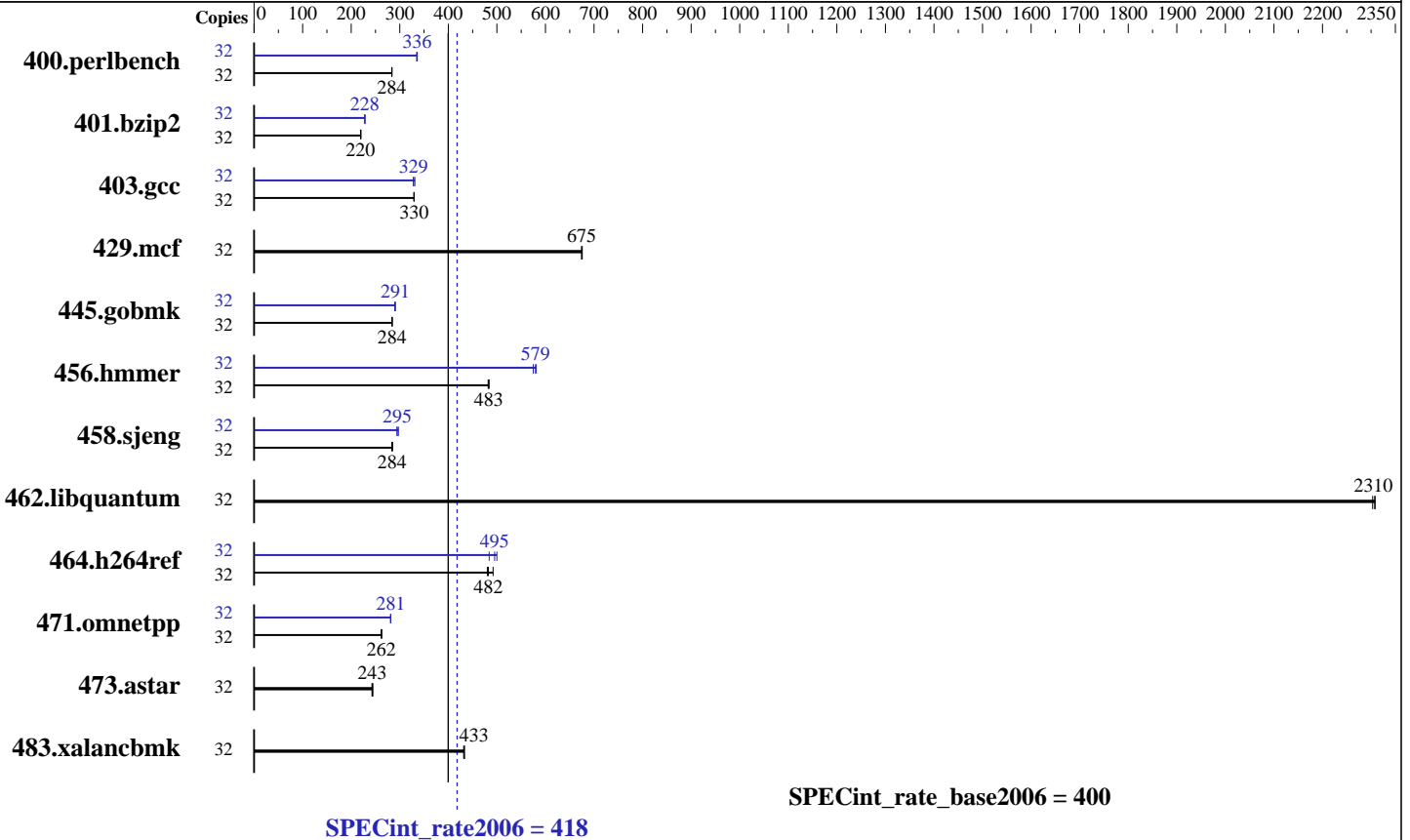
Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012



### Hardware

CPU Name: Intel Xeon E5-2648L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1102	284	1101	284	<b><u>1102</u></b>	<b><u>284</u></b>	32	<b><u>932</u></b>	<b><u>336</u></b>	930	336	934	335
401.bzip2	32	1409	219	1403	220	<b><u>1407</u></b>	<b><u>220</u></b>	32	<b><u>1354</u></b>	<b><u>228</u></b>	1353	228	1354	228
403.gcc	32	781	330	783	329	<b><u>782</u></b>	<b><u>330</u></b>	32	<b><u>784</u></b>	<b><u>329</u></b>	778	331	785	328
429.mcf	32	433	674	<b><u>432</u></b>	<b><u>675</u></b>	432	676	32	433	674	<b><u>432</u></b>	<b><u>675</u></b>	432	676
445.gobmk	32	1180	284	<b><u>1180</u></b>	<b><u>284</u></b>	1182	284	32	<b><u>1155</u></b>	<b><u>291</u></b>	1158	290	1154	291
456.hammer	32	617	484	<b><u>618</u></b>	<b><u>483</u></b>	619	482	32	<b><u>515</u></b>	<b><u>579</u></b>	514	581	519	575
458.sjeng	32	1357	285	<b><u>1362</u></b>	<b><u>284</u></b>	1366	284	32	<b><u>1314</u></b>	<b><u>295</u></b>	1316	294	1303	297
462.libquantum	32	288	2300	287	2310	<b><u>287</u></b>	<b><u>2310</u></b>	32	288	2300	287	2310	<b><u>287</u></b>	<b><u>2310</u></b>
464.h264ref	32	<b><u>1468</u></b>	<b><u>482</u></b>	1474	480	1439	492	32	1416	500	1463	484	<b><u>1430</u></b>	<b><u>495</u></b>
471.omnetpp	32	<b><u>762</u></b>	<b><u>262</u></b>	761	263	763	262	32	712	281	<b><u>711</u></b>	<b><u>281</u></b>	710	282
473.astar	32	925	243	918	245	<b><u>923</u></b>	<b><u>243</u></b>	32	925	243	918	245	<b><u>923</u></b>	<b><u>243</u></b>
483.xalancbmk	32	510	433	<b><u>510</u></b>	<b><u>433</u></b>	512	432	32	510	433	<b><u>510</u></b>	<b><u>433</u></b>	512	432

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on x3650M4 Fri Nov 9 12:47:13 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2648L 0 @ 1.80GHz  
2 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The  
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## Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

From /proc/meminfo

```
MemTotal:      132107616 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.3 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux x3650M4 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 9 12:39

SPEC is set to: /home/SPECcpu-v1.2

```
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_x3650m4-lv_home
                ext4      221G  4.3G  205G   3% /home
```

Additional information from dmidecode:

```
Memory:
16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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