



# SPEC<sup>®</sup> CINT2006 Result

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Acer Incorporated  
Acer AR580 F2 (Xeon E5-4650)

SPECint<sup>®</sup>\_rate2006 = **NC**  
SPECint\_rate\_base2006 = **NC**

CPU2006 license: 97  
Test sponsor: Acer Incorporated  
Tested by: Acer Incorporated

Test date: Sep-2012  
Hardware Availability: Dec-2012  
Software Availability: Dec-2011

**SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result used pre-production hardware and the production hardware would reduce production system performance by more than 1.75%.  
Acer will republish these results with production hardware.**

Copies  
400.perlbench  
401.bzip2  
403.gcc  
429.mcf  
445.gobmk  
456.hmmer  
458.sjeng  
462.libquantum  
464.h264ref  
471.omnetpp  
473.astar  
483

**Non-Compliant**

### Hardware

CPU Name: Intel Xeon E5-4650  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2700  
FPU: Integrated  
CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
CPU(s) orderable: 2,4 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
2.6.32-279.el6.x86\_64  
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
Auto Parallel: No  
File System: ext4  
System State: Run level 3 (multi-user)

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Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
Disk Subsystem: 1 x 300 GB SAS, 15K RPM  
Other Hardware: None

Base Processors: 32-bit  
Link Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V9.01

## Result Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
401.bzip2	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
403.gcc	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
429.mcf	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
445.gobmk	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
456.hmmer	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
458.sjeng	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
462.libquantum	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
464.h264ref	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
471.omnetpp	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
473.astar	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
483.xs	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.



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## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2e...5032aaa42e583f96b07f99d3  
running on spec Sat Sep 22 17:32:31 2012

This section contains SUT (system Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/config.html#sysinfo>

From /proc/cpuinfo  
model name : Genuine Intel(R) CPU @ 2.70GHz  
4 "physical id" (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpt from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 1  
siblings : 1  
physical 0: cores 0 1 2 3 4 5 6 7  
physical 1: cores 0 1 2 3 4 5 6 7  
physical 2: cores 0 1 2 3 4 5 6 7  
physical 3: cores 0 1 2 3 4 5 6 7  
cache size : 20480 KB

From /proc/meminfo  
MemTotal: 264591176 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)

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## Platform Notes (Continued)

```
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
uname -a:
Linux spec 2.6.32-279.el6.x86_64 SMP Wed Jun 13 18:24:36 EDT 2012 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Sep 21 19:10

SPEC is set to: /usr/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       ext4      152G      89G      56G   62% /

Additional information from dmidecode:
Memory:
32x Hynix HMT31GR7CER4C-PB 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

```
C benchmarks:
icc -m32
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -lsmartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-mem-layout-trans=3 -region-strategy=block -Wl,-z,muldefs  
-lsmartheap -lsmartheap

473.aspec: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML file source by using the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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