



# SPEC® CINT2006 Result

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## NEC Corporation

**SPECint®\_rate2006 = 98.1**

Express5800/GT110e-S (Intel Core i3-3240)

**SPECint\_rate\_base2006 = 93.3**

CPU2006 license: 9006

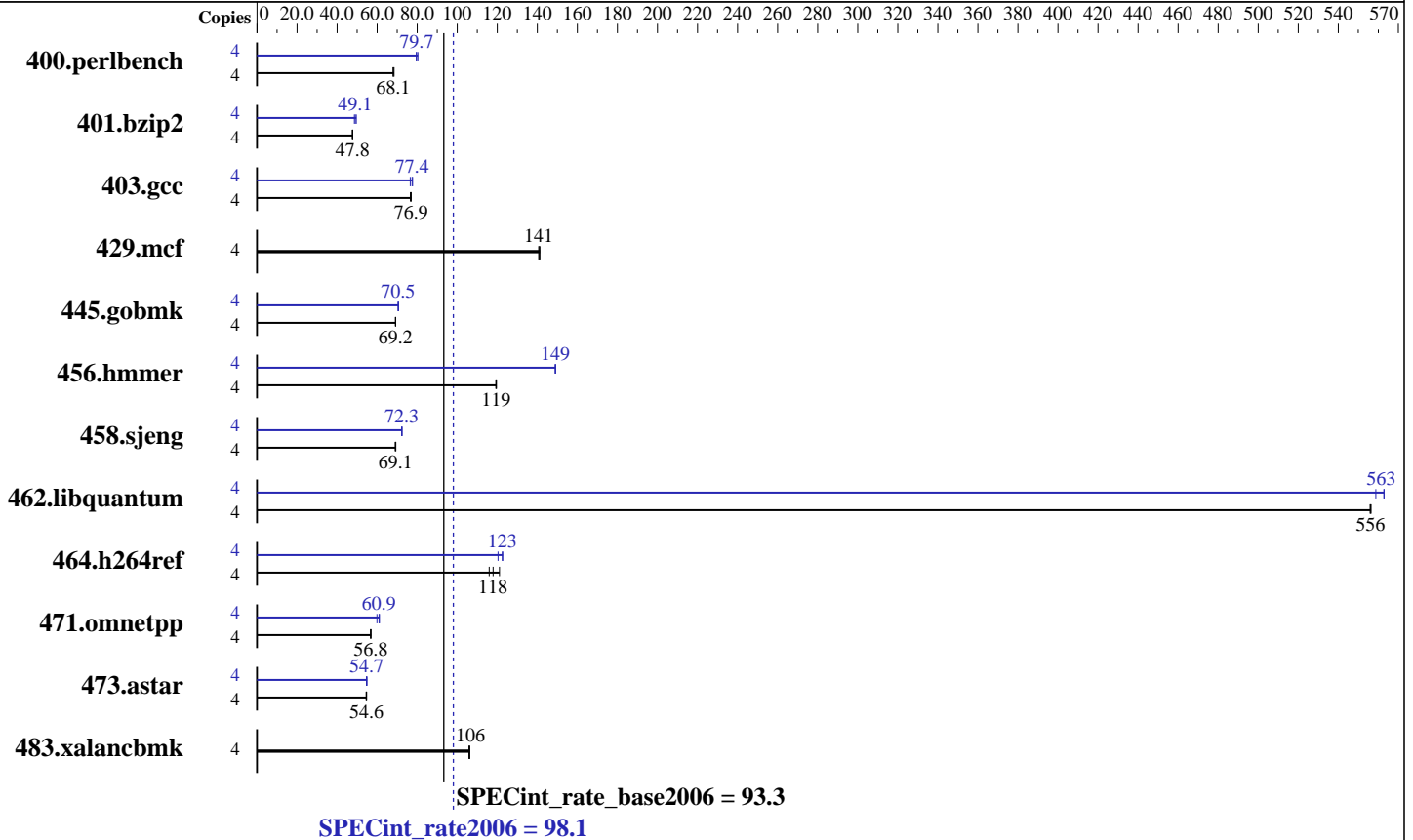
Test date: Oct-2012

Test sponsor: NEC Corporation

Hardware Availability: Oct-2012

Tested by: NEC Corporation

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Core i3-3240  
 CPU Characteristics:  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux;  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	575	68.0	571	68.4	<b>574</b>	<b>68.1</b>	4	491	79.6	486	80.3	<b>490</b>	<b>79.7</b>
401.bzip2	4	<b>808</b>	<b>47.8</b>	812	47.6	808	47.8	4	<b>786</b>	<b>49.1</b>	794	48.6	778	49.6
403.gcc	4	<b>419</b>	<b>76.9</b>	420	76.6	418	77.0	4	414	77.7	<b>416</b>	<b>77.4</b>	421	76.5
429.mcf	4	259	141	258	141	<b>259</b>	<b>141</b>	4	259	141	258	141	<b>259</b>	<b>141</b>
445.gobmk	4	607	69.1	606	69.3	<b>606</b>	<b>69.2</b>	4	<b>595</b>	<b>70.5</b>	594	70.6	595	70.5
456.hammer	4	<b>313</b>	<b>119</b>	313	119	312	120	4	<b>251</b>	<b>149</b>	251	149	250	149
458.sjeng	4	<b>700</b>	<b>69.1</b>	701	69.1	698	69.3	4	668	72.4	<b>669</b>	<b>72.3</b>	670	72.3
462.libquantum	4	149	556	149	556	<b>149</b>	<b>556</b>	4	<b>147</b>	<b>563</b>	147	563	148	559
464.h264ref	4	731	121	763	116	<b>751</b>	<b>118</b>	4	<b>723</b>	<b>123</b>	735	120	721	123
471.omnetpp	4	<b>440</b>	<b>56.8</b>	441	56.7	439	56.9	4	409	61.1	<b>410</b>	<b>60.9</b>	417	59.9
473.astar	4	513	54.8	<b>514</b>	<b>54.6</b>	515	54.5	4	<b>513</b>	<b>54.7</b>	511	54.9	514	54.7
483.xalancbmk	4	261	106	260	106	<b>260</b>	<b>106</b>	4	261	106	260	106	<b>260</b>	<b>106</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Default BIOS settings were used.

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Added glibc-static-2.12-1.47.el6.x86\_64.rpm to enable static linking

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch  
 -opt-mem-layout-trans=3

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch  
 -opt-mem-layout-trans=3 -Wl,-z,muldefs  
 -L/opt/SmartHeap\_8.1/lib -lsmarheap

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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