



# SPEC® CFP2006 Result

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## IBM Corporation

IBM System x3100 M4  
(Intel Pentium G540T, 2.10 GHz)

SPECfp®\_rate2006 = 49.2

SPECfp\_rate\_base2006 = 48.4

CPU2006 license: 11

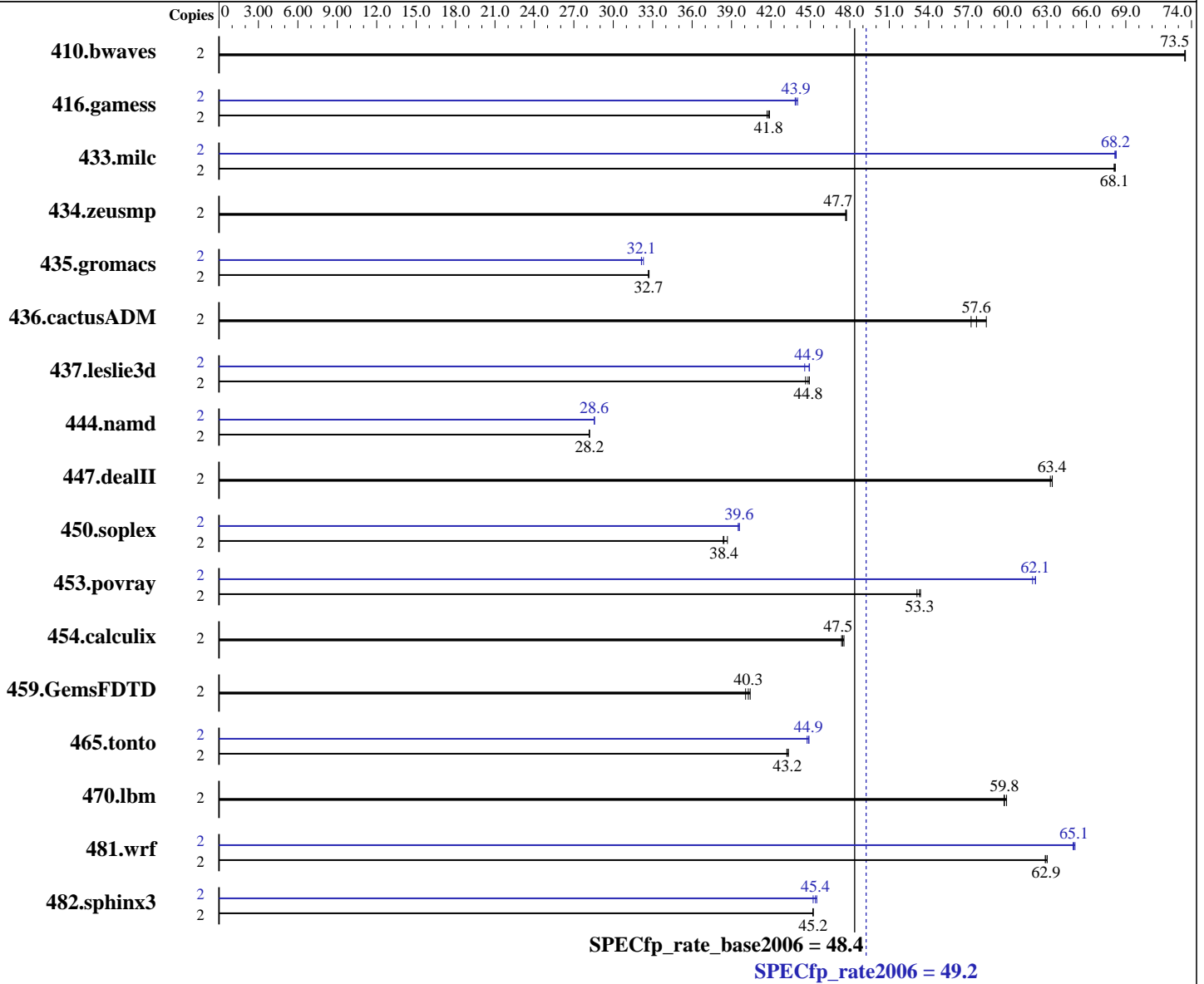
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Pentium G540T  
 CPU Characteristics:  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 2 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1066 MHz)  
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	<b>370</b>	<b>73.5</b>	370	73.5	370	73.5	2	<b>370</b>	<b>73.5</b>	370	73.5	370	73.5
416.gamess	2	<b>936</b>	<b>41.8</b>	939	41.7	935	41.9	2	893	43.8	<b>892</b>	<b>43.9</b>	889	44.0
433.milc	2	270	68.1	<b>269</b>	<b>68.1</b>	269	68.2	2	<b>269</b>	<b>68.2</b>	269	68.2	269	68.3
434.zeusmp	2	381	47.8	<b>382</b>	<b>47.7</b>	382	47.7	2	381	47.8	<b>382</b>	<b>47.7</b>	382	47.7
435.gromacs	2	<b>437</b>	<b>32.7</b>	436	32.7	437	32.7	2	442	32.3	<b>444</b>	<b>32.1</b>	444	32.1
436.cactusADM	2	409	58.4	<b>415</b>	<b>57.6</b>	418	57.2	2	409	58.4	<b>415</b>	<b>57.6</b>	418	57.2
437.leslie3d	2	421	44.6	<b>419</b>	<b>44.8</b>	418	44.9	2	418	44.9	<b>419</b>	<b>44.9</b>	422	44.6
444.namd	2	569	28.2	<b>569</b>	<b>28.2</b>	570	28.2	2	<b>562</b>	<b>28.6</b>	562	28.6	561	28.6
447.dealII	2	361	63.4	<b>361</b>	<b>63.4</b>	362	63.3	2	361	63.4	<b>361</b>	<b>63.4</b>	362	63.3
450.soplex	2	<b>434</b>	<b>38.4</b>	431	38.7	435	38.4	2	422	39.5	421	39.6	<b>421</b>	<b>39.6</b>
453.povray	2	199	53.4	200	53.1	<b>200</b>	<b>53.3</b>	2	<b>171</b>	<b>62.1</b>	172	61.9	171	62.1
454.calculix	2	347	47.6	<b>348</b>	<b>47.5</b>	348	47.4	2	347	47.6	<b>348</b>	<b>47.5</b>	348	47.4
459.GemsFDTD	2	530	40.1	<b>527</b>	<b>40.3</b>	525	40.4	2	530	40.1	<b>527</b>	<b>40.3</b>	525	40.4
465.tonto	2	454	43.3	<b>455</b>	<b>43.2</b>	455	43.2	2	<b>439</b>	<b>44.9</b>	438	44.9	440	44.7
470.lbm	2	<b>460</b>	<b>59.8</b>	460	59.7	459	59.9	2	<b>460</b>	<b>59.8</b>	460	59.7	459	59.9
481.wrf	2	355	62.9	354	63.0	<b>355</b>	<b>62.9</b>	2	343	65.1	<b>343</b>	<b>65.1</b>	344	65.0
482.sphinx3	2	863	45.2	<b>862</b>	<b>45.2</b>	862	45.2	2	<b>859</b>	<b>45.4</b>	862	45.2	857	45.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Turbo Mode enabled in BIOS  
C-State enabled in BIOS

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### Platform Notes (Continued)

```
Sysinfo program /root/SPECcpul.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Wed Oct 3 01:11:55 2012
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Celeron(R) CPU G540T @ 2.10GHz
1 "physical id"s (chips)
2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 2
siblings : 2
physical 0: cores 0 1
cache size : 2048 KB
```

```
From /proc/meminfo
MemTotal: 16322724 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 2 15:04
```

```
SPEC is set to: /root/SPECcpul.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4 50G 28G 19G 60% /
```

```
Additional information from dmidecode:
Memory:
2x Micron 18JSF1G72AZ-1G6D1 8 GB 1067 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

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## Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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