



SPEC[®] CINT2006 Result

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Itautec

SPECint[®]_rate2006 = 238

Servidor Itautec MX205 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 228

CPU2006 license: 9001

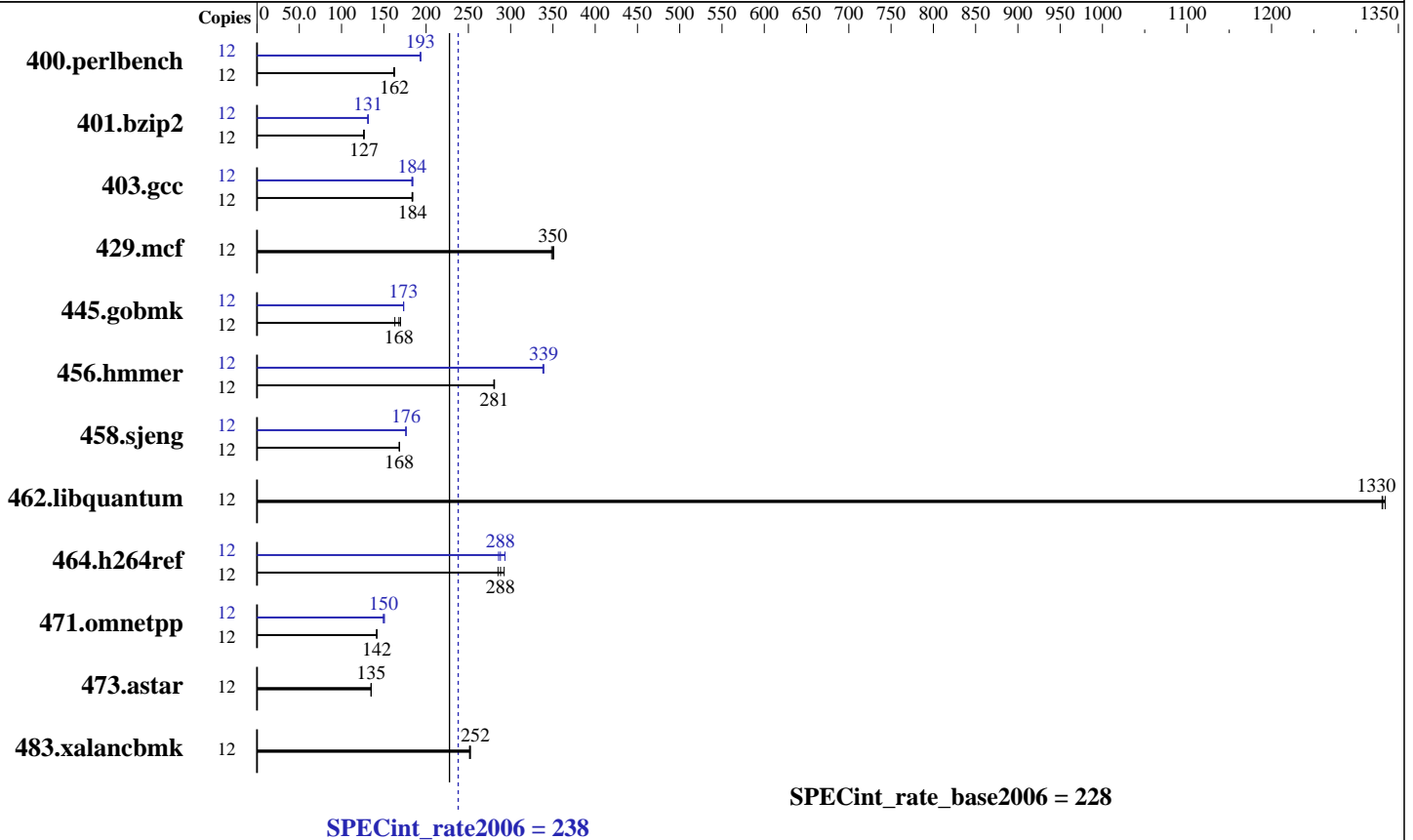
Test date: Jan-2012

Test sponsor: Itautec

Hardware Availability: Jun-2012

Tested by: Itautec

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2640
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86_64
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	<u>722</u>	<u>162</u>	725	162	720	163	12	607	193	<u>606</u>	<u>193</u>	605	194
401.bzip2	12	918	126	914	127	<u>915</u>	<u>127</u>	12	884	131	<u>881</u>	<u>131</u>	880	132
403.gcc	12	527	183	<u>525</u>	<u>184</u>	525	184	12	524	184	<u>526</u>	<u>184</u>	527	183
429.mcf	12	314	348	<u>313</u>	<u>350</u>	312	351	12	314	348	<u>313</u>	<u>350</u>	312	351
445.gobmk	12	772	163	<u>750</u>	<u>168</u>	742	170	12	726	173	<u>726</u>	<u>173</u>	726	173
456.hammer	12	<u>399</u>	<u>281</u>	400	280	399	281	12	330	339	331	338	<u>330</u>	<u>339</u>
458.sjeng	12	<u>862</u>	<u>168</u>	863	168	862	169	12	824	176	<u>824</u>	<u>176</u>	824	176
462.libquantum	12	187	1330	<u>187</u>	<u>1330</u>	186	1330	12	187	1330	<u>187</u>	<u>1330</u>	186	1330
464.h264ref	12	<u>922</u>	<u>288</u>	932	285	908	292	12	905	293	<u>922</u>	<u>288</u>	929	286
471.omnetpp	12	529	142	530	142	<u>530</u>	<u>142</u>	12	501	150	498	151	<u>501</u>	<u>150</u>
473.astar	12	<u>624</u>	<u>135</u>	625	135	623	135	12	<u>624</u>	<u>135</u>	625	135	623	135
483.xalancbmk	12	329	251	<u>329</u>	<u>252</u>	328	252	12	329	251	<u>329</u>	<u>252</u>	328	252

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on itaotec2fam5 Wed Jan 4 17:46:08 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 0 @ 2.50GHz
1 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12

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Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal: 32850880 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux itaotec2fam5 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 4 17:33

SPEC is set to: /home/rcaneca/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_itaotec2fam5-lv_home
ext4 94G 2.1G 87G 3% /home

(End of data from sysinfo program)

General Notes

This result was measured on the Servidor Itaotec MX205.
The Servidor Itaotec LX205, Servidor Itaotec MX205 and the Servidor Itaotec MX225+ are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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