SPEC® CINT2006 Result

Supermicro

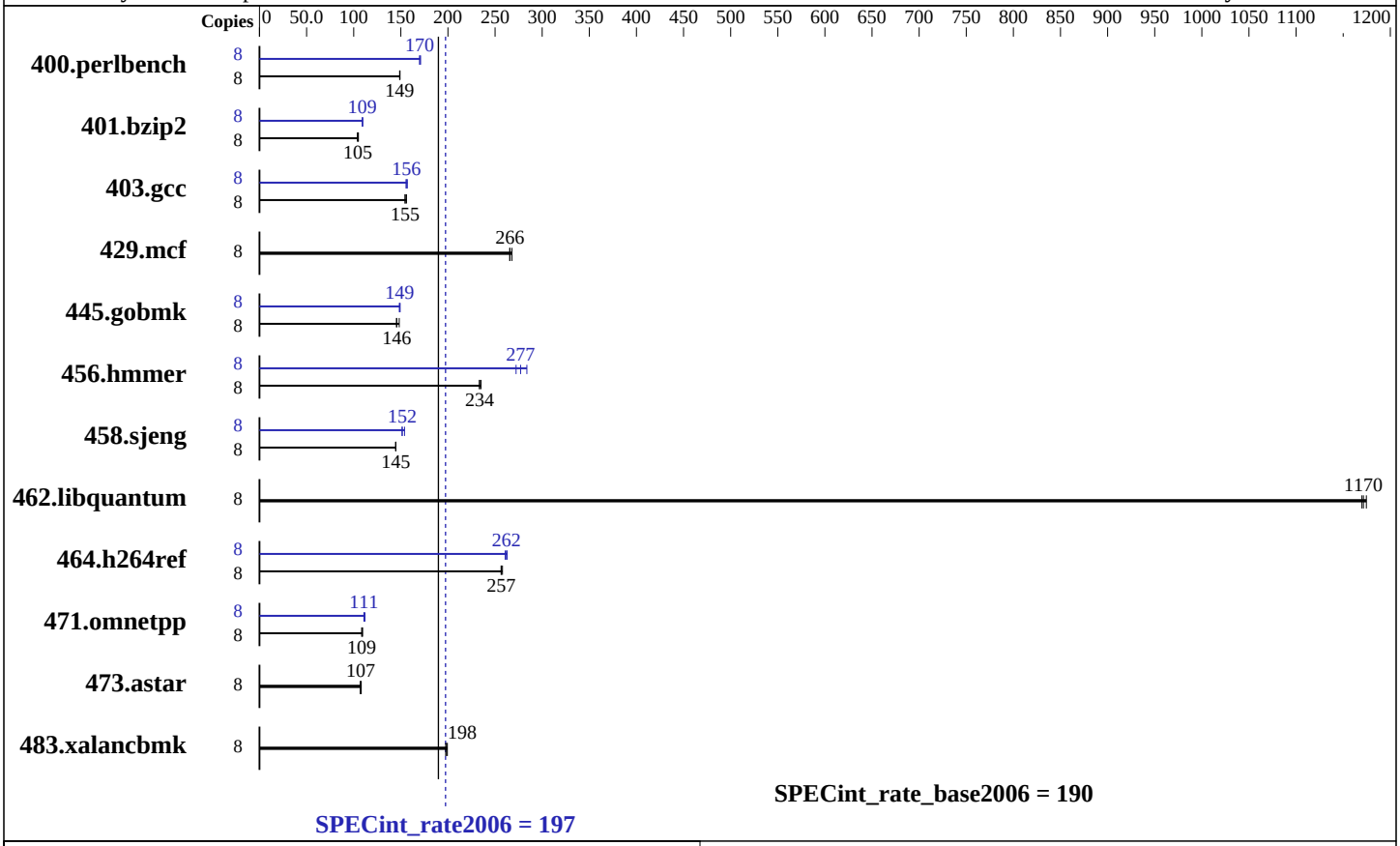
SuperServer 5037A-iL (X9SAE, Intel Xeon E3-1270 v2)

SPECint®_rate2006 = 197

SPECint_rate_base2006 = 190

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2012
Hardware Availability: Apr-2012
Software Availability: Jun-2012



Hardware

CPU Name:	Intel Xeon E3-1270 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 2Rx8 PC3-12800U-11)
Disk Subsystem:	1 x 300 GB SATA II, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.3, Kernel 2.6.32-279.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**525**	**149**	524	149	525	149	8	460	170	457	171	**459**	**170**
401.bzip2	8	**738**	**105**	735	105	743	104	8	705	110	708	109	**706**	**109**
403.gcc	8	413	156	**416**	**155**	417	154	8	410	157	**412**	**156**	414	155
429.mcf	8	275	266	**275**	**266**	272	268	8	275	266	**275**	**266**	272	268
445.gobmk	8	566	148	**576**	**146**	577	145	8	566	148	563	149	**563**	**149**
456.hmmer	8	**319**	**234**	317	235	320	233	8	274	272	263	284	**269**	**277**
458.sjeng	8	670	144	669	145	**669**	**145**	8	640	151	**639**	**152**	629	154
462.libquantum	8	141	1170	142	1170	**141**	**1170**	8	141	1170	142	1170	**141**	**1170**
464.h264ref	8	**689**	**257**	691	256	687	258	8	674	263	679	261	**676**	**262**
471.omnetpp	8	457	109	461	109	**460**	**109**	8	447	112	451	111	**450**	**111**
473.astar	8	520	108	525	107	**523**	**107**	8	520	108	525	107	**523**	**107**
483.xalancbmk	8	279	198	**278**	**198**	277	199	8	279	198	**278**	**198**	277	199

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /usr/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on localhost Mon Sep 24 18:59:55 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1270 V2 @ 3.50GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
```

Continued on next page

Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3
     cache size : 8192 KB

  From /proc/meminfo
     MemTotal:       16412704 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux localhost 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Sep 24 18:55

  SPEC is set to: /usr/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/VolGroup-lv_root
                    ext4     50G   36G   12G  76% /

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`
`-Wl,-z,muldefs -L/smartheap -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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