



# SPEC<sup>®</sup> CINT2006 Result

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Itaotec

SPECint<sup>®</sup>\_rate2006 = 388

Servidor Itaotec MP225 (Intel Xeon E5-2630L)

SPECint\_rate\_base2006 = 371

CPU2006 license: 9001

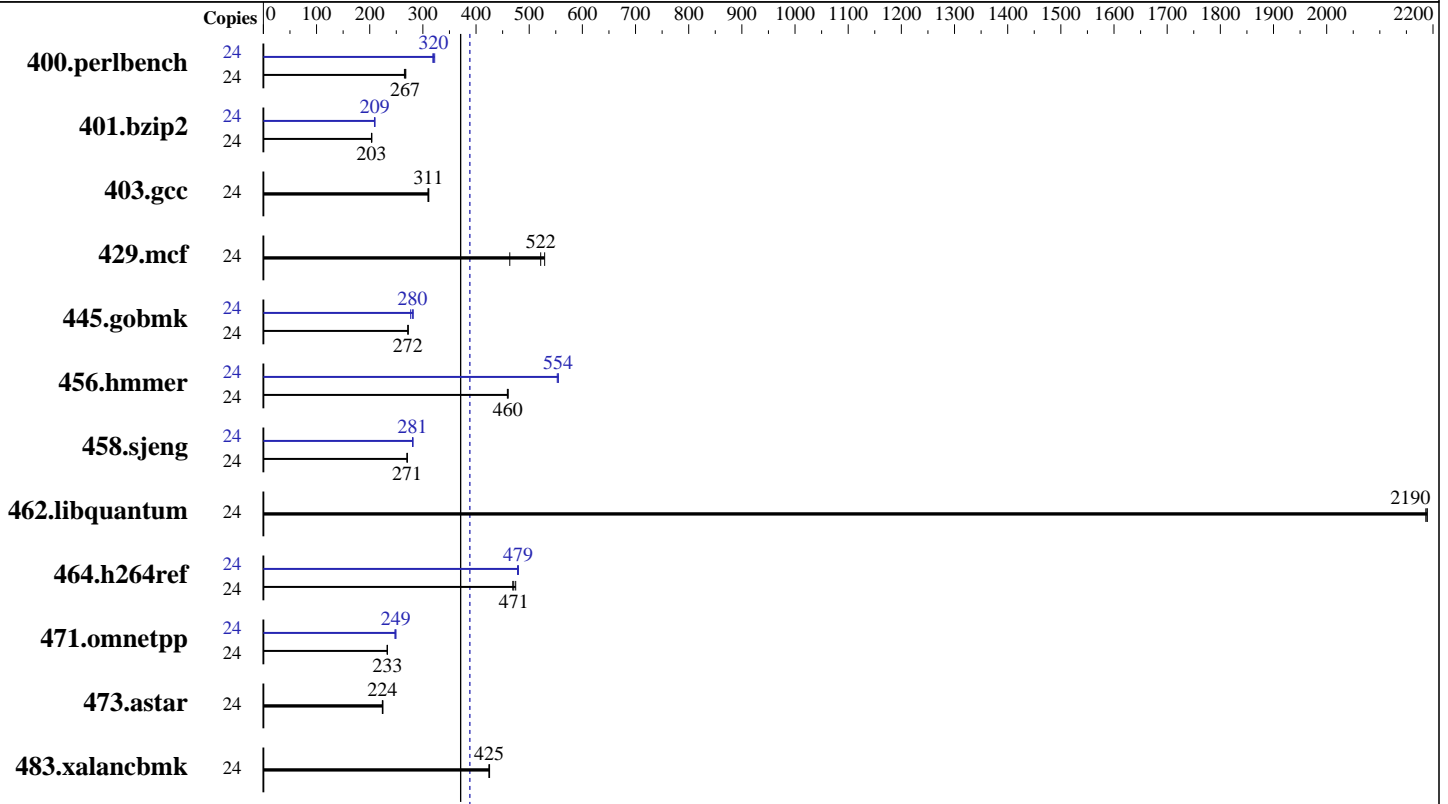
Test date: Sep-2012

Test sponsor: Itaotec

Hardware Availability: Jun-2012

Tested by: Itaotec

Software Availability: Dec-2011



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## Hardware

CPU Name: Intel Xeon E5-2630L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 2 x 146 GB, SAS, 15000 RPM, RAID 1  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	24	875	268	884	265	<b>878</b>	<b>267</b>	24	<b>732</b>	<b>320</b>	735	319	728	322		
401.bzip2	24	1135	204	1139	203	<b>1138</b>	<b>203</b>	24	<b>1106</b>	<b>209</b>	1104	210	1108	209		
403.gcc	24	624	310	621	311	<b>622</b>	<b>311</b>	24	624	310	621	311	<b>622</b>	<b>311</b>		
429.mcf	24	472	463	414	529	<b>420</b>	<b>522</b>	24	472	463	414	529	<b>420</b>	<b>522</b>		
445.gobmk	24	924	272	926	272	<b>926</b>	<b>272</b>	24	<b>898</b>	<b>280</b>	909	277	893	282		
456.hammer	24	<b>487</b>	<b>460</b>	488	459	487	460	24	405	553	404	555	<b>404</b>	<b>554</b>		
458.sjeng	24	1075	270	1073	271	<b>1073</b>	<b>271</b>	24	1032	281	1035	281	<b>1034</b>	<b>281</b>		
462.libquantum	24	<b>227</b>	<b>2190</b>	227	2190	227	2190	24	<b>227</b>	<b>2190</b>	227	2190	227	2190		
464.h264ref	24	1119	475	1132	469	<b>1128</b>	<b>471</b>	24	1111	478	1107	480	<b>1108</b>	<b>479</b>		
471.omnetpp	24	643	233	644	233	<b>643</b>	<b>233</b>	24	606	247	602	249	<b>603</b>	<b>249</b>		
473.astar	24	749	225	753	224	<b>752</b>	<b>224</b>	24	749	225	753	224	<b>752</b>	<b>224</b>		
483.xalancbmk	24	389	425	390	424	<b>390</b>	<b>425</b>	24	389	425	390	424	<b>390</b>	<b>425</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on mp225 Sat Sep 22 04:06:07 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2630L 0 @ 2.00GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12

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## Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      32850356 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.3 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux mp225 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 22 03:57
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_mp225-lv_home
                ext4       67G  2.1G  62G   4% /home
```

(End of data from sysinfo program)

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>



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You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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