



# SPEC® CFP2006 Result

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## Itaotec

### SPECfp®\_rate2006 = 331

### Servidor Itaotec MX225+ (Intel Xeon E5-2630L)

### SPECfp\_rate\_base2006 = 323

CPU2006 license: 9001

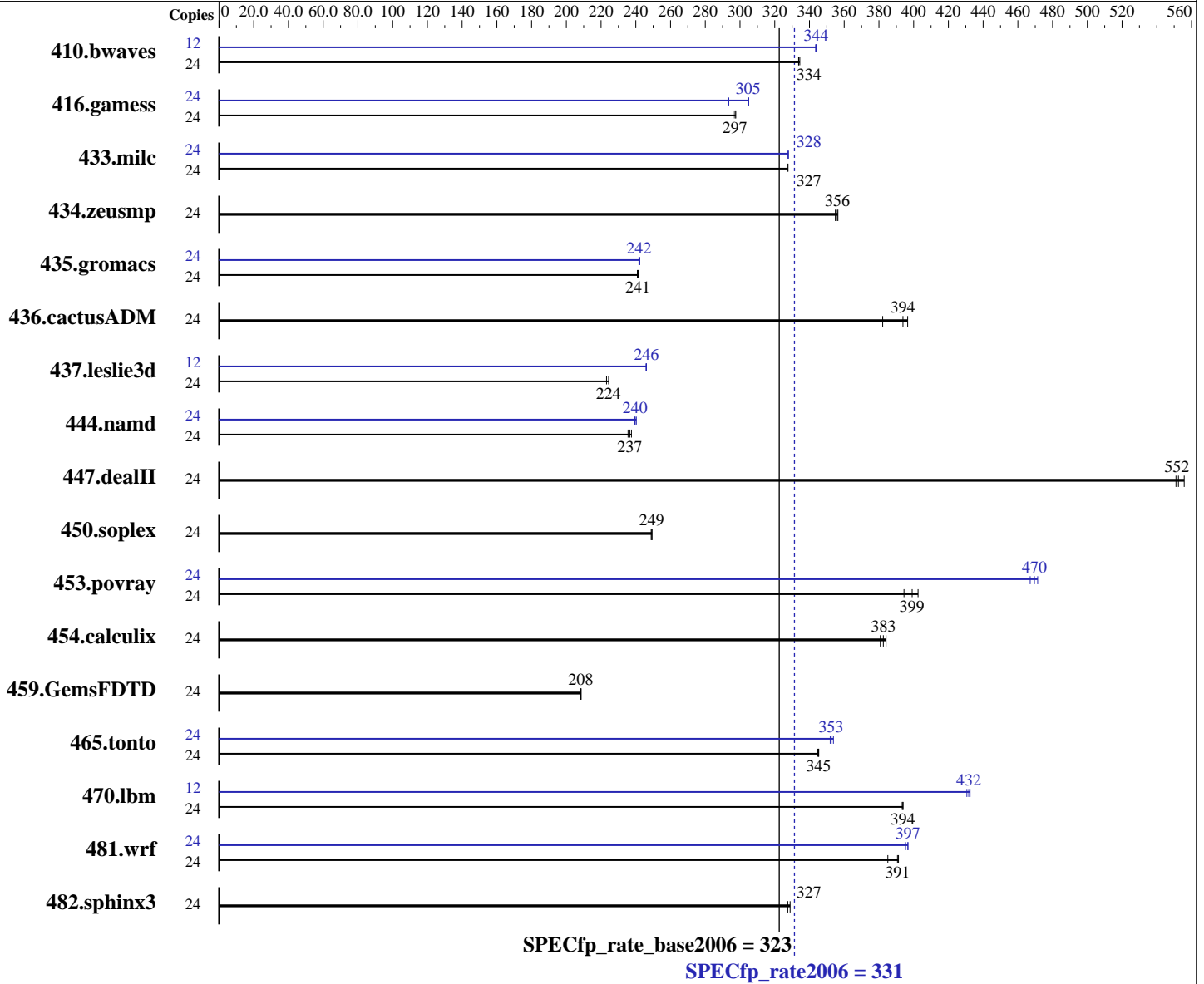
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2012

Hardware Availability: Jun-2012

Software Availability: Dec-2011



#### Hardware

CPU Name: Intel Xeon E5-2630L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++/Fortran: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
Disk Subsystem: 500 GB, SATA-2, 7200 RPM  
Other Hardware: None

Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	975	334	977	334	<b>977</b>	<b>334</b>	12	<b>474</b>	<b>344</b>	475	344	474	344
416.gamess	24	<b>1581</b>	<b>297</b>	1579	298	1587	296	24	<b>1542</b>	<b>305</b>	1540	305	1601	294
433.milc	24	673	327	672	328	<b>673</b>	<b>327</b>	24	<b>672</b>	<b>328</b>	672	328	672	328
434.zeusmp	24	615	355	613	356	<b>613</b>	<b>356</b>	24	615	355	613	356	<b>613</b>	<b>356</b>
435.gromacs	24	710	241	711	241	<b>710</b>	<b>241</b>	24	707	242	709	242	<b>708</b>	<b>242</b>
436.cactusADM	24	723	397	750	382	<b>728</b>	<b>394</b>	24	723	397	750	382	<b>728</b>	<b>394</b>
437.leslie3d	24	1011	223	1005	225	<b>1006</b>	<b>224</b>	12	459	246	458	246	<b>458</b>	<b>246</b>
444.namd	24	817	236	810	237	<b>814</b>	<b>237</b>	24	<b>803</b>	<b>240</b>	801	240	804	239
447.dealII	24	<b>497</b>	<b>552</b>	494	556	498	551	24	<b>497</b>	<b>552</b>	494	556	498	551
450.soplex	24	<b>803</b>	<b>249</b>	804	249	803	249	24	<b>803</b>	<b>249</b>	804	249	803	249
453.povray	24	317	403	<b>320</b>	<b>399</b>	324	394	24	<b>272</b>	<b>470</b>	271	471	273	467
454.calculix	24	520	381	<b>518</b>	<b>383</b>	515	384	24	520	381	<b>518</b>	<b>383</b>	515	384
459.GemsFDTD	24	1223	208	<b>1222</b>	<b>208</b>	1221	209	24	1223	208	<b>1222</b>	<b>208</b>	1221	209
465.tonto	24	685	345	684	345	<b>684</b>	<b>345</b>	24	668	354	<b>670</b>	<b>353</b>	671	352
470.lbm	24	<b>837</b>	<b>394</b>	838	393	837	394	12	<b>382</b>	<b>432</b>	383	431	381	432
481.wrf	24	696	385	685	391	<b>686</b>	<b>391</b>	24	676	397	<b>676</b>	<b>397</b>	678	395
482.sphinx3	24	<b>1428</b>	<b>327</b>	1422	329	1429	327	24	<b>1428</b>	<b>327</b>	1422	329	1429	327

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## General Notes

This result was measured on the Servidor Itaotec MX205.  
The Servidor Itaotec MX205, the Servidor Itaotec LX205 and the Servidor Itaotec MX225+ are electronically equivalent.



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Test date: Mar-2012  
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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3
```

```
470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32
-opt-mem-layout-trans=3
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32
```

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## Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll12 -inline-level=0 -scalar-rep-  
-static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll14 -auto -inline-alloc  
-opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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