SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL380p Gen8
(2.40 GHz, Intel Xeon E5-2609)

$SPECint®2006 =$ 32.9

$SPECint\_base2006 =$ 31.3

CPU2006 license: 3	**Test date:** May-2012
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Oct-2012
Tested by: Hewlett-Packard Company	**Software Availability:** Mar-2012

400.perlbench 25.3 / 21.1
401.bzip2 16.5 / 16.1
403.gcc 20.9 / 21.3
429.mcf 43.8
445.gobmk 18.1 / 18.1
456.hmmer 37.5 / 36.7
458.sjeng 19.3 / 19.3
462.libquantum 753
464.h264ref 37.9 / 34.6
471.omnetpp 15.3 / 12.5
473.astar 20.1
483.xalancbmk 36.1 / 33.4

SPECint_base2006 = 31.3

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Hardware

CPU Name:	Intel Xeon E5-2609
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
Disk Subsystem:	1 x 146 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2, (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01 HP Array Configuration Utility, CLI

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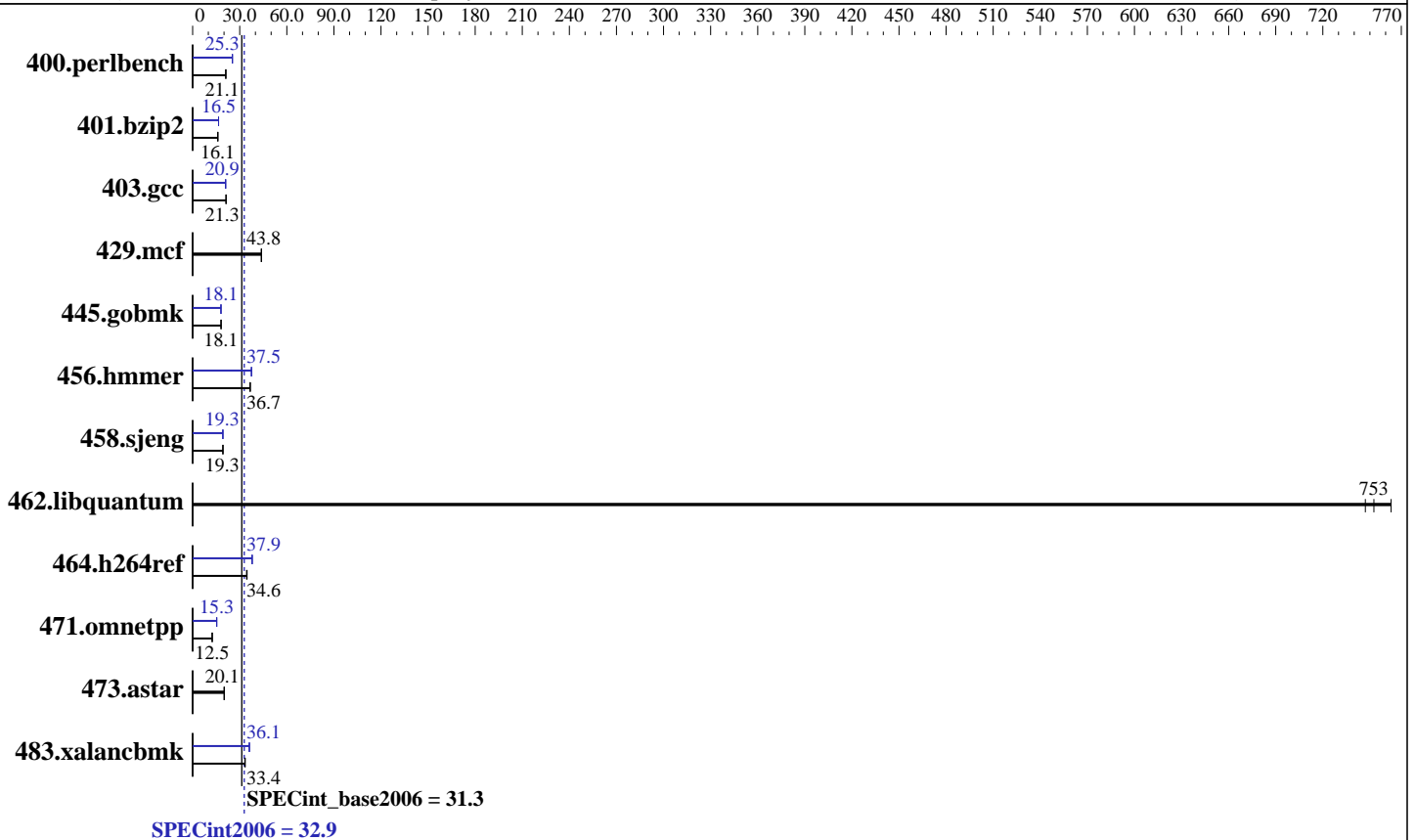
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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	462	21.2	**462**	**21.1**	463	21.1	386	25.3	**386**	**25.3**	385	25.4
401.bzip2	601	16.1	**601**	**16.1**	603	16.0	584	16.5	**584**	**16.5**	584	16.5
403.gcc	378	21.3	**378**	**21.3**	378	21.3	381	21.1	**384**	**20.9**	385	20.9
429.mcf	208	43.9	**208**	**43.8**	209	43.7	208	43.9	**208**	**43.8**	209	43.7
445.gobmk	579	18.1	**579**	**18.1**	579	18.1	581	18.1	**581**	**18.1**	581	18.1
456.hmmer	255	36.5	254	36.7	**254**	**36.7**	249	37.5	249	37.5	**249**	**37.5**
458.sjeng	627	19.3	**627**	**19.3**	627	19.3	**628**	**19.3**	629	19.3	628	19.3
462.libquantum	27.7	747	**27.5**	**753**	27.1	764	27.7	747	**27.5**	**753**	27.1	764
464.h264ref	639	34.7	**639**	**34.6**	644	34.3	585	37.8	583	38.0	**584**	**37.9**
471.omnetpp	**501**	**12.5**	501	12.5	503	12.4	**408**	**15.3**	407	15.4	408	15.3
473.astar	349	20.1	**349**	**20.1**	348	20.1	349	20.1	**349**	**20.1**	348	20.1
483.xalancbmk	206	33.5	207	33.3	**207**	**33.4**	191	36.1	191	36.1	**191**	**36.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 >        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility,
 CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write
 in HP Array Configuration Utility, CLI
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Custom
 Energy/Performance Bias is set to Maximum Performance
 Thermal Configuration set to Maximum Cooling

Sysinfo program /cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Wed May 30 20:27:10 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:       132119996 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
   EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 30 20:26

SPEC is set to: /cpu2006
   Filesystem      Type     Size  Used Avail Use% Mounted on
   /dev/sda3       ext4     133G  8.2G  118G   7% /

Additional information from dmidecode:
  BIOS HP P70 07/15/2012
  Memory:
   16x HP Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"
OMP_NUM_THREADS = "4"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
  400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

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Peak Compiler Invocation

C benchmarks (except as noted below):
      `icc -m64`

    400.perlbench: `icc -m32`

    445.gobmk: `icc -m32`

    464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
      `icpc -m32`

      473.astar: `icpc -m64`

Peak Portability Flags

        400.perlbench: `-DSPEC_CPU_LINUX_IA32`
        401.bzip2: `-DSPEC_CPU_LP64`
        403.gcc: `-DSPEC_CPU_LP64`
        429.mcf: `-DSPEC_CPU_LP64`
        456.hmmer: `-DSPEC_CPU_LP64`
        458.sjeng: `-DSPEC_CPU_LP64`
        462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
        473.astar: `-DSPEC_CPU_LP64`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-opt-prefetch -ansi-alias`

        401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
                `-opt-prefetch -ansi-alias`

        403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
                `-opt-malloc-options=3 -auto-ilp32`

        429.mcf: `basepeak = yes`

        445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                `-ansi-alias`

Continued on next page

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Peak Optimization Flags (Continued)

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4`

462.libquantum: basepeak = yes

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-ra-region-strategy=block        -ansi-alias`
`-Wl,-z,muldefs -L/smartheap -lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`
`-Wl,-z,muldefs -L/smartheap -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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