SPEC® CINT2006 Result

Hewlett-Packard Company

SPECint®_rate2006 = 430

ProLiant DL380p Gen8
(2.30 GHz, Intel Xeon E5-2630)

SPECint_rate_base2006 = 414

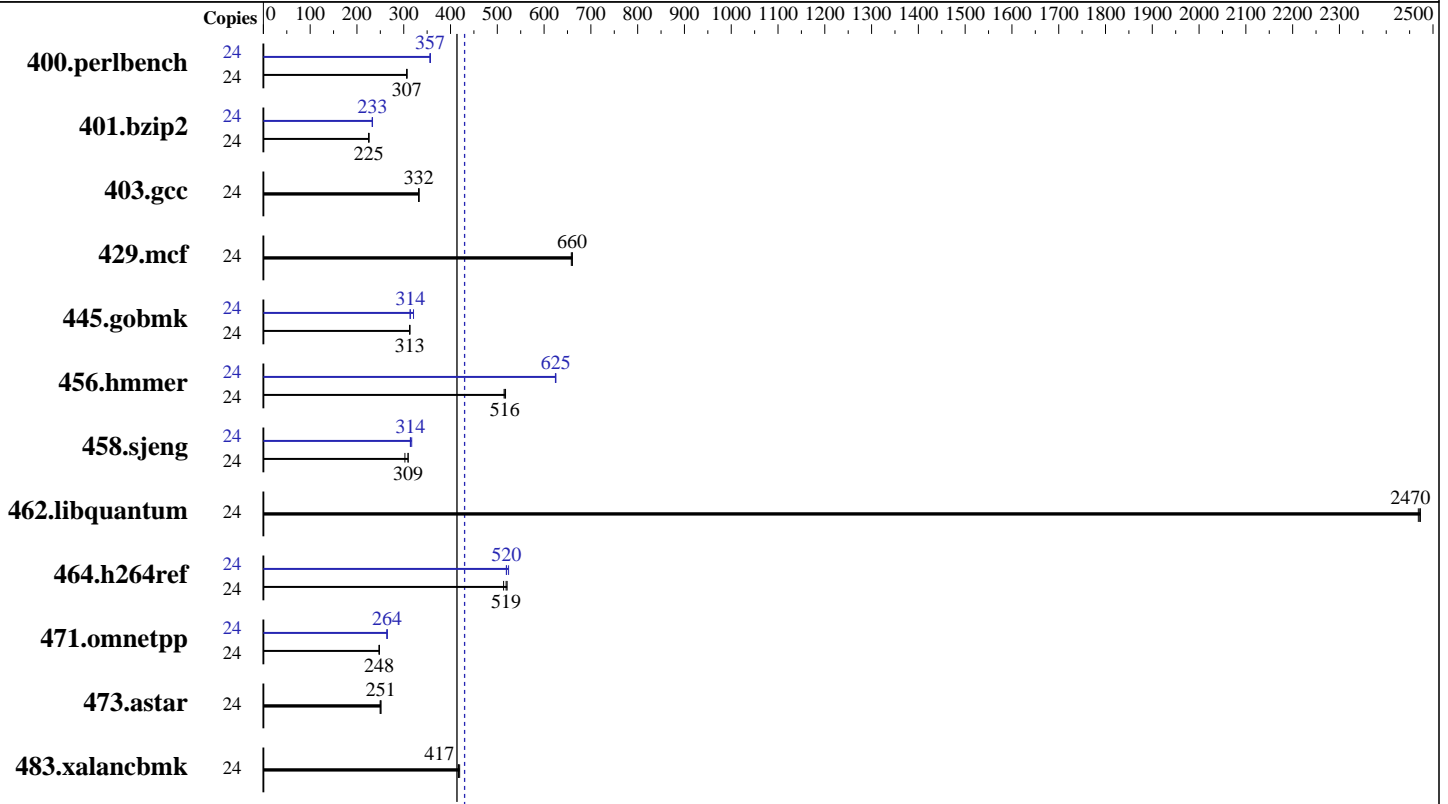|	
CPU2006 license: 3	**Test date:** May-2012
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Jun-2012
Tested by: Hewlett-Packard Company	**Software Availability:** Mar-2012

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Hardware

CPU Name:	Intel Xeon E5-2630
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem:	2 x 146 GB 15 K SAS, RAID 1
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel Compiler XE
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01 HP Array Configuration Utility, CLI version

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**765**	**307**	765	306	764	307	24	656	357	**657**	**357**	659	356
401.bzip2	24	1026	226	1029	225	**1028**	**225**	24	993	233	**994**	**233**	996	233
403.gcc	24	**581**	**332**	581	333	582	332	24	**581**	**332**	581	333	582	332
429.mcf	24	**332**	**660**	333	658	331	660	24	**332**	**660**	333	658	331	660
445.gobmk	24	**805**	**313**	805	313	805	313	24	**801**	**314**	803	314	784	321
456.hmmer	24	435	515	**434**	**516**	433	518	24	**358**	**625**	359	624	358	625
458.sjeng	24	960	303	**939**	**309**	939	309	24	**923**	**314**	924	314	916	317
462.libquantum	24	**201**	**2470**	201	2470	201	2470	24	**201**	**2470**	201	2470	201	2470
464.h264ref	24	1020	521	1034	513	**1024**	**519**	24	**1022**	**520**	1013	524	1023	519
471.omnetpp	24	**606**	**248**	606	248	606	248	24	566	265	**567**	**264**	568	264
473.astar	24	671	251	675	250	**672**	**251**	24	671	251	675	250	**672**	**251**
483.xalancbmk	24	395	420	397	417	**397**	**417**	24	395	420	397	417	**397**	**417**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
    Filesystem page cache cleared with:
    echo 1 >   /proc/sys/vm/drop_caches
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

Platform Notes

    BIOS Configuration:
     HP Power Profile set to Custom
     Energy/Performance Bias is set to Maximum Performance
     Thermal Configuration set to Maximum Cooling
     Collaborative Power Control set to Disabled
     Processor Power and Utilization Monitoring set to Disabled
     Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version
     Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version
    Sysinfo program /cpu2006/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on DL380G8-2 Thu May 3 09:11:12 2012

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       132120004 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux DL380G8-2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 2 10:10 last=5

SPEC is set to: /cpu2006
   Filesystem      Type     Size  Used Avail Use% Mounted on
   /dev/mapper/vg_rh62-lv_root
                   ext4      50G   15G   32G  33% /

Additional information from dmidecode:
  BIOS HP P70 02/21/2012
  Memory:
   16x Not Specified Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs2/32:/cpu2006/libs2/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/spec/libs2/32 -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
  `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
          `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
          `-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
          `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
          `-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
          `-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
          `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
          `-unroll4 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/libs2/32 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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