



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 95.8

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint_rate_base2006 = 91.4

CPU2006 license: 19

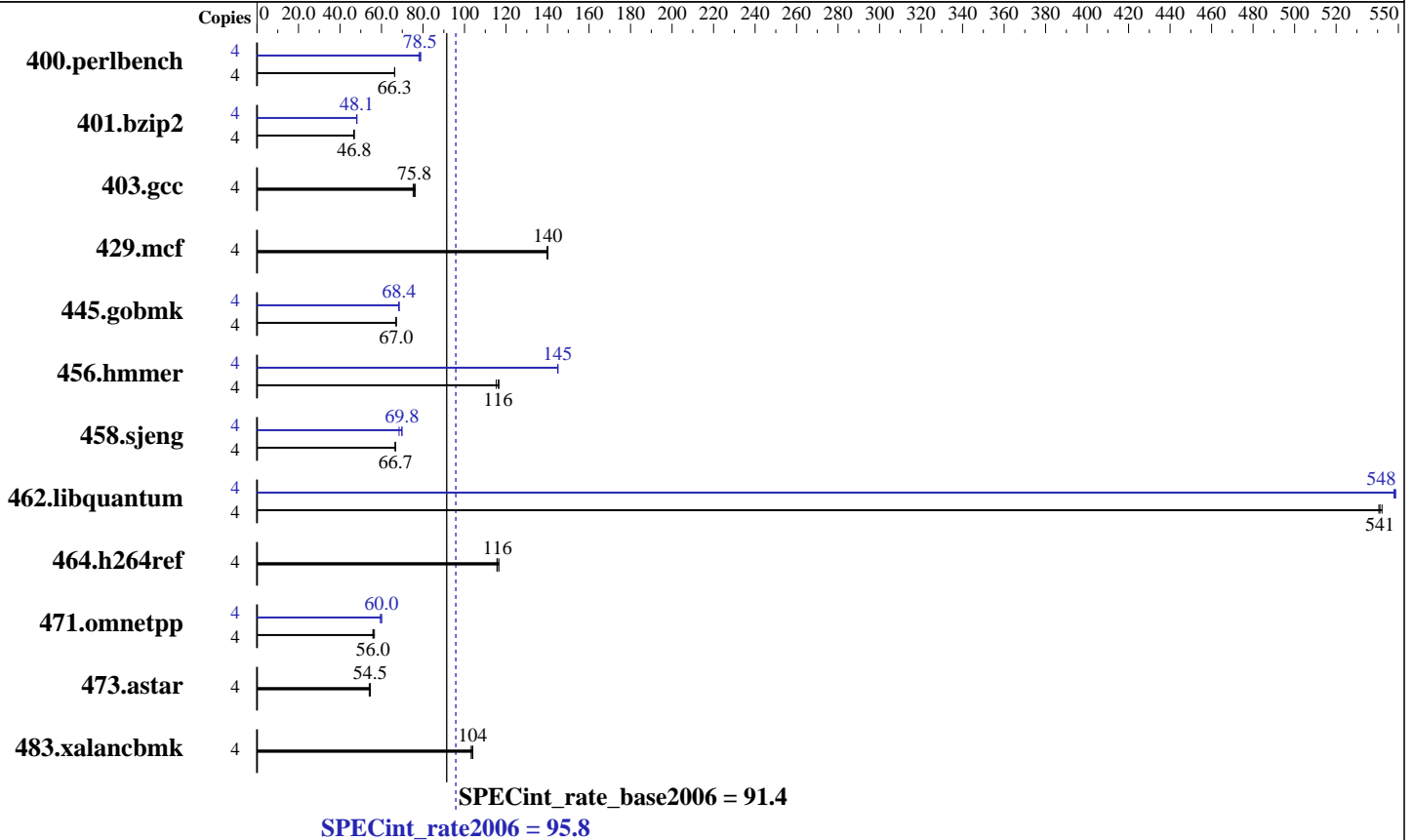
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Core i3-3220
 CPU Characteristics:
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	589	66.4	<u>589</u>	<u>66.3</u>	589	66.3	4	<u>498</u>	<u>78.5</u>	501	78.0	495	78.9
401.bzip2	4	<u>824</u>	<u>46.8</u>	824	46.8	829	46.6	4	803	48.1	802	48.1	<u>803</u>	<u>48.1</u>
403.gcc	4	428	75.2	<u>425</u>	<u>75.8</u>	422	76.3	4	428	75.2	<u>425</u>	<u>75.8</u>	422	76.3
429.mcf	4	261	140	260	140	<u>260</u>	<u>140</u>	4	261	140	260	140	<u>260</u>	<u>140</u>
445.gobmk	4	626	67.0	626	67.1	<u>626</u>	<u>67.0</u>	4	613	68.5	<u>613</u>	<u>68.4</u>	614	68.4
456.hammer	4	324	115	<u>321</u>	<u>116</u>	320	117	4	<u>257</u>	<u>145</u>	258	145	257	145
458.sjeng	4	<u>726</u>	<u>66.7</u>	728	66.5	724	66.8	4	693	69.8	<u>693</u>	<u>69.8</u>	707	68.5
462.libquantum	4	153	542	<u>153</u>	<u>541</u>	153	541	4	151	548	<u>151</u>	<u>548</u>	151	549
464.h264ref	4	759	117	<u>764</u>	<u>116</u>	766	116	4	759	117	<u>764</u>	<u>116</u>	766	116
471.omnetpp	4	442	56.6	448	55.9	<u>446</u>	<u>56.0</u>	4	417	60.0	<u>417</u>	<u>60.0</u>	421	59.4
473.astar	4	514	54.6	519	54.1	<u>515</u>	<u>54.5</u>	4	514	54.6	519	54.1	<u>515</u>	<u>54.5</u>
483.xalancbmk	4	265	104	<u>266</u>	<u>104</u>	267	103	4	265	104	<u>266</u>	<u>104</u>	267	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 2x E5-2650 CPU + 96 GB memory using RHEL6.2
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
 -opt-mem-layout-trans=3

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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