



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 44.1

PRIMERGY RX100 S7p, Intel Core i3-3220, 3.30 GHz

SPECint_base2006 = 41.7

CPU2006 license: 19

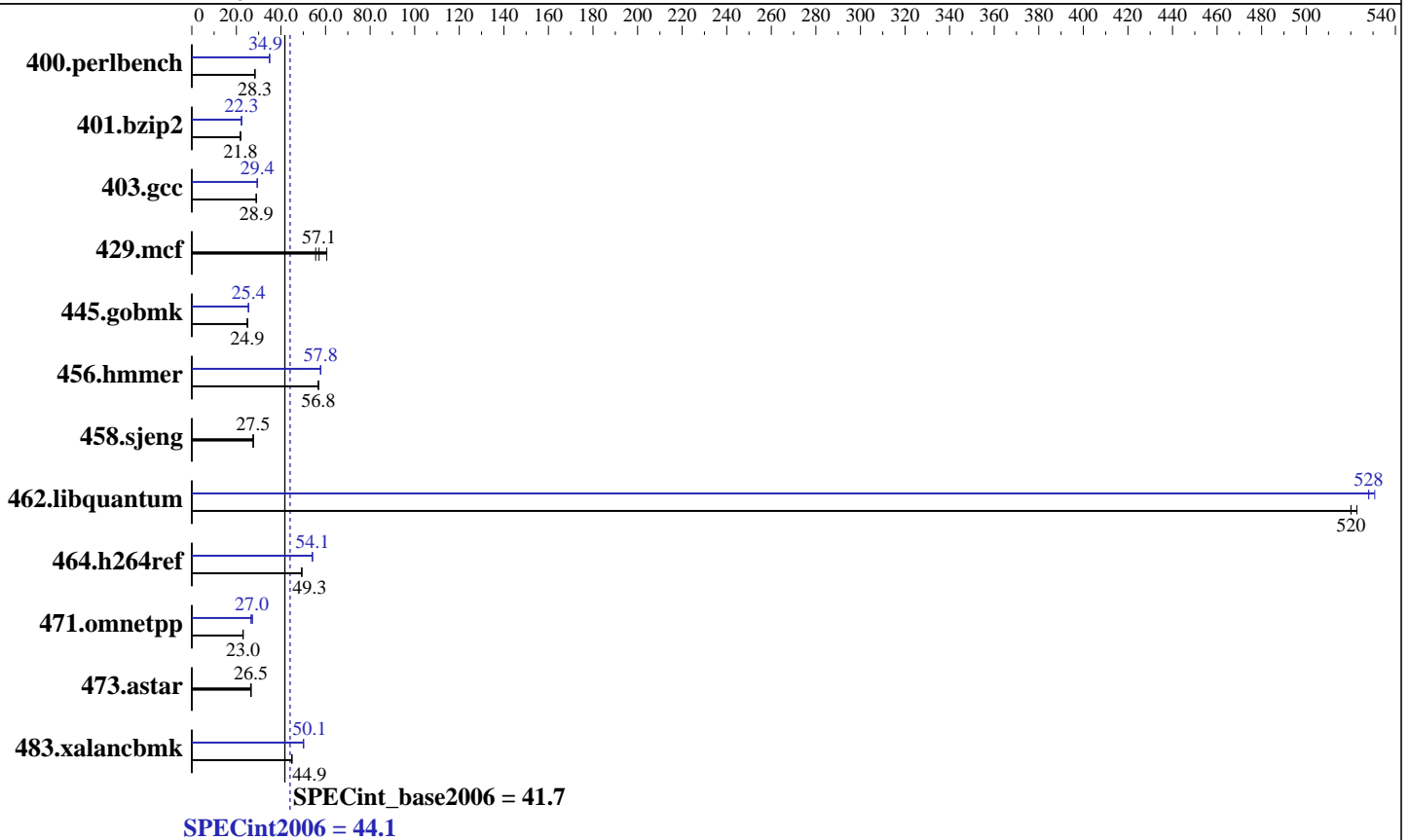
Test date: May-2012

Test sponsor: Fujitsu

Hardware Availability: Sep-2012

Tested by: Fujitsu

Software Availability: Feb-2012



Hardware

CPU Name: Intel Core i3-3220
 CPU Characteristics:
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	346	28.3	345	28.3	<u>345</u>	<u>28.3</u>	280	34.9	280	34.9	<u>280</u>	<u>34.9</u>
401.bzip2	<u>442</u>	<u>21.8</u>	441	21.9	443	21.8	432	22.3	<u>433</u>	<u>22.3</u>	434	22.2
403.gcc	<u>278</u>	<u>28.9</u>	278	28.9	278	28.9	274	29.3	<u>274</u>	<u>29.4</u>	274	29.4
429.mcf	151	60.6	<u>160</u>	<u>57.1</u>	164	55.6	151	60.6	<u>160</u>	<u>57.1</u>	164	55.6
445.gobmk	<u>421</u>	<u>24.9</u>	421	24.9	421	24.9	412	25.4	<u>413</u>	<u>25.4</u>	413	25.4
456.hmmer	164	56.8	164	56.8	<u>164</u>	<u>56.8</u>	162	57.8	<u>161</u>	<u>57.8</u>	161	57.8
458.sjeng	<u>439</u>	<u>27.5</u>	440	27.5	439	27.5	<u>439</u>	<u>27.5</u>	440	27.5	439	27.5
462.libquantum	39.6	523	<u>39.8</u>	<u>520</u>	39.8	520	<u>39.2</u>	<u>528</u>	39.0	531	39.2	528
464.h264ref	448	49.4	450	49.2	<u>449</u>	<u>49.3</u>	409	54.1	410	54.0	<u>409</u>	<u>54.1</u>
471.omnetpp	<u>272</u>	<u>23.0</u>	273	22.9	271	23.1	230	27.2	236	26.5	<u>232</u>	<u>27.0</u>
473.astar	<u>265</u>	<u>26.5</u>	266	26.4	263	26.7	<u>265</u>	<u>26.5</u>	266	26.4	263	26.7
483.xalancbmk	154	44.9	154	44.8	<u>154</u>	<u>44.9</u>	138	50.1	138	50.2	<u>138</u>	<u>50.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Platform Notes

BIOS configuration:
Intel HT Technology = Disable

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x E3-1270V2 CPU + 32 GB
memory using RHEL6.2
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32

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Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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