



# SPEC® CFP2006 Result

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## Fujitsu

### SPECfp®\_rate2006 = 135

### CELSIUS C620 (Intel Xeon E3-1280 v2)

### SPECfp\_rate\_base2006 = 133

CPU2006 license: 19

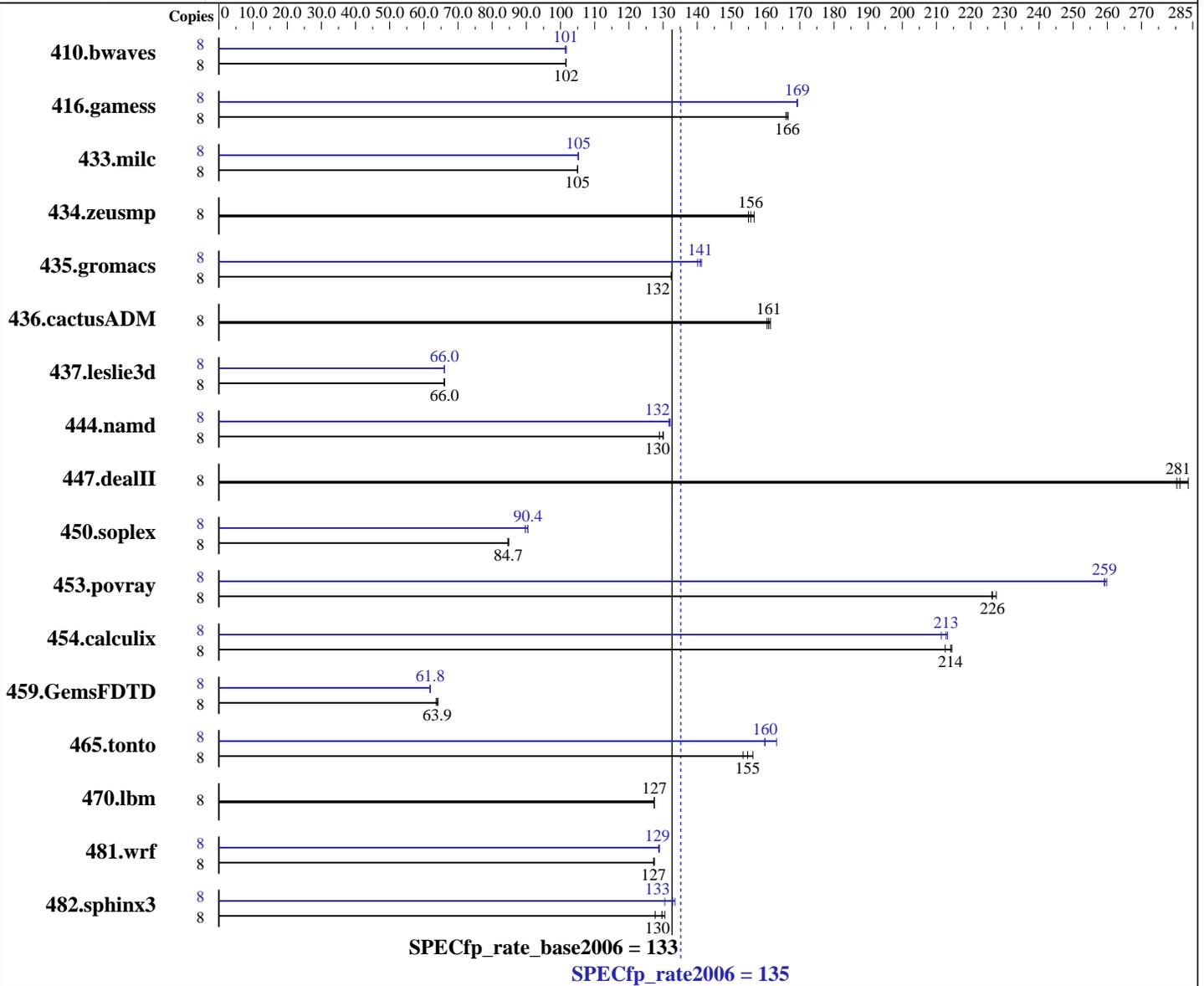
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2012

Hardware Availability: Jul-2012

Software Availability: Dec-2011



#### Hardware

CPU Name: Intel Xeon E3-1280 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.3.293 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi - user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800E-11, ECC)  
Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<b>1070</b>	<b>102</b>	1071	102	1070	102	8	1069	102	1073	101	<b>1072</b>	<b>101</b>
416.gamess	8	940	167	<b>942</b>	<b>166</b>	944	166	8	926	169	<b>925</b>	<b>169</b>	925	169
433.milc	8	700	105	699	105	<b>699</b>	<b>105</b>	8	<b>698</b>	<b>105</b>	698	105	698	105
434.zeusmp	8	465	157	<b>468</b>	<b>156</b>	470	155	8	465	157	<b>468</b>	<b>156</b>	470	155
435.gromacs	8	431	133	<b>431</b>	<b>132</b>	431	132	8	<b>405</b>	<b>141</b>	404	141	408	140
436.cactusADM	8	592	161	<b>594</b>	<b>161</b>	596	160	8	592	161	<b>594</b>	<b>161</b>	596	160
437.leslie3d	8	<b>1139</b>	<b>66.0</b>	1140	65.9	1138	66.1	8	<b>1139</b>	<b>66.0</b>	1140	66.0	1139	66.0
444.namd	8	498	129	493	130	<b>494</b>	<b>130</b>	8	<b>487</b>	<b>132</b>	487	132	485	132
447.dealII	8	323	284	326	280	<b>325</b>	<b>281</b>	8	323	284	326	280	<b>325</b>	<b>281</b>
450.soplex	8	<b>788</b>	<b>84.7</b>	789	84.6	786	84.9	8	737	90.5	744	89.7	<b>738</b>	<b>90.4</b>
453.povray	8	<b>188</b>	<b>226</b>	187	228	188	226	8	164	260	164	259	<b>164</b>	<b>259</b>
454.calculix	8	<b>308</b>	<b>214</b>	308	215	310	213	8	312	211	<b>310</b>	<b>213</b>	309	213
459.GemsFDTD	8	1335	63.6	<b>1328</b>	<b>63.9</b>	1323	64.2	8	1374	61.8	<b>1373</b>	<b>61.8</b>	1372	61.9
465.tonto	8	<b>509</b>	<b>155</b>	504	156	513	153	8	493	160	482	163	<b>492</b>	<b>160</b>
470.lbm	8	<b>862</b>	<b>127</b>	863	127	862	127	8	<b>862</b>	<b>127</b>	863	127	862	127
481.wrf	8	702	127	701	128	<b>702</b>	<b>127</b>	8	<b>694</b>	<b>129</b>	694	129	693	129
482.sphinx3	8	1195	131	<b>1202</b>	<b>130</b>	1221	128	8	1195	131	1168	133	<b>1174</b>	<b>133</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /work/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on rhiannon Sat Aug 4 02:13:41 2012

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**Test date:** Aug-2012  
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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E3-1280 V2 @ 3.60GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 4
  siblings     : 8
  physical 0   : cores 0 1 2 3
cache size     : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16262140 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsc_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux rhiannon 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 3 16:37
```

```
SPEC is set to: /work/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       ext4      193G      55G  128G  30% /work
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

```
Binaries were compiled on a system with
with 2 x E5-2650 CPU + 96 GB memory using RHEL 6.2.
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
```



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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3:icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex:icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
-unroll2

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static  
-auto-p32 -ansi-alias -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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Standard Performance Evaluation Corporation

[info@spec.org](mailto:info@spec.org)

<http://www.spec.org/>

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