



SPEC® CINT2006 Result

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IBM Corporation

SPECint_rate2006 = 61.8

IBM System x3530 M4 (Intel Pentium 1403)

SPECint_rate_base2006 = 59.3

CPU2006 license: 11

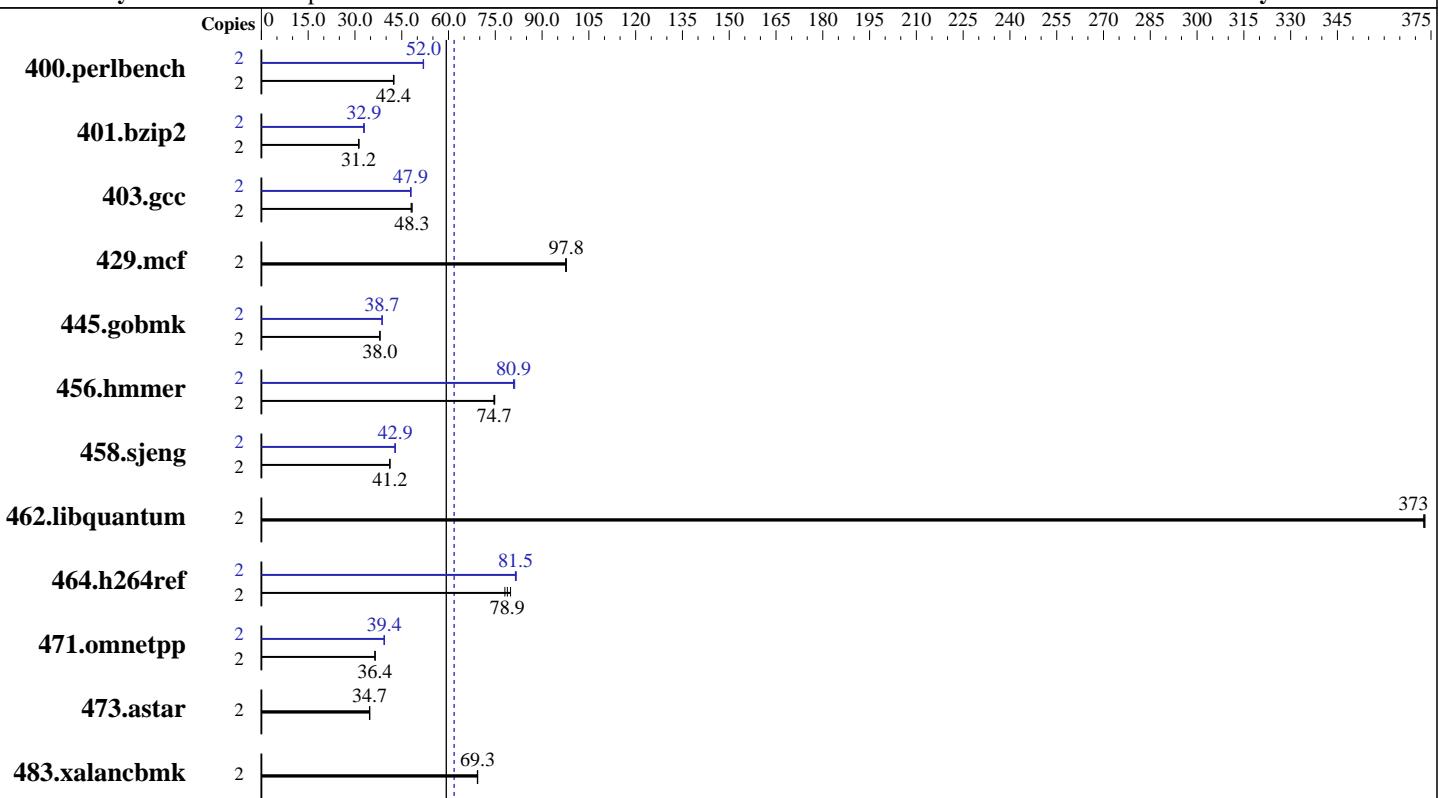
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



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Hardware

| | |
|----------------------|---|
| CPU Name: | Intel Pentium 1403 |
| CPU Characteristics: | |
| CPU MHz: | 2600 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 5 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz) |
| Disk Subsystem: | 1 x 500 GB SATA, 7200 RPM |
| Other Hardware: | None |

Software

| | |
|-------------------|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | 461 | 42.4 | 462 | 42.3 | 460 | 42.5 | 2 | 376 | 52.0 | 377 | 51.9 | 376 | 52.0 |
| 401.bzip2 | 2 | 620 | 31.1 | 616 | 31.3 | 620 | 31.2 | 2 | 586 | 32.9 | 586 | 32.9 | 588 | 32.8 |
| 403.gcc | 2 | 335 | 48.0 | 333 | 48.3 | 333 | 48.3 | 2 | 336 | 47.9 | 337 | 47.8 | 336 | 48.0 |
| 429.mcf | 2 | 187 | 97.5 | 187 | 97.8 | 187 | 97.8 | 2 | 187 | 97.5 | 187 | 97.8 | 187 | 97.8 |
| 445.gobmk | 2 | 552 | 38.0 | 552 | 38.0 | 552 | 38.0 | 2 | 542 | 38.7 | 542 | 38.7 | 542 | 38.7 |
| 456.hmmer | 2 | 249 | 74.8 | 250 | 74.7 | 250 | 74.6 | 2 | 231 | 80.9 | 231 | 80.9 | 230 | 81.2 |
| 458.sjeng | 2 | 587 | 41.2 | 587 | 41.2 | 588 | 41.2 | 2 | 564 | 42.9 | 564 | 42.9 | 565 | 42.9 |
| 462.libquantum | 2 | 111 | 373 | 111 | 373 | 111 | 373 | 2 | 111 | 373 | 111 | 373 | 111 | 373 |
| 464.h264ref | 2 | 567 | 78.1 | 561 | 78.9 | 554 | 79.9 | 2 | 543 | 81.4 | 542 | 81.7 | 543 | 81.5 |
| 471.omnetpp | 2 | 343 | 36.5 | 343 | 36.4 | 343 | 36.4 | 2 | 317 | 39.4 | 318 | 39.4 | 317 | 39.4 |
| 473.astar | 2 | 405 | 34.7 | 404 | 34.8 | 405 | 34.6 | 2 | 405 | 34.7 | 404 | 34.8 | 405 | 34.6 |
| 483.xalancbmk | 2 | 199 | 69.3 | 199 | 69.2 | 199 | 69.3 | 2 | 199 | 69.3 | 199 | 69.2 | 199 | 69.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Sysinfo program /root/SPECcpu-v1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date::: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Mon Jul 23 15:41:53 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Pentium(R) CPU 1403 @ 2.60GHz
  1 "physical id"s (chips)
  2 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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Platform Notes (Continued)

```
cpu cores : 2
siblings : 2
physical 0: cores 0 1
cache size : 5120 KB

From /proc/meminfo
MemTotal:      49406052 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 23 15:14

SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type    Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3530m4-lv_root
                  ext4    133G   13G  114G  10%  /


Additional information from dmidecode:
Memory:
 6x Samsung M393B1K70DH0-CK0 8 GB 1066 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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