# SPEC® CINT2006 Result

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## NEC Corporation

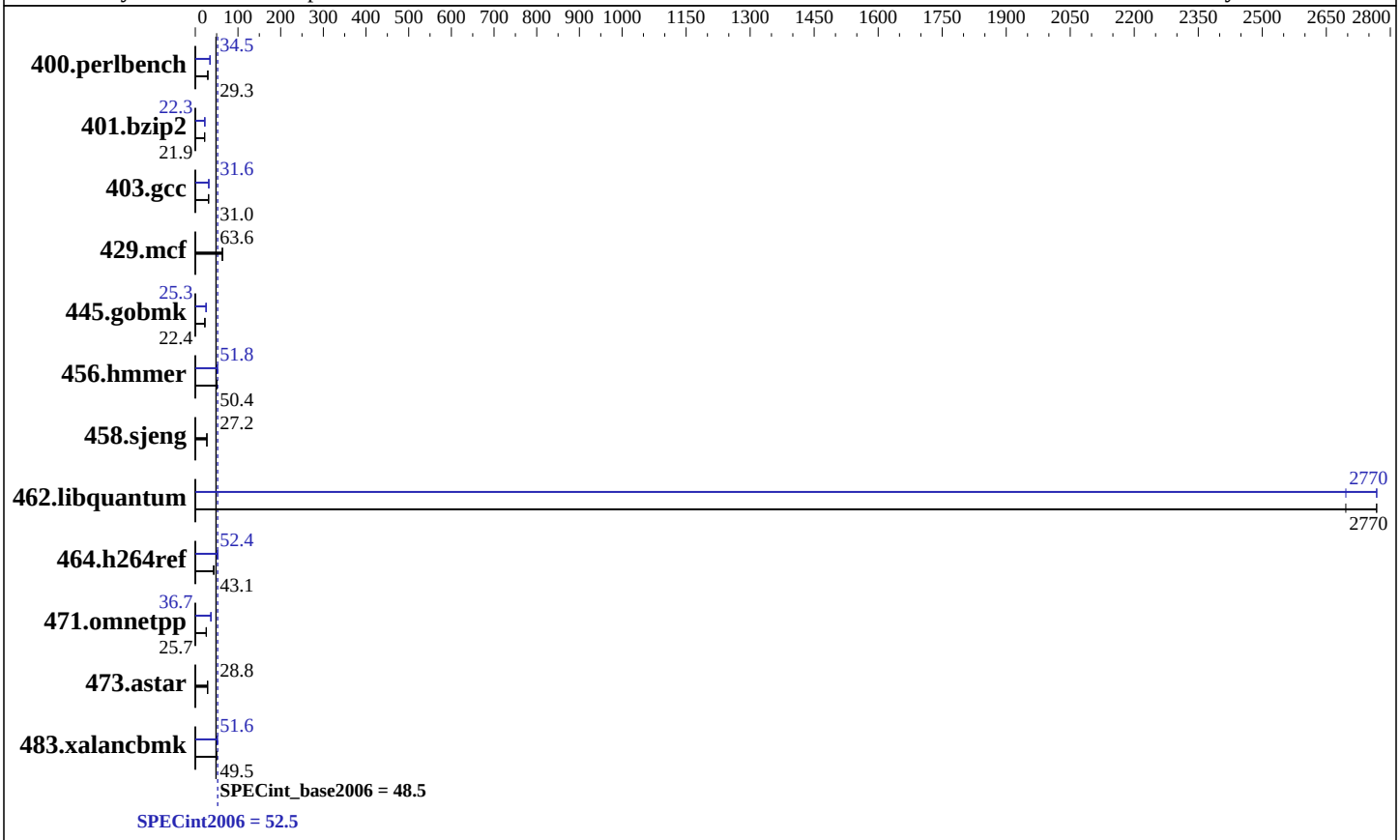
## Express5800/B120d-h (Intel Xeon E5-2670)

**SPECint®2006 = 52.5**

**SPECint_base2006 = 48.5**

**CPU2006 license:** 9006
**Test sponsor:** NEC Corporation
**Tested by:** NEC Corporation

**Test date:** Jul-2012
**Hardware Availability:** Jun-2012
**Software Availability:** Feb-2012




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2670 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.30 GHz |
| CPU MHz: | 2600 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC) |
| Disk Subsystem: | 1 x 146.5 GB SAS, 15000 RPM |
| Other Hardware: | Express5800/AD106b for Disk Subsystem |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>Kernel 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux; |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 333 | 29.3 | **333** | **29.3** | 333 | 29.3 | **283** | **34.5** | 283 | 34.6 | 283 | 34.5 |
| 401.bzip2 | 441 | 21.9 | **441** | **21.9** | 441 | 21.9 | 433 | 22.3 | **433** | **22.3** | 433 | 22.3 |
| 403.gcc | 259 | 31.1 | 260 | 31.0 | **259** | **31.0** | **255** | **31.6** | 255 | 31.6 | 255 | 31.6 |
| 429.mcf | 145 | 63.0 | 143 | 63.8 | **143** | **63.6** | 145 | 63.0 | 143 | 63.8 | **143** | **63.6** |
| 445.gobmk | **468** | **22.4** | 460 | 22.8 | 475 | 22.1 | **414** | **25.3** | 414 | 25.3 | 414 | 25.3 |
| 456.hmmer | 185 | 50.5 | 186 | 50.2 | **185** | **50.4** | **180** | **51.8** | 180 | 51.8 | 180 | 51.7 |
| 458.sjeng | **446** | **27.2** | 446 | 27.1 | 445 | 27.2 | **446** | **27.2** | 446 | 27.1 | 445 | 27.2 |
| 462.libquantum | 7.69 | 2700 | 7.49 | 2770 | **7.49** | **2770** | **7.49** | **2770** | 7.69 | 2700 | 7.48 | 2770 |
| 464.h264ref | 515 | 43.0 | **514** | **43.1** | 513 | 43.2 | 423 | 52.3 | **423** | **52.4** | 422 | 52.5 |
| 471.omnetpp | 243 | 25.8 | **243** | **25.7** | 244 | 25.6 | **170** | **36.7** | 170 | 36.8 | 172 | 36.4 |
| 473.astar | 244 | 28.8 | **244** | **28.8** | 244 | 28.7 | 244 | 28.8 | **244** | **28.8** | 244 | 28.7 |
| 483.xalancbmk | 139 | 49.5 | **139** | **49.5** | 139 | 49.5 | 134 | 51.7 | 134 | 51.4 | **134** | **51.6** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
 Memory Voltage: 1.5 V
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "16"

 Added glibc-static-2.12-1.47.el6.x86_64.rpm
 to enable static linking

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

Continued on next page

## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32

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## Peak Optimization Flags (Continued)

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml


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