



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®_rate2006 = 194

PowerEdge R515 (AMD Opteron 4276 HE, 2.60 GHz)

SPECfp_rate_base2006 = 178

CPU2006 license: 55

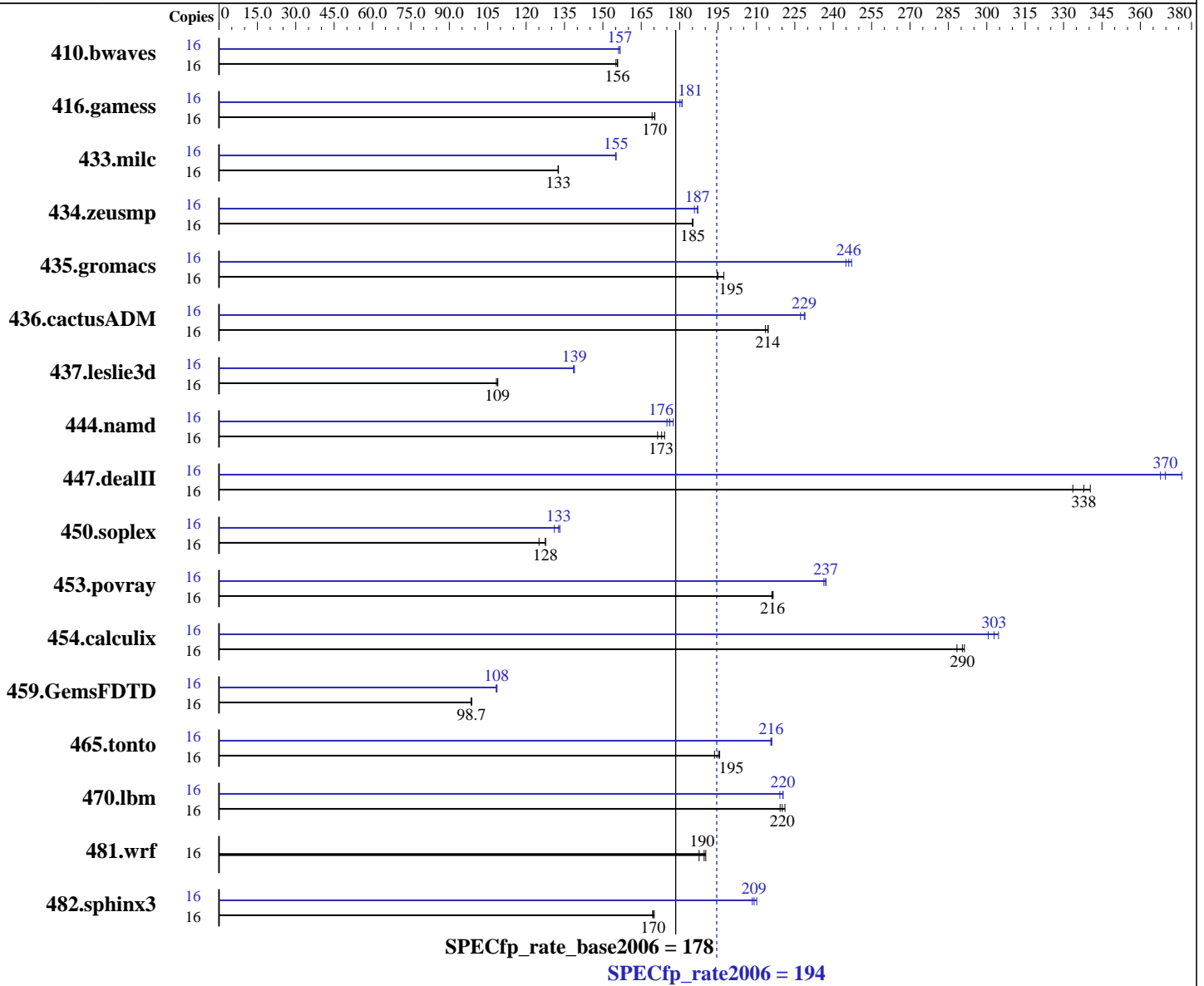
Test date: Jul-2012

Test sponsor: Dell Inc.

Hardware Availability: Jun-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: AMD Opteron 4276 HE
 CPU Characteristics: AMD Turbo CORE technology up to 3.60 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP2(x86_64)
 3.0.13-0.27-default
 Compiler: C/C++/Fortran: Version 4.5.1 of x86 Open64
 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

Continued on next page



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Primary Cache: 256 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 32 GB (4 x 8 GB 2Rx4 PC3-12800R-11, ECC)

Disk Subsystem: 1 x 250 GB SATA 7200 RPM

Other Hardware: None

Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|-------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | 1396 | 156 | <u>1397</u> | <u>156</u> | 1402 | 155 | 16 | 1393 | 156 | 1388 | 157 | <u>1388</u> | <u>157</u> |
| 416.gamess | 16 | <u>1841</u> | <u>170</u> | 1851 | 169 | 1839 | 170 | 16 | <u>1732</u> | <u>181</u> | 1740 | 180 | 1731 | 181 |
| 433.milc | 16 | <u>1108</u> | <u>133</u> | 1107 | 133 | 1109 | 132 | 16 | 948 | 155 | 947 | 155 | <u>947</u> | <u>155</u> |
| 434.zeusmp | 16 | 786 | 185 | 787 | 185 | <u>787</u> | <u>185</u> | 16 | 784 | 186 | <u>779</u> | <u>187</u> | 778 | 187 |
| 435.gromacs | 16 | 579 | 197 | <u>586</u> | <u>195</u> | 587 | 195 | 16 | 466 | 245 | 462 | 247 | <u>464</u> | <u>246</u> |
| 436.cactusADM | 16 | 891 | 215 | 896 | 213 | <u>892</u> | <u>214</u> | 16 | 835 | 229 | <u>836</u> | <u>229</u> | 841 | 227 |
| 437.leslie3d | 16 | 1387 | 108 | 1380 | 109 | <u>1382</u> | <u>109</u> | 16 | <u>1083</u> | <u>139</u> | 1083 | 139 | 1086 | 139 |
| 444.namd | 16 | 737 | 174 | <u>742</u> | <u>173</u> | 749 | 171 | 16 | <u>729</u> | <u>176</u> | 723 | 178 | 733 | 175 |
| 447.dealII | 16 | 538 | 340 | 549 | 334 | <u>542</u> | <u>338</u> | 16 | 487 | 376 | <u>495</u> | <u>370</u> | 498 | 368 |
| 450.soplex | 16 | 1067 | 125 | <u>1047</u> | <u>128</u> | 1046 | 128 | 16 | 1018 | 131 | <u>1006</u> | <u>133</u> | 1002 | 133 |
| 453.povray | 16 | <u>394</u> | <u>216</u> | 394 | 216 | 393 | 217 | 16 | 360 | 236 | 359 | 237 | <u>359</u> | <u>237</u> |
| 454.calculix | 16 | <u>454</u> | <u>290</u> | 458 | 288 | 453 | 291 | 16 | 433 | 305 | 439 | 301 | <u>436</u> | <u>303</u> |
| 459.GemsFDTD | 16 | 1719 | 98.8 | <u>1720</u> | <u>98.7</u> | 1723 | 98.5 | 16 | 1567 | 108 | <u>1565</u> | <u>108</u> | 1563 | 109 |
| 465.tonto | 16 | <u>807</u> | <u>195</u> | 805 | 196 | 813 | 194 | 16 | 729 | 216 | 730 | 216 | <u>730</u> | <u>216</u> |
| 470.lbm | 16 | 994 | 221 | <u>999</u> | <u>220</u> | 1003 | 219 | 16 | 997 | 220 | 1003 | 219 | <u>998</u> | <u>220</u> |
| 481.wrf | 16 | 940 | 190 | 953 | 188 | <u>943</u> | <u>190</u> | 16 | 940 | 190 | 953 | 188 | <u>943</u> | <u>190</u> |
| 482.sphinx3 | 16 | 1840 | 169 | <u>1838</u> | <u>170</u> | 1833 | 170 | 16 | 1497 | 208 | 1484 | 210 | <u>1491</u> | <u>209</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Transparent huge pages were enabled for this run (OS default)

Continued on next page



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Operating System Notes (Continued)

Huge pages were not configured for this run.

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/cpu2006/amd1104-rate-libs-revC/32:/root/cpu2006/amd1104-rate-libs-revC/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6274 chips + 64GB Memory using RHEL 6.1

Base Compiler Invocation

C benchmarks:
openc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
openc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
 -fno-second-underscore

Continued on next page



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Base Portability Flags (Continued)

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:

-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1
-INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:

-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:

-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off
-OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore

Continued on next page



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Peak Portability Flags (Continued)

437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
 -fno-second-underscore
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on
 -HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200
 -OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: -march=bdver1 -Ofast -CG:cmp_peep=on
 -OPT:unroll_times_max=8 -OPT:unroll_size=256
 -OPT:unroll_level=2 -OPT:keep_ext=on -HP:bdt=2m:heap=2m
 -IPA:plimit=8000 -IPA:small_pu=100 -mso

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=1000
 -OPT:malloc_alg=2 -CG:cmp_peep=on -CG:local_sched_alg=2
 -CG:p2align=0 -INLINE:aggressive=on -LNO:prefetch=2
 -LNO:prefetch_ahead=4 -mso

C++ benchmarks:

444.namd: -march=bdver1 -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=3000
 -LNO:ignore_feedback=off -CG:local_sched_alg=2
 -CG:load_exe=0 -OPT:unroll_size=256 -fno-exceptions
 -HP:bdt=2m:heap=2m

447.dealIII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static
 -INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0
 -fno-emit-exceptions -m32 -OPT:unroll_times_max=8
 -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
 -GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off
 -TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -LNO:ignore_feedback=off
 -INLINE:aggressive=on -OPT:RO=1 -OPT:IEEE_arith=3
 -OPT:IEEE_NaN_Inf=off -OPT:fold_unsigned_relops=on

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Peak Optimization Flags (Continued)

450.soplex (continued):

-fno-exceptions -CG:p2align=0 -m32 -HP:bdt=2m:heap=2m
-WOPT:sib=on

453.povray:

-march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-CG:p2align=0 -CG:p2align_split=on -CG:dsched=on
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves:

-march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bdt=2m:heap=2m -CG:cmp_peep=on

416.gamess:

-march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m -WOPT:sib=on

434.zeusmp:

-march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-IPA:plimit=1500 -HP:bdt=2m:heap=2m

437.leslie3d:

-march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bdt=2m:heap=2m -mso

459.GemsFDTD:

-march=bdver1 -Ofast -IPA:plimit=1500 -OPT:unroll_size=0
-LNO:fission=2 -CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto:

-march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs:

-march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m -CG:local_sched_alg=2 -GRA:unspill=ON
-CG:load_exe=3 -LNO:simd=3

436.cactusADM:

-march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:blocking=off
-LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0
-CG:dsched=on -WOPT:sib=on

Continued on next page



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Peak Optimization Flags (Continued)

454.calculix: -march=bdver1 -Ofast -OPT:unroll_size=256
-GRA:optimize_boundary=on -CG:dsched=on -HP:bdt=2m:heap=2m

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/amd-platform-speed-revA-I.html>

<http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/amd-platform-speed-revA-I.xml>

<http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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<http://www.spec.org/>