



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECint[®]_rate2006 = 197

Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 190

CPU2006 license: 001176

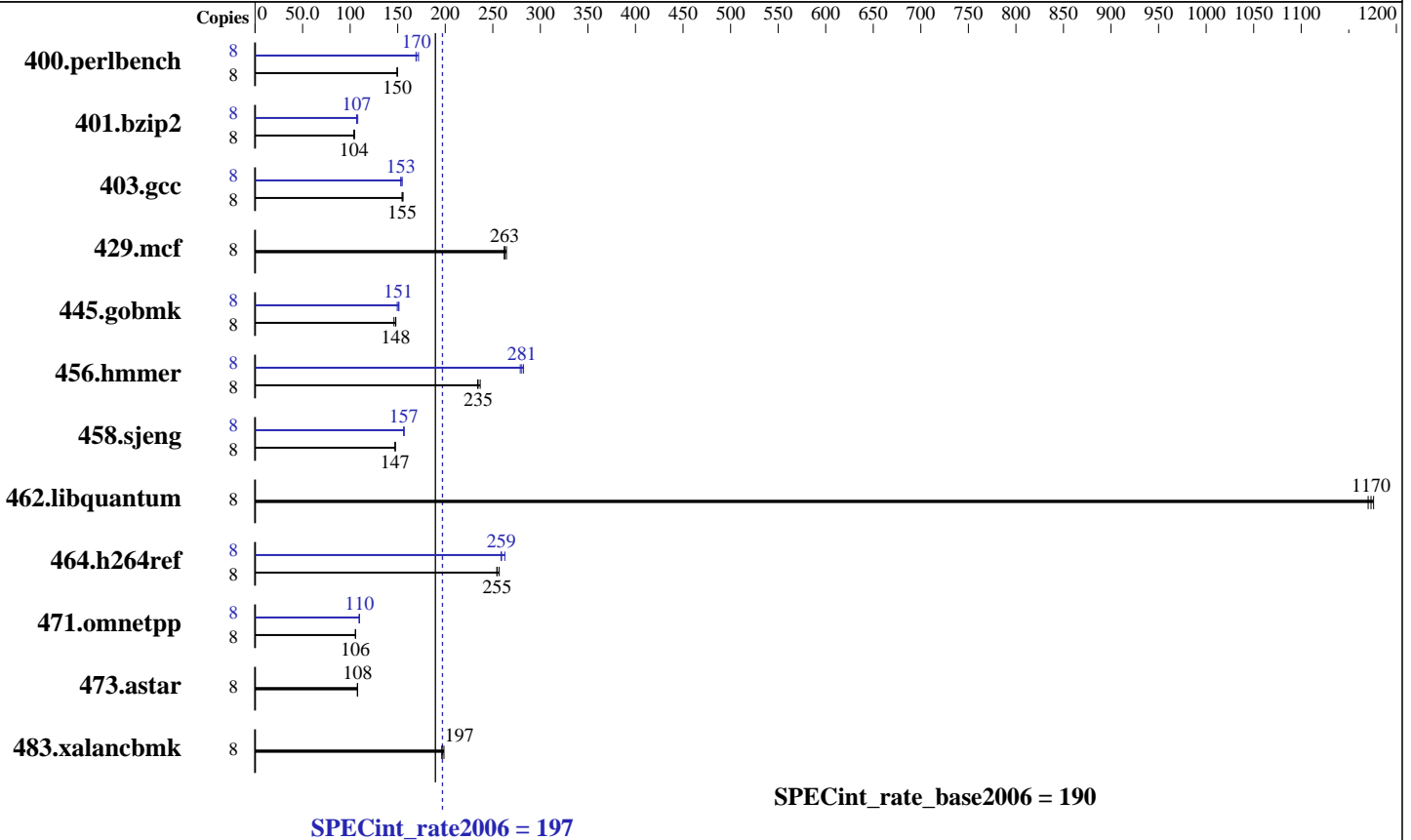
Test date: Jun-2012

Test sponsor: Supermicro

Hardware Availability: May-2012

Tested by: Supermicro

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1280 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago), Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECint_rate2006 = 197

Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 190

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	523	149	<u>523</u>	<u>150</u>	523	150	8	<u>461</u>	<u>170</u>	462	169	455	172
401.bzip2	8	742	104	738	105	<u>742</u>	<u>104</u>	8	715	108	<u>722</u>	<u>107</u>	723	107
403.gcc	8	414	156	416	155	<u>416</u>	<u>155</u>	8	416	155	<u>420</u>	<u>153</u>	420	153
429.mcf	8	<u>278</u>	<u>263</u>	279	262	276	264	8	<u>278</u>	<u>263</u>	279	262	276	264
445.gobmk	8	575	146	567	148	<u>568</u>	<u>148</u>	8	<u>557</u>	<u>151</u>	561	150	555	151
456.hammer	8	319	234	315	237	<u>318</u>	<u>235</u>	8	<u>266</u>	<u>281</u>	268	279	264	282
458.sjeng	8	657	147	657	147	<u>657</u>	<u>147</u>	8	619	156	<u>618</u>	<u>157</u>	618	157
462.libquantum	8	141	1180	142	1170	<u>141</u>	<u>1170</u>	8	141	1180	142	1170	<u>141</u>	<u>1170</u>
464.h264ref	8	696	254	690	257	<u>695</u>	<u>255</u>	8	<u>683</u>	<u>259</u>	674	263	684	259
471.omnetpp	8	474	106	473	106	<u>473</u>	<u>106</u>	8	<u>455</u>	<u>110</u>	457	109	455	110
473.astar	8	521	108	522	108	<u>521</u>	<u>108</u>	8	521	108	522	108	<u>521</u>	<u>108</u>
483.xalancbmk	8	<u>281</u>	<u>197</u>	281	196	278	199	8	<u>281</u>	<u>197</u>	281	196	278	199

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECint_rate2006 = 197

Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 190

CPU2006 license: 001176

Test date: Jun-2012

Test sponsor: Supermicro

Hardware Availability: May-2012

Tested by: Supermicro

Software Availability: Dec-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECint_rate2006 = 197

Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 190

CPU2006 license: 001176

Test date: Jun-2012

Test sponsor: Supermicro

Hardware Availability: May-2012

Tested by: Supermicro

Software Availability: Dec-2011

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzp2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECint_rate2006 = 197

Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 190

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 09:44:59 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 July 2012.