SPEC® CINT2006 Result
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Acer Incorporated

Acer AR320 F2 (Xeon E3-1280 v2)

SPECint®_rate2006 =   187

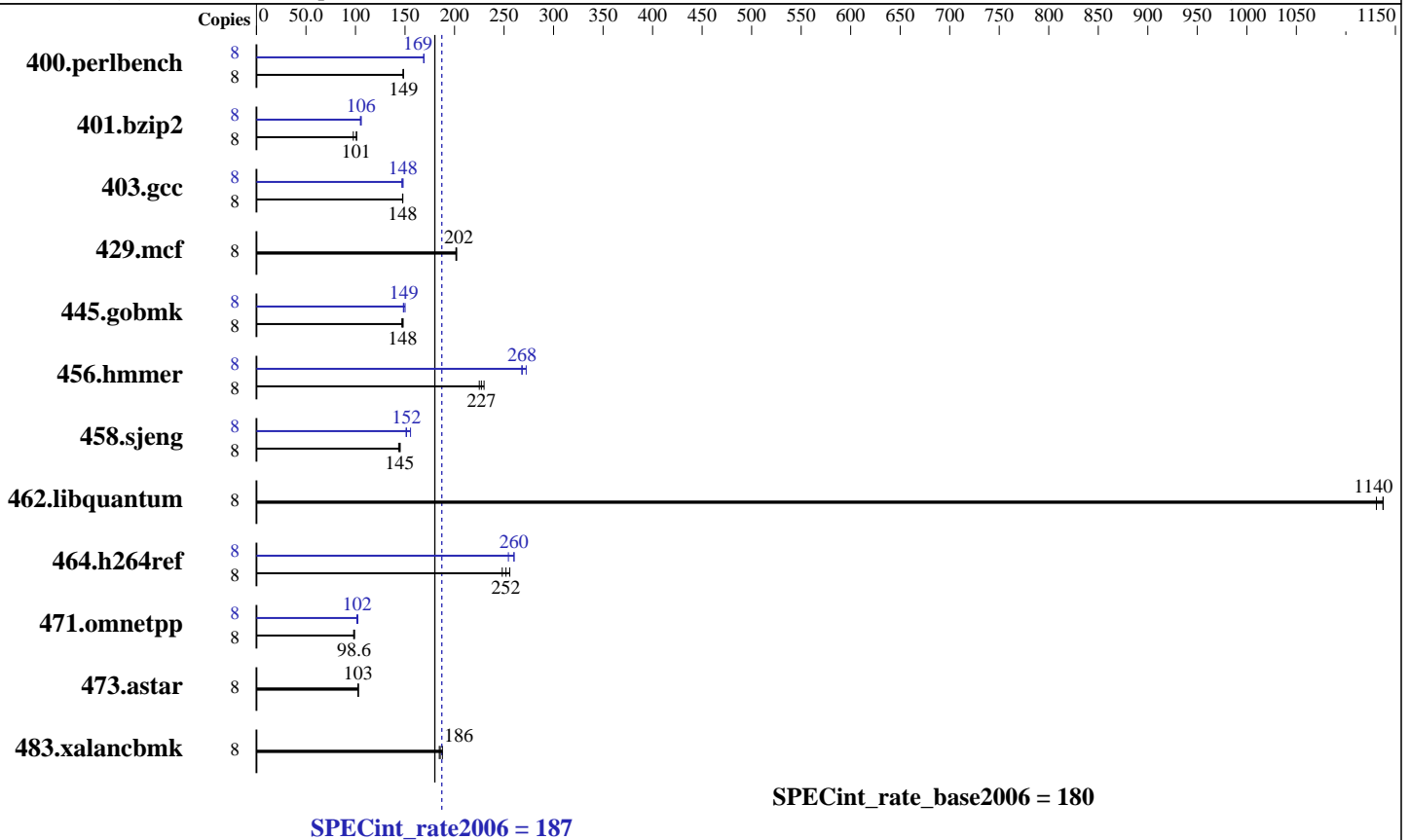SPECint_rate_base2006 = 180

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2012
Hardware Availability: Jan-2012
Software Availability: Dec-2011

Copies

Benchmark	Peak	Base
400.perlbench	169	149
401.bzip2	106	101
403.gcc	148	148
429.mcf		202
445.gobmk	149	148
456.hmmer	268	227
458.sjeng	152	145
462.libquantum		1140
464.h264ref	260	252
471.omnetpp	102	98.6
473.astar		103
483.xalancbmk		186

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Hardware

CPU Name: Intel Xeon E3-1280 v2
CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9,ECC)
Disk Subsystem: 1 x 640 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
2.6.32-131.0.15.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	526	149	**526**	**149**	529	148	8	463	169	**462**	**169**	462	169
401.bzip2	8	790	97.8	**766**	**101**	763	101	8	736	105	**732**	**106**	730	106
403.gcc	8	437	148	**436**	**148**	436	148	8	439	147	435	148	**436**	**148**
429.mcf	8	**362**	**202**	362	202	361	202	8	**362**	**202**	362	202	361	202
445.gobmk	8	**568**	**148**	567	148	572	147	8	566	148	559	150	**564**	**149**
456.hmmer	8	332	225	**329**	**227**	325	230	8	**278**	**268**	274	272	279	268
458.sjeng	8	674	144	**669**	**145**	667	145	8	641	151	**639**	**152**	622	156
462.libquantum	8	146	1140	**146**	**1140**	147	1130	8	146	1140	**146**	**1140**	147	1130
464.h264ref	8	**703**	**252**	713	248	692	256	8	696	254	**681**	**260**	680	260
471.omnetpp	8	505	99.0	509	98.1	**507**	**98.6**	8	493	101	488	102	**491**	**102**
473.astar	8	**547**	**103**	547	103	546	103	8	**547**	**103**	547	103	546	103
483.xalancbmk	8	294	188	299	185	**297**	**186**	8	294	188	299	185	**297**	**186**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on localhost.localdomain Mon Jun 11 23:05:04 2012

    This section contains SUT (System Under Test) info as seen by
    some common utilities. To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E3-1280 V2 @ 3.60GHz
          1 "physical id"s (chips)
          8 "processors"
       cores, siblings (Caution: counting these is hw and system dependent. The
       following excerpts from /proc/cpuinfo might not be reliable. Use with
       caution.)
          cpu cores : 4
          siblings : 8

Continued on next page

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Platform Notes (Continued)

```
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       16306112 kB
   HugePages_Total:        0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
   15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 11 23:04 last=5

SPEC is set to: /usr/cpu2006
   Filesystem     Type     Size  Used Avail Use% Mounted on
   /dev/mapper/VolGroup-lv_root
                  ext4      50G   30G   18G  63% /

Additional information from dmidecode:
  Memory:
   2x Hynix/Hyundai HMT41GU7MFR8A-H9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

    401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias

      403.gcc: -xAVX -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias

    456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap

     473.astar: basepeak = yes

 483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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