



SPEC® CINT2006 Result

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Fujitsu

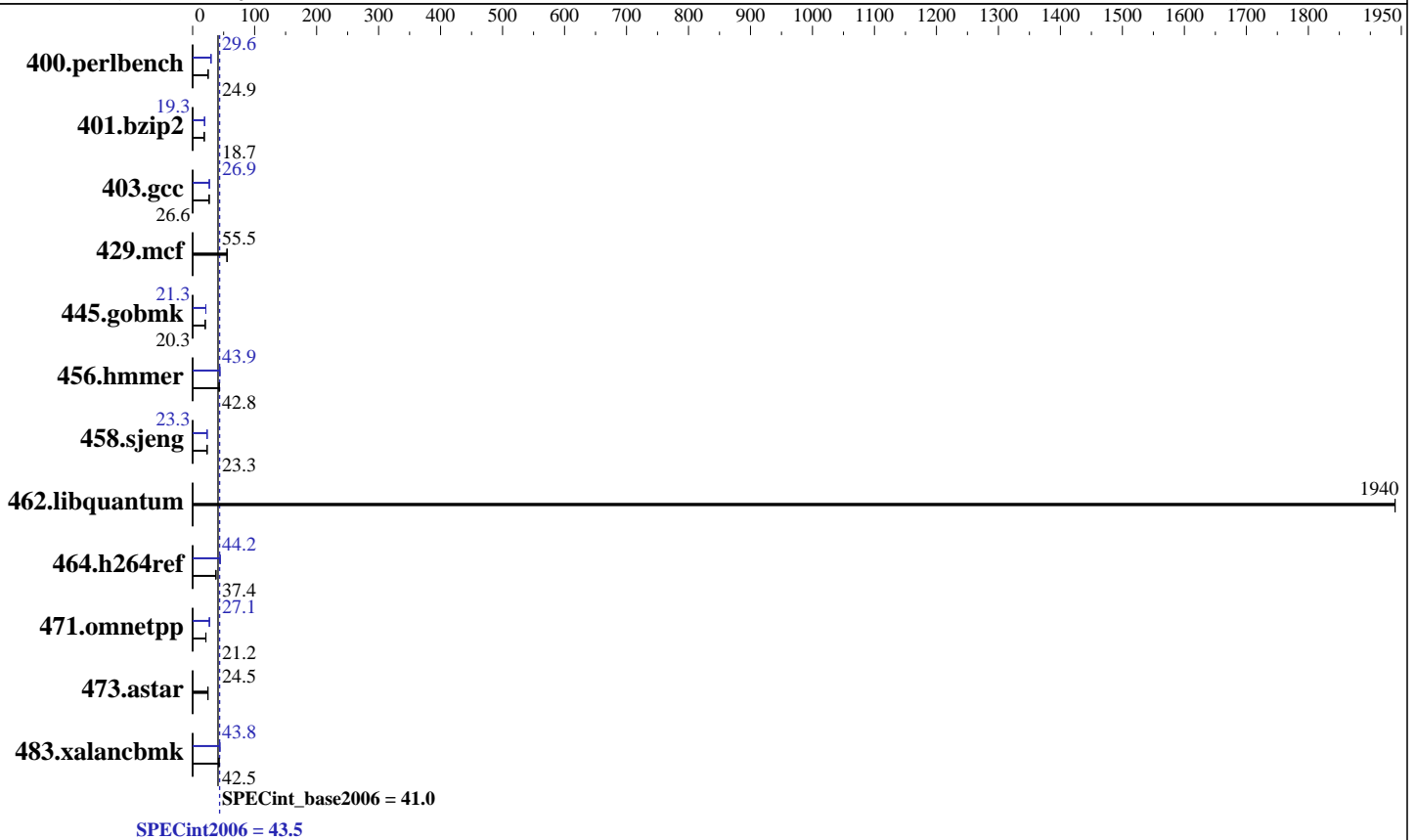
SPECint®2006 = 43.5

PRIMERGY CX250 S1, Intel Xeon E5-2630, 2.30 GHz

SPECint_base2006 = 41.0

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2630
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>392</u>	<u>24.9</u>	392	24.9	391	25.0	329	29.7	331	29.6	<u>330</u>	<u>29.6</u>
401.bzip2	<u>516</u>	<u>18.7</u>	517	18.7	516	18.7	500	19.3	501	19.3	<u>501</u>	<u>19.3</u>
403.gcc	302	26.6	<u>303</u>	<u>26.6</u>	303	26.6	299	26.9	<u>299</u>	<u>26.9</u>	299	26.9
429.mcf	165	55.3	<u>164</u>	<u>55.5</u>	164	55.7	165	55.3	<u>164</u>	<u>55.5</u>	164	55.7
445.gobmk	<u>517</u>	<u>20.3</u>	517	20.3	516	20.3	493	21.3	493	21.3	<u>493</u>	<u>21.3</u>
456.hammer	218	42.8	218	42.7	<u>218</u>	<u>42.8</u>	213	43.9	<u>213</u>	<u>43.9</u>	213	43.8
458.sjeng	<u>520</u>	<u>23.3</u>	520	23.3	519	23.3	519	23.3	<u>519</u>	<u>23.3</u>	519	23.3
462.libquantum	<u>10.7</u>	<u>1940</u>	10.7	1940	10.7	1940	<u>10.7</u>	<u>1940</u>	10.7	1940	10.7	1940
464.h264ref	587	37.7	593	37.3	<u>591</u>	<u>37.4</u>	502	44.1	<u>501</u>	<u>44.2</u>	498	44.4
471.omnetpp	295	21.2	<u>294</u>	<u>21.2</u>	290	21.5	<u>231</u>	<u>27.1</u>	230	27.1	232	26.9
473.astar	282	24.9	<u>286</u>	<u>24.5</u>	287	24.4	282	24.9	<u>286</u>	<u>24.5</u>	287	24.4
483.xalancbmk	162	42.7	163	42.3	<u>162</u>	<u>42.5</u>	157	43.8	157	43.9	<u>157</u>	<u>43.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Platform Notes

BIOS configuration:
Intel HT Technology = Disable

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
This result was measured on the PRIMERGY CX250 S1. The PRIMERGY CX250 S1 and the PRIMERGY CX270 S1 are electronically equivalent.
For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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