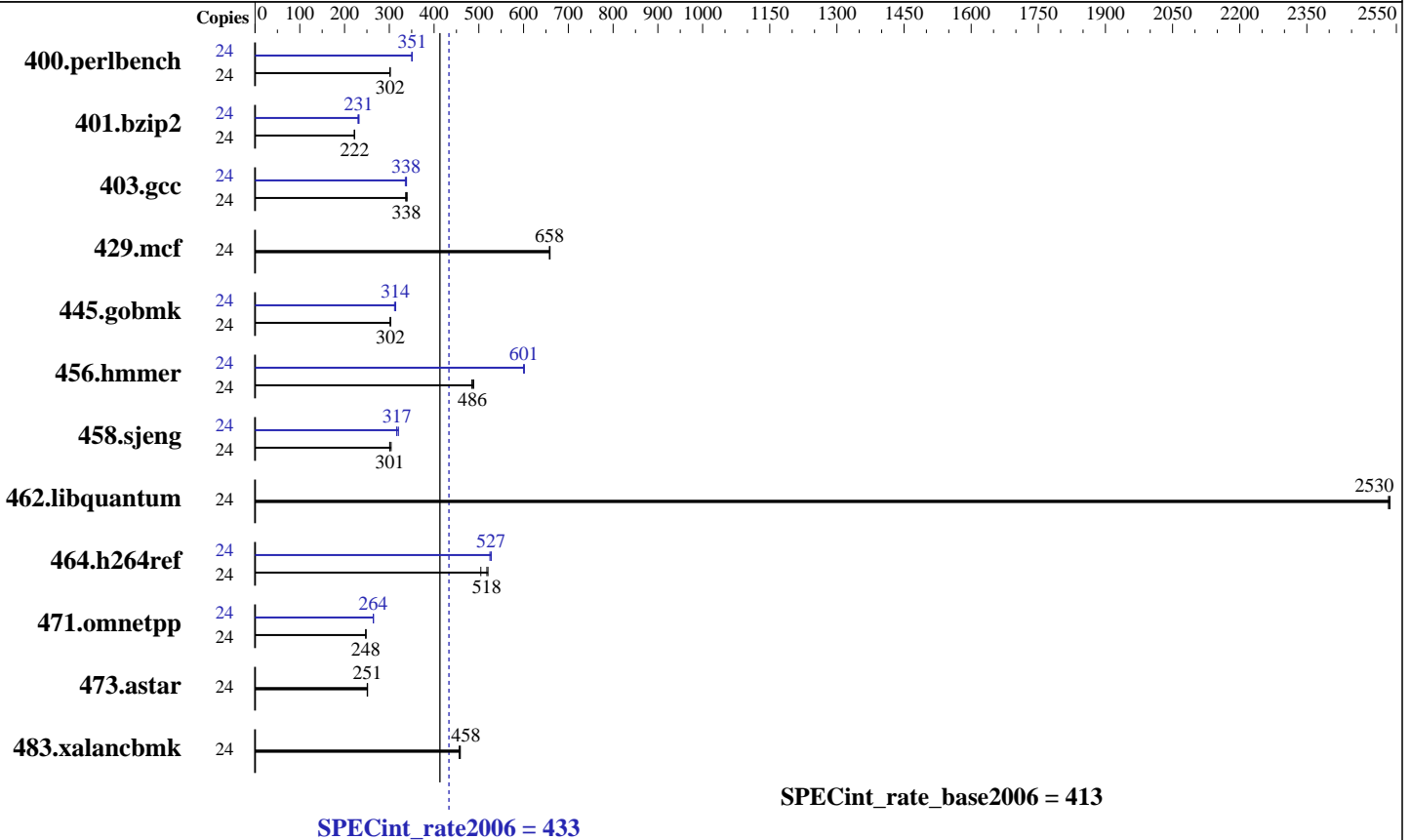# SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated
Acer AT350 F2 (Xeon E5-2630)

SPECint®_rate2006 =   433

SPECint_rate_base2006 = 413

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Benchmark	Copies	Result (peak)	Result (base)
400.perlbench	24 / 24	351	302
401.bzip2	24 / 24	231	222
403.gcc	24 / 24	338	338
429.mcf	24	658	
445.gobmk	24 / 24	314	302
456.hmmer	24 / 24	601	486
458.sjeng	24 / 24	317	301
462.libquantum	24	2530	
464.h264ref	24 / 24	527	518
471.omnetpp	24 / 24	264	248
473.astar	24	251	
483.xalancbmk	24	458	

SPECint_rate_base2006 = 413

SPECint_rate2006 = 433

Hardware
CPU Name: Intel Xeon E5-2630

CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz

CPU MHz: 2300

FPU: Integrated

CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 15 MB I+D on chip per chip

Other Cache: None

Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-10600R-9, ECC)

Disk Subsystem: 1 x 2 TB SAS, 7200 RPM

Other Hardware: None

Software
Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64

Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux

Auto Parallel: No

File System: ext4

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer AT350 F2 (Xeon E5-2630)

SPECint_rate2006 = 433

SPECint_rate_base2006 = 413

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	776	302	**_777_**	**_302_**	779	301	24	**_668_**	**_351_**	670	350	668	351
401.bzip2	24	**_1042_**	**_222_**	1044	222	1041	222	24	997	232	1007	230	**_1001_**	**_231_**
403.gcc	24	**_572_**	**_338_**	568	340	574	337	24	575	336	571	338	**_572_**	**_338_**
429.mcf	24	332	659	**_333_**	**_658_**	333	658	24	332	659	**_333_**	**_658_**	333	658
445.gobmk	24	**_833_**	**_302_**	833	302	832	303	24	801	314	**_803_**	**_314_**	808	312
456.hmmer	24	**_461_**	**_486_**	458	489	462	484	24	373	600	**_373_**	**_601_**	372	602
458.sjeng	24	**_964_**	**_301_**	955	304	965	301	24	917	317	906	320	**_916_**	**_317_**
462.libquantum	24	**_196_**	**_2530_**	196	2540	196	2530	24	**_196_**	**_2530_**	196	2540	196	2530
464.h264ref	24	1053	504	**_1025_**	**_518_**	1020	520	24	**_1008_**	**_527_**	1006	528	1012	525
471.omnetpp	24	**_606_**	**_248_**	606	248	606	248	24	**_568_**	**_264_**	567	265	568	264
473.astar	24	670	251	672	251	**_671_**	**_251_**	24	670	251	672	251	**_671_**	**_251_**
483.xalancbmk	24	361	459	363	456	**_362_**	**_458_**	24	361	459	363	456	**_362_**	**_458_**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sun May 24 14:44:27 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
      2 "physical id"s (chips)
     24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
```

Acer Incorporated

Acer AT350 F2 (Xeon E5-2630)

SPECint_rate2006 =   433

SPECint_rate_base2006 = 413

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Platform Notes (Continued)

```
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

 From /proc/meminfo
    MemTotal:       132271000 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.2 (Santiago)

 From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
    Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
    EST 2011 x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 May 23 18:05 last=5

 SPEC is set to: /usr/cpu2006
    Filesystem     Type    Size  Used Avail Use% Mounted on
    /dev/mapper/VolGroup-lv_root
                   ext4    1.2T   51G  1.1T   5% /

 Additional information from dmidecode:
   Memory:
    8x Hynix Semiconductor HMT31GR7BFR4A-H9 8 GB 1333 MHz 1 rank
    8x Hynix Semiconductor HMT31GR7CFR4A-H9 8 GB 1333 MHz 1 rank

 (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>         /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Acer Incorporated

Acer AT350 F2 (Xeon E5-2630)

SPECint_rate2006 = 433

SPECint_rate_base2006 = 413

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer AT350 F2 (Xeon E5-2630)

SPECint_rate2006 = 433

SPECint_rate_base2006 = 413

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

    401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias

      403.gcc: -xAVX -ipo -O3 -no-prec-div

     429.mcf: basepeak = yes

   445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
              -ansi-alias -opt-mem-layout-trans=3

   456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/smartheap -lsmartheap

    473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer AT350 F2 (Xeon E5-2630)

SPECint_rate2006 =   433

SPECint_rate_base2006 = 413

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 10:34:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 July 2012.