SPEC® CINT2006 Result

Supermicro

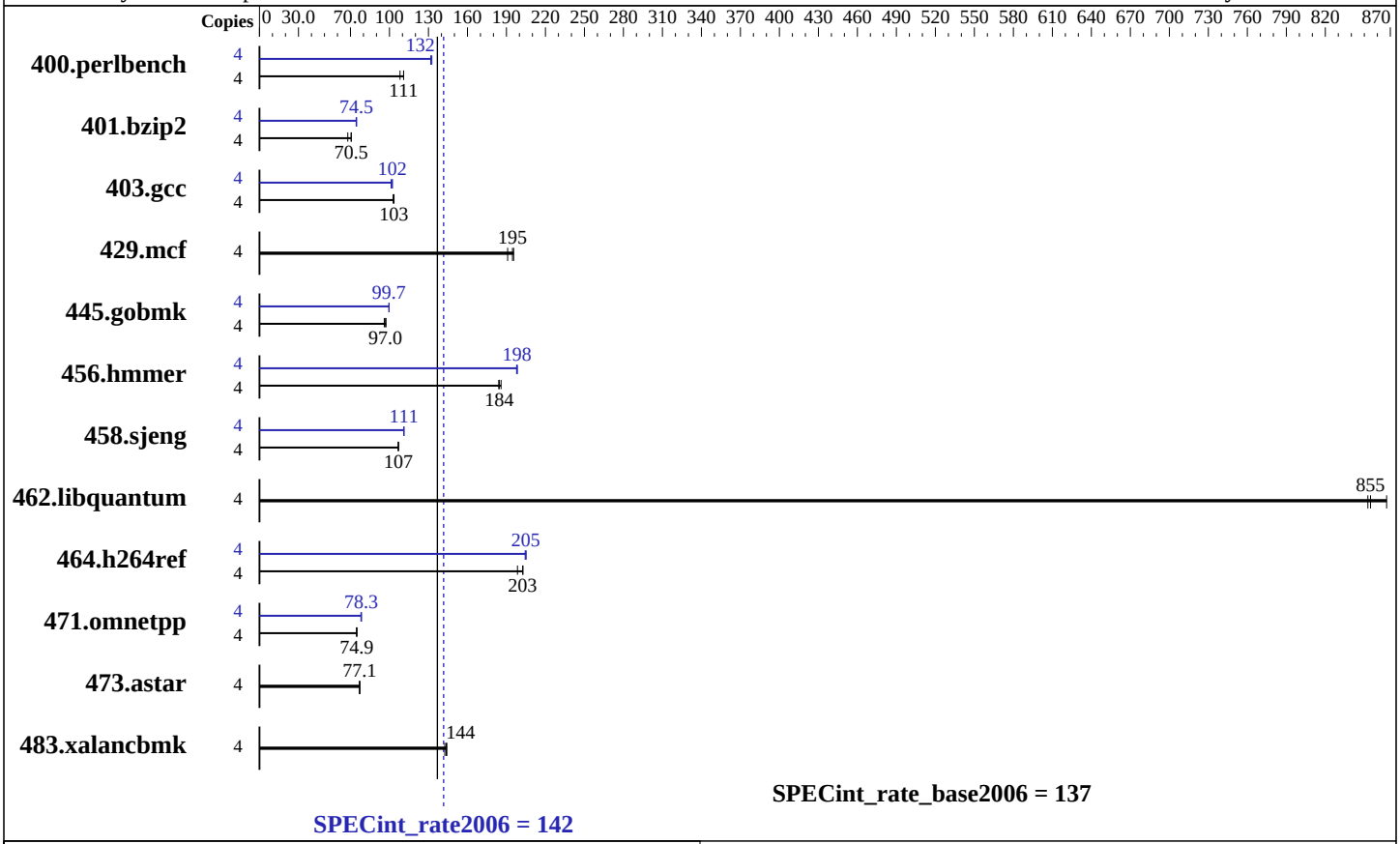
Supermicro C7Q67 motherboard (Intel Core i5-2500K)

SPECint®_rate2006 = 142

SPECint_rate_base2006 = 137

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Core i5-2500K
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (4 x 2 GB 2Rx8 PC3-12800U-11)
Disk Subsystem:	1 x 600 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	352	111	361	108	**352**	**111**	4	297	132	295	133	**296**	**132**
401.bzip2	4	**547**	**70.5**	568	67.9	547	70.6	4	519	74.4	516	74.8	**518**	**74.5**
403.gcc	4	311	104	313	103	**311**	**103**	4	318	101	315	102	**315**	**102**
429.mcf	4	**187**	**195**	187	196	191	191	4	**187**	**195**	187	196	191	191
445.gobmk	4	**433**	**97.0**	437	96.0	431	97.3	4	421	99.6	**421**	**99.7**	421	99.7
456.hmmer	4	**202**	**184**	203	184	201	186	4	189	198	**188**	**198**	188	198
458.sjeng	4	453	107	**453**	**107**	453	107	4	**436**	**111**	435	111	436	111
462.libquantum	4	**97.0**	**855**	97.2	853	95.6	867	4	**97.0**	**855**	97.2	853	95.6	867
464.h264ref	4	446	198	437	203	**437**	**203**	4	431	205	433	204	**432**	**205**
471.omnetpp	4	335	74.7	334	74.9	**334**	**74.9**	4	320	78.1	**319**	**78.3**	319	78.5
473.astar	4	**364**	**77.1**	364	77.2	365	76.9	4	**364**	**77.1**	364	77.2	365	76.9
483.xalancbmk	4	**192**	**144**	191	144	194	143	4	**192**	**144**	191	144	194	143

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
As tested, the system used a Supermicro CSE-731i-300B chassis.
The chassis is configured with a PWS-303-PQ power supply, 1 SNK-P0046A4 heatsink,
as well as 1 FAN-0108L4 rear cooling fan and 1 FAN-0113L4 front intake fan.
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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