SPEC® CFP2006 Result

Fujitsu

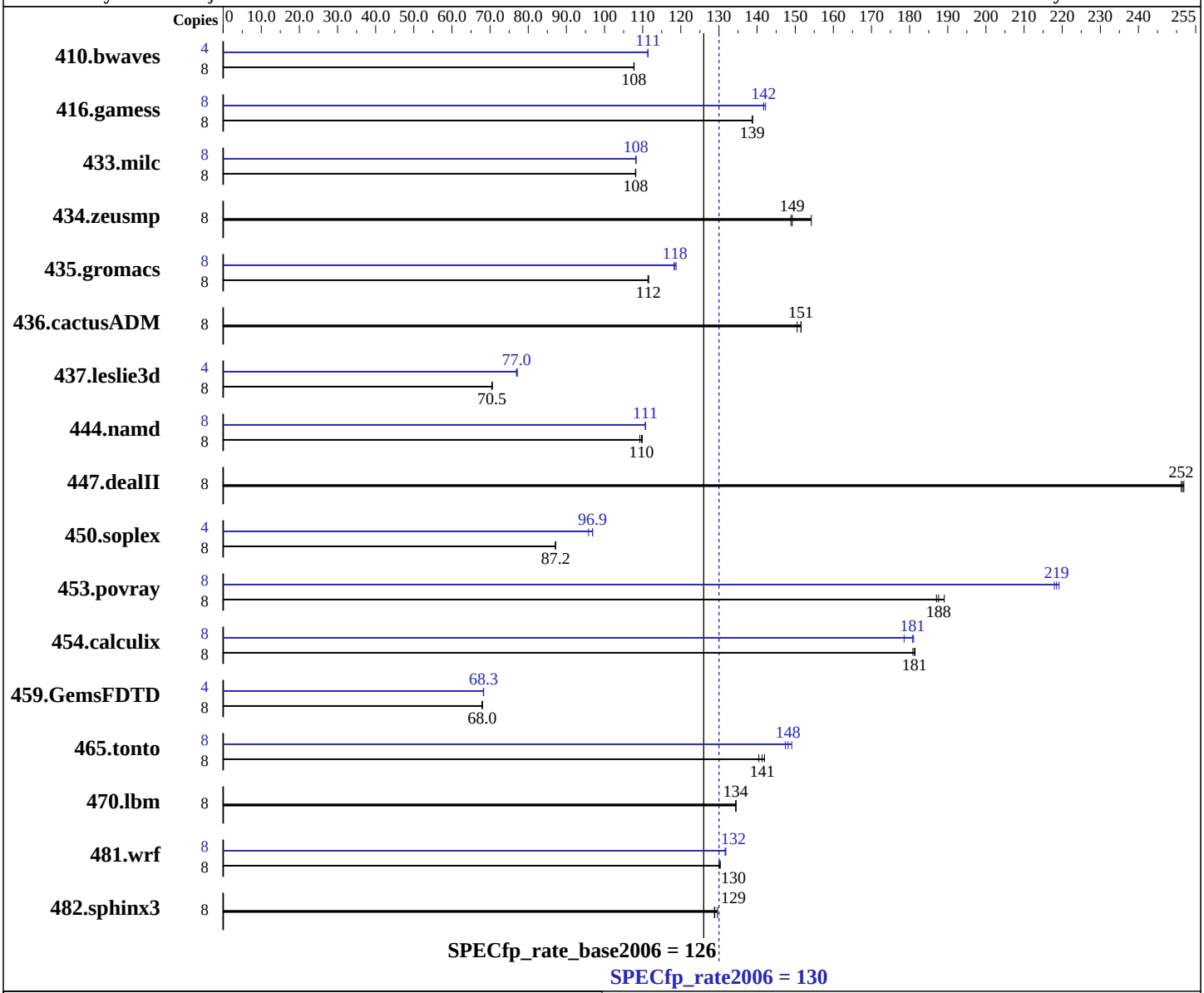
PRIMERGY TX120 S3p, Intel Xeon E3-1265L v2, 2.50 GHz

SPECfp®_rate2006 = 130

SPECfp_rate_base2006 = 126

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E3-1265L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.5 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.293 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

Continued on next page

SPEC CFP2006 Result

Fujitsu

PRIMERGY TX120 S3p, Intel Xeon E3-1265L v2, 2.50 GHz

SPECfp_rate2006 = 130

SPECfp_rate_base2006 = 126

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Feb-2012

L3 Cache:	8 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**1009**	**108**	1009	108	1010	108	4	488	111	488	111	**488**	**111**
416.gamess	8	**1129**	**139**	1129	139	1129	139	8	1101	142	**1105**	**142**	1105	142
433.milc	8	**679**	**108**	679	108	679	108	8	**679**	**108**	678	108	679	108
434.zeusmp	8	**488**	**149**	489	149	472	154	8	**488**	**149**	489	149	472	154
435.gromacs	8	512	112	**512**	**112**	513	111	8	481	119	**482**	**118**	483	118
436.cactusADM	8	**631**	**151**	635	151	631	152	8	**631**	**151**	635	151	631	152
437.leslie3d	8	1065	70.6	1067	70.5	**1067**	**70.5**	4	**488**	**77.0**	487	77.2	489	76.9
444.namd	8	**585**	**110**	587	109	584	110	8	580	111	**579**	**111**	579	111
447.dealII	8	**364**	**252**	364	251	363	252	8	**364**	**252**	364	251	363	252
450.soplex	8	765	87.2	766	87.1	**765**	**87.2**	4	348	95.8	**344**	**96.9**	344	96.9
453.povray	8	225	189	228	187	**227**	**188**	8	195	218	194	219	**195**	**219**
454.calculix	8	**364**	**181**	364	181	365	181	8	**365**	**181**	365	181	370	179
459.GemsFDTD	8	1251	67.9	**1249**	**68.0**	1248	68.0	4	621	68.3	**621**	**68.3**	622	68.2
465.tonto	8	555	142	560	140	**557**	**141**	8	**531**	**148**	534	147	528	149
470.lbm	8	817	134	818	134	**818**	**134**	8	817	134	818	134	**818**	**134**
481.wrf	8	687	130	**687**	**130**	685	130	8	678	132	**678**	**132**	679	132
482.sphinx3	8	1211	129	**1210**	**129**	1203	130	8	1211	129	**1210**	**129**	1203	130

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
```

Continued on next page

General Notes (Continued)

```
Binaries compiled on a system with 2x E5-2650 CPU + 96 GB
memory using RHEL6.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
This result was measured on the PRIMERGY TX140 S1p. The PRIMERGY TX140 S1p
and the PRIMERGY TX120 S3p are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

436.cactusADM: `basepeak = yes`

454.calculix: `-xAVX -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-mem-layout-trans=3`

481.wrf: `Same as 454.calculix`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 05:22:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 June 2012.