



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

PRIMERGY TX120 S3p, Intel Xeon E3-1265L v2, 2.50 GHz

SPECint<sup>®</sup>2006 = 51.4

SPECint\_base2006 = 48.0

CPU2006 license: 19

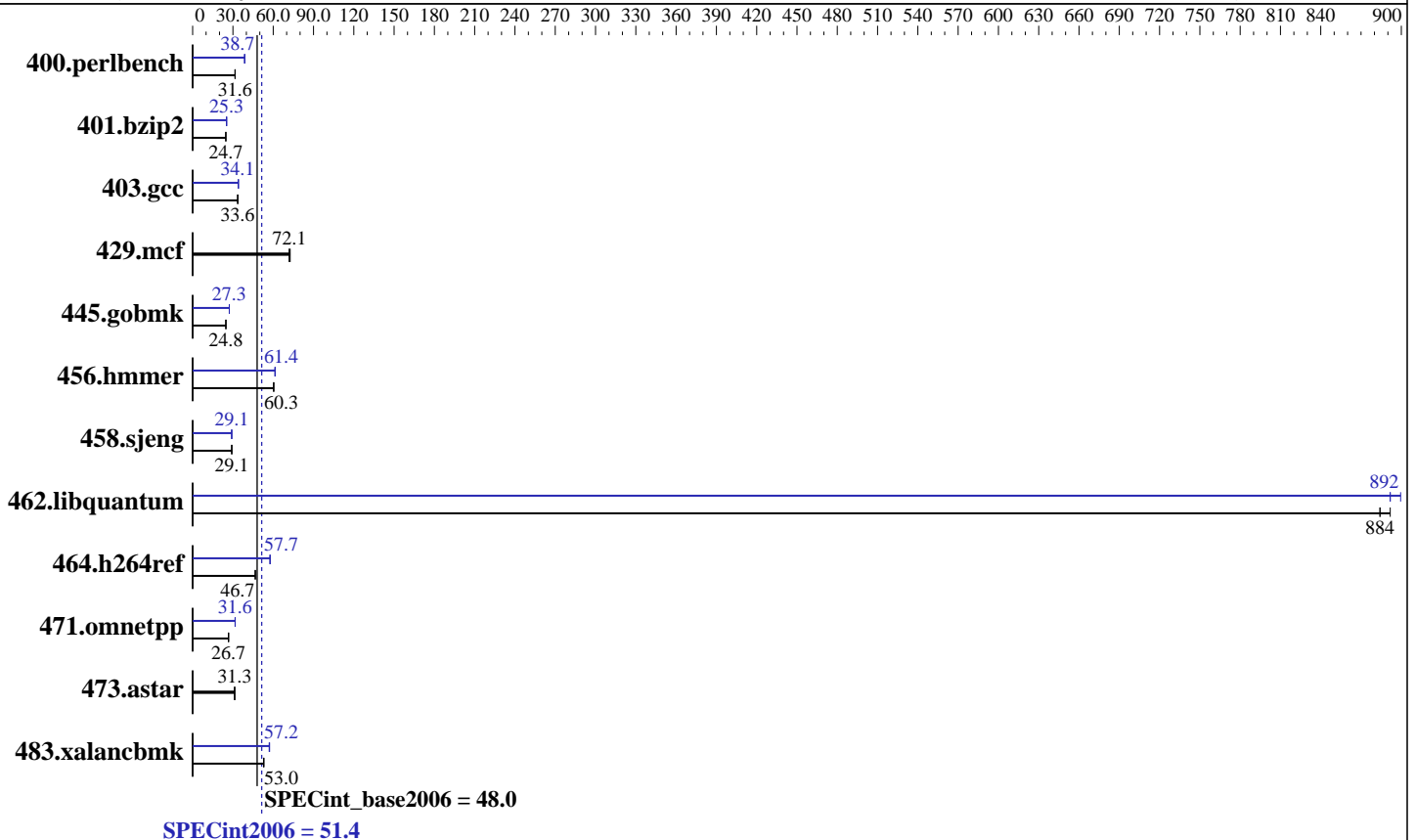
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E3-1265L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.5 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	310	31.6	<b><u>309</u></b>	<b><u>31.6</u></b>	309	31.6	<b><u>252</u></b>	<b><u>38.7</u></b>	252	38.8	252	38.7
401.bzip2	<b><u>390</u></b>	<b><u>24.7</u></b>	390	24.7	391	24.7	382	25.3	<b><u>382</u></b>	<b><u>25.3</u></b>	382	25.3
403.gcc	240	33.6	<b><u>240</u></b>	<b><u>33.6</u></b>	240	33.5	236	34.1	237	34.0	<b><u>236</u></b>	<b><u>34.1</u></b>
429.mcf	127	71.9	<b><u>126</u></b>	<b><u>72.1</u></b>	126	72.6	127	71.9	<b><u>126</u></b>	<b><u>72.1</u></b>	126	72.6
445.gobmk	<b><u>424</u></b>	<b><u>24.8</u></b>	423	24.8	424	24.7	<b><u>384</u></b>	<b><u>27.3</u></b>	384	27.3	385	27.3
456.hammer	<b><u>155</u></b>	<b><u>60.3</u></b>	154	60.4	155	60.3	152	61.5	152	61.3	<b><u>152</u></b>	<b><u>61.4</u></b>
458.sjeng	416	29.1	<b><u>415</u></b>	<b><u>29.1</u></b>	415	29.2	416	29.1	416	29.1	<b><u>416</u></b>	<b><u>29.1</u></b>
462.libquantum	23.2	892	23.4	884	<b><u>23.4</u></b>	<b><u>884</u></b>	<b><u>23.2</u></b>	<b><u>892</u></b>	23.2	892	23.0	899
464.h264ref	<b><u>474</u></b>	<b><u>46.7</u></b>	475	46.6	474	46.7	384	57.6	<b><u>384</u></b>	<b><u>57.7</u></b>	383	57.7
471.omnetpp	<b><u>234</u></b>	<b><u>26.7</u></b>	234	26.7	233	26.8	197	31.7	<b><u>198</u></b>	<b><u>31.6</u></b>	198	31.6
473.astar	225	31.2	<b><u>225</u></b>	<b><u>31.3</u></b>	224	31.4	225	31.2	<b><u>225</u></b>	<b><u>31.3</u></b>	224	31.4
483.xalancbmk	131	52.8	130	53.0	<b><u>130</u></b>	<b><u>53.0</u></b>	121	57.0	<b><u>121</u></b>	<b><u>57.2</u></b>	120	57.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"  
OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x E3-1270V2 CPU + 32 GB memory using RHEL6.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

This result was measured on the PRIMERGY TX140 S1p. The PRIMERGY TX140 S1p and the PRIMERGY TX120 S3p are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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**Tested by:** Fujitsu

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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