SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint®_rate2006 = 295

SPECint_rate_base2006 = 282
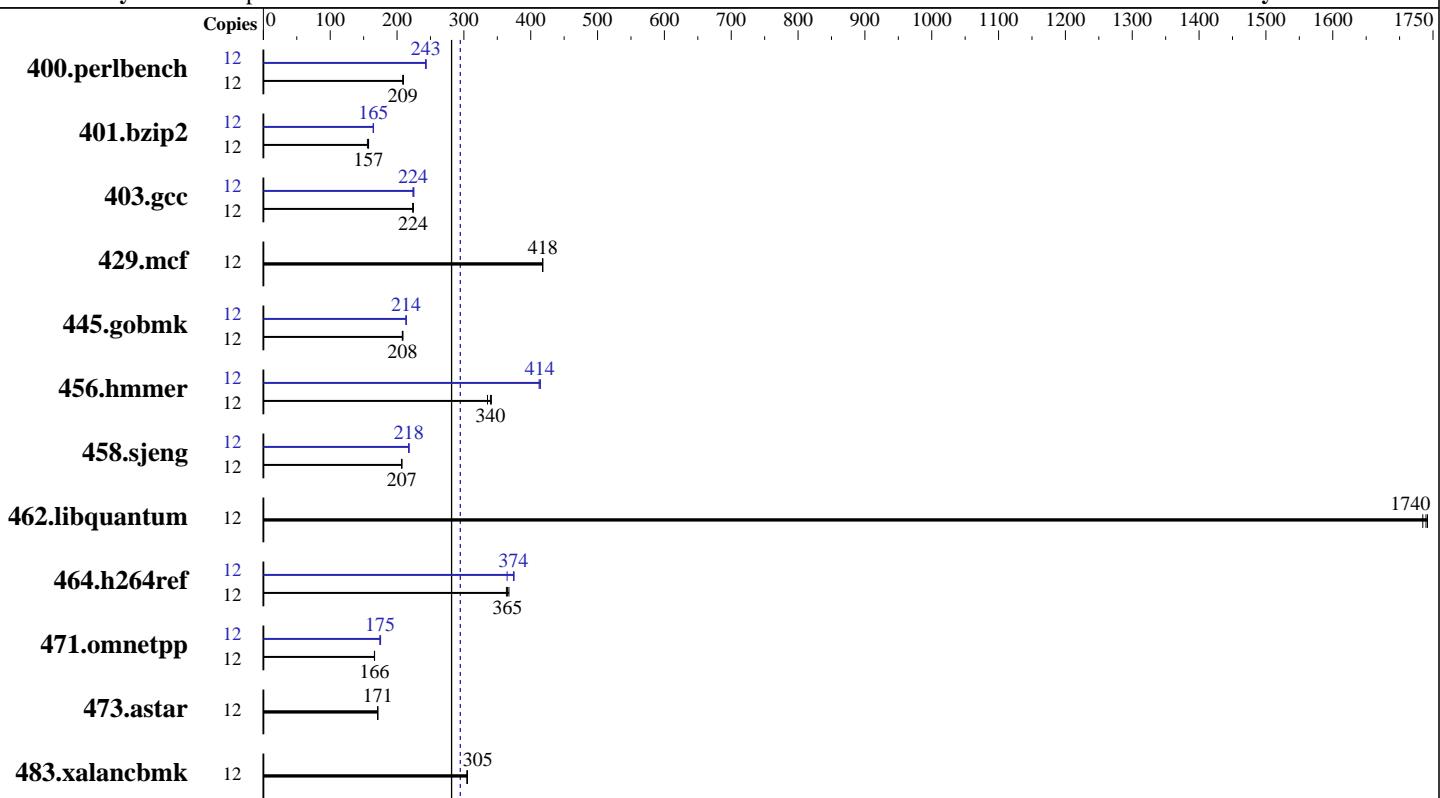
CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

SPECint_rate2006 = 295

SPECint_rate_base2006 = 282

Hardware

CPU Name:	Intel Xeon E5-1660
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 300 GB SATA II, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint_rate2006 = 295

SPECint_rate_base2006 = 282

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	**561**	**209**	562	209	559	210	12	481	244	483	243	**483**	**243**
401.bzip2	12	736	157	**736**	**157**	744	156	12	705	164	**704**	**165**	703	165
403.gcc	12	432	223	431	224	**431**	**224**	12	431	224	**431**	**224**	429	225
429.mcf	12	**262**	**418**	262	418	262	418	12	**262**	**418**	262	418	262	418
445.gobmk	12	606	208	**604**	**208**	603	209	12	**589**	**214**	589	214	589	214
456.hmmer	12	334	335	**330**	**340**	328	341	12	270	414	271	413	**271**	**414**
458.sjeng	12	701	207	**701**	**207**	702	207	12	668	217	**667**	**218**	666	218
462.libquantum	12	**143**	**1740**	143	1740	143	1730	12	**143**	**1740**	143	1740	143	1730
464.h264ref	12	**727**	**365**	723	367	730	364	12	728	365	708	375	**710**	**374**
471.omnetpp	12	**451**	**166**	451	166	451	166	12	428	175	**429**	**175**	430	174
473.astar	12	492	171	**493**	**171**	493	171	12	492	171	**493**	**171**	493	171
483.xalancbmk	12	**271**	**305**	271	305	272	305	12	**271**	**305**	271	305	272	305

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RHEL5.5
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

Supermicro

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint_rate2006 =   295

SPECint_rate_base2006 = 282

CPU2006 license: 001176		**Test date:**	May-2012
Test sponsor: Supermicro		**Hardware Availability:**	Mar-2012
Tested by: Supermicro		**Software Availability:**	Dec-2011

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

```
    400.perlbench: icc -m64
```

```
     401.bzip2: icc -m64
```

```
    456.hmmer: icc -m64
```

```
    458.sjeng: icc -m64
```

C++ benchmarks:
```
    icpc -m32
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
```

Supermicro

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint_rate2006 =   295

SPECint_rate_base2006 = 282

CPU2006 license: 001176		**Test date:**	May-2012
Test sponsor: Supermicro		**Hardware Availability:**	Mar-2012
Tested by: Supermicro		**Software Availability:**	Dec-2011

Peak Portability Flags (Continued)

        462.libquantum: `-DSPEC_CPU_LINUX`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

        401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch`
                `-auto-ilp32 -ansi-alias`

          403.gcc: `-xAVX -ipo -O3 -no-prec-div`

          429.mcf: `basepeak = yes`

        445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                `-ansi-alias -opt-mem-layout-trans=3`

        456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

        458.sjeng: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -prof-use(pass 2) -unroll4`
                `-auto-ilp32`

    462.libquantum: `basepeak = yes`

      464.h264ref: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -prof-use(pass 2) -unroll2`
                `-ansi-alias`

C++ benchmarks:

      471.omnetpp: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias`
                `-opt-ra-region-strategy=block -Wl,-z,muldefs`
                `-L/smartheap -lsmartheap`

        473.astar: `basepeak = yes`

    483.xalancbmk: `basepeak = yes`

Supermicro

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint_rate2006 = 295

SPECint_rate_base2006 = 282

CPU2006 license: 001176	**Test date:** May-2012
Test sponsor: Supermicro	**Hardware Availability:** Mar-2012
Tested by: Supermicro	**Software Availability:** Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 04:26:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 June 2012.