



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 66.0

SuperServer 1017C-TF (X9SCL-F, Intel G840)

SPECint_rate_base2006 = 63.3

CPU2006 license: 001176

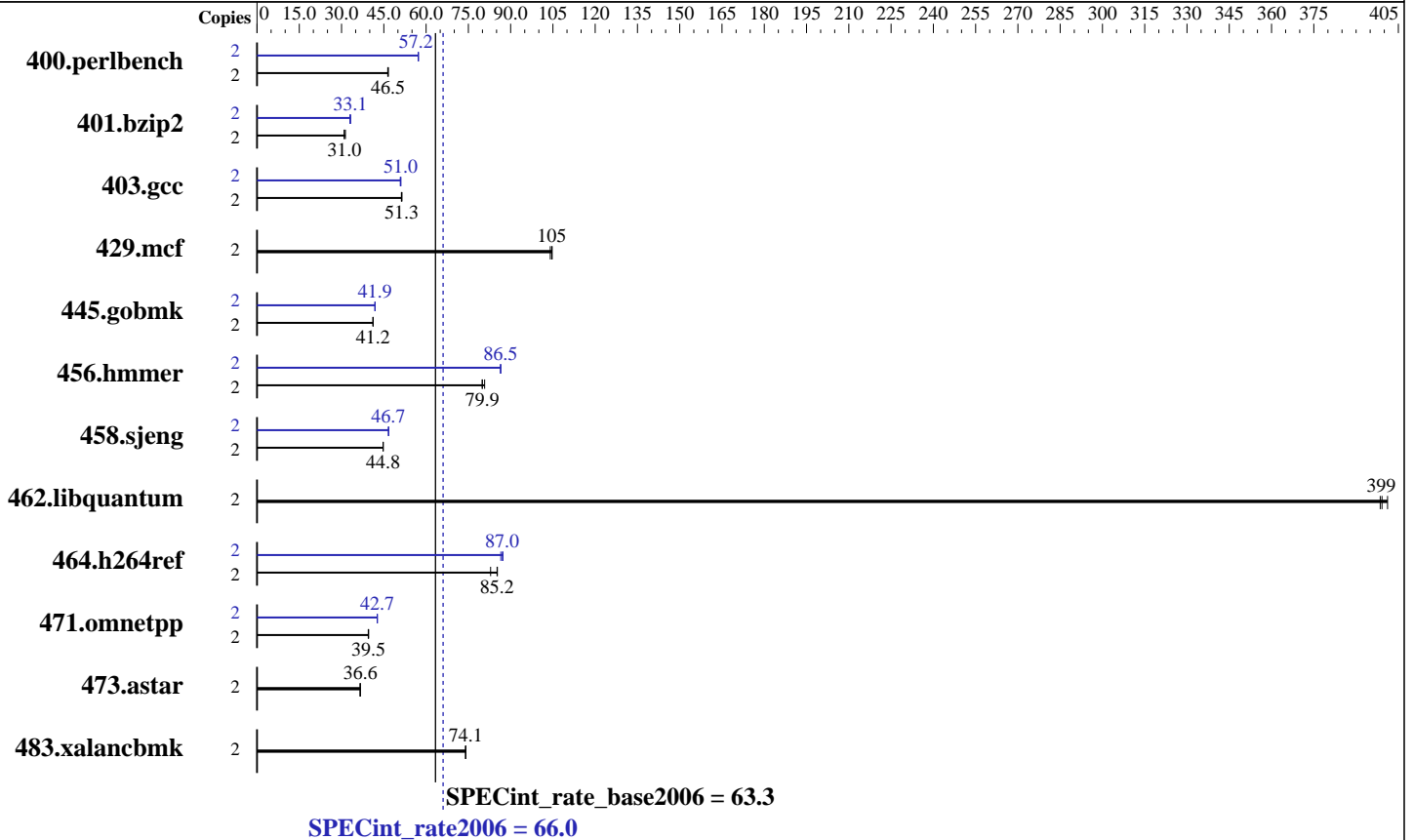
Test date: Apr-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Pentium G840
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | 420 | 46.6 | 420 | 46.5 | 421 | 46.4 | 2 | 341 | 57.3 | 341 | 57.2 | 342 | 57.2 |
| 401.bzip2 | 2 | 615 | 31.4 | 624 | 30.9 | 623 | 31.0 | 2 | 581 | 33.2 | 583 | 33.1 | 585 | 33.0 |
| 403.gcc | 2 | 314 | 51.2 | 313 | 51.4 | 314 | 51.3 | 2 | 316 | 51.0 | 315 | 51.0 | 317 | 50.8 |
| 429.mcf | 2 | 174 | 105 | 174 | 105 | 175 | 104 | 2 | 174 | 105 | 174 | 105 | 175 | 104 |
| 445.gobmk | 2 | 510 | 41.2 | 509 | 41.2 | 510 | 41.2 | 2 | 500 | 41.9 | 502 | 41.8 | 501 | 41.9 |
| 456.hammer | 2 | 233 | 79.9 | 233 | 79.9 | 231 | 80.8 | 2 | 216 | 86.4 | 216 | 86.5 | 216 | 86.5 |
| 458.sjeng | 2 | 540 | 44.8 | 541 | 44.7 | 541 | 44.8 | 2 | 518 | 46.7 | 519 | 46.7 | 519 | 46.6 |
| 462.libquantum | 2 | 104 | 399 | 104 | 399 | 103 | 401 | 2 | 104 | 399 | 104 | 399 | 103 | 401 |
| 464.h264ref | 2 | 519 | 85.3 | 520 | 85.2 | 534 | 82.9 | 2 | 508 | 87.0 | 507 | 87.3 | 512 | 86.5 |
| 471.omnetpp | 2 | 316 | 39.6 | 316 | 39.5 | 317 | 39.5 | 2 | 293 | 42.7 | 293 | 42.7 | 293 | 42.7 |
| 473.astar | 2 | 383 | 36.7 | 383 | 36.6 | 384 | 36.6 | 2 | 383 | 36.7 | 383 | 36.6 | 384 | 36.6 |
| 483.xalancbmk | 2 | 187 | 74.0 | 186 | 74.1 | 186 | 74.1 | 2 | 187 | 74.0 | 186 | 74.1 | 186 | 74.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/usr/cpu2006/libs/32:/home/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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